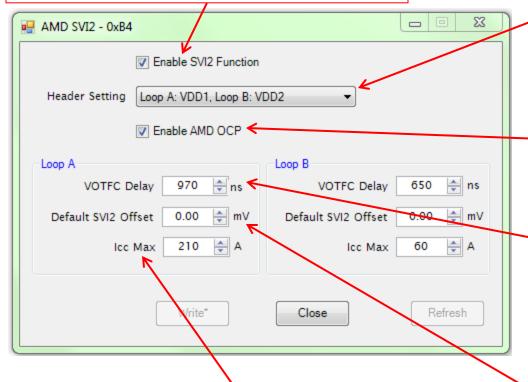# AMD SVI2...

This window contains the few settings relevant to SVI2 operation to meet AMD requirements

Enable SVI2 Function
Must be marked when using AMD processor to use SVI2 mode.
If not marked it select PWM VID mode

Header Setting
Controls wich of loop A and B are assigned to VDD1 and VDD2.

Enable AMD OCP
When marked it Activate the special overcurrent delay of 10us that AMD require for Instant OCP fault

VOTFC Delay:
Voltage On The Fly Complete signal.
Can be set to delay the VOFTC signal after Vout ramp completed. Example: in case of a small overshoot the delay allow Vout to stabalise

Icc Max is the setting for full scale current for the reporting via the SVI2 bus. Set it to the maximum current expected.

Default SVI2 Offset
Compensation for any offset from the requested SVI2 voltage level to what Vout really is.
On a design where the Vout do not match the SVI2 voltage this offset can be adjusted.
Reason for offset can as example be layout of the PCB causes some small voltage drop.

AMD SVI2 - 0xB4

☑ Enable SVI2 Function

Header Setting Loop A: VDD1, Loop B: VDD2

☑ Enable AMD OCP

Loop A
VOTFC Delay 970 ns
Default SVI2 Offset 0.00 mV
Icc Max 210 A

Loop B
VOTFC Delay 650 ns
Default SVI2 Offset 0.00 mV
Icc Max 60 A

Write*   Close   Refresh