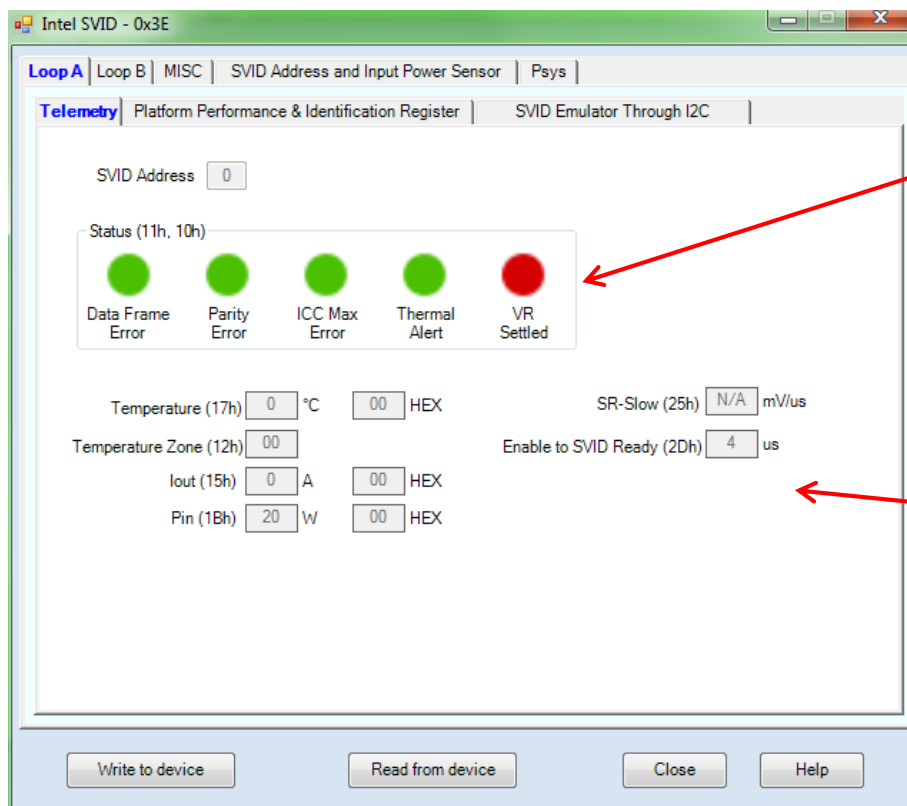


SVID... Telemetry

The Intel SVID window is only available for devices that support SVID



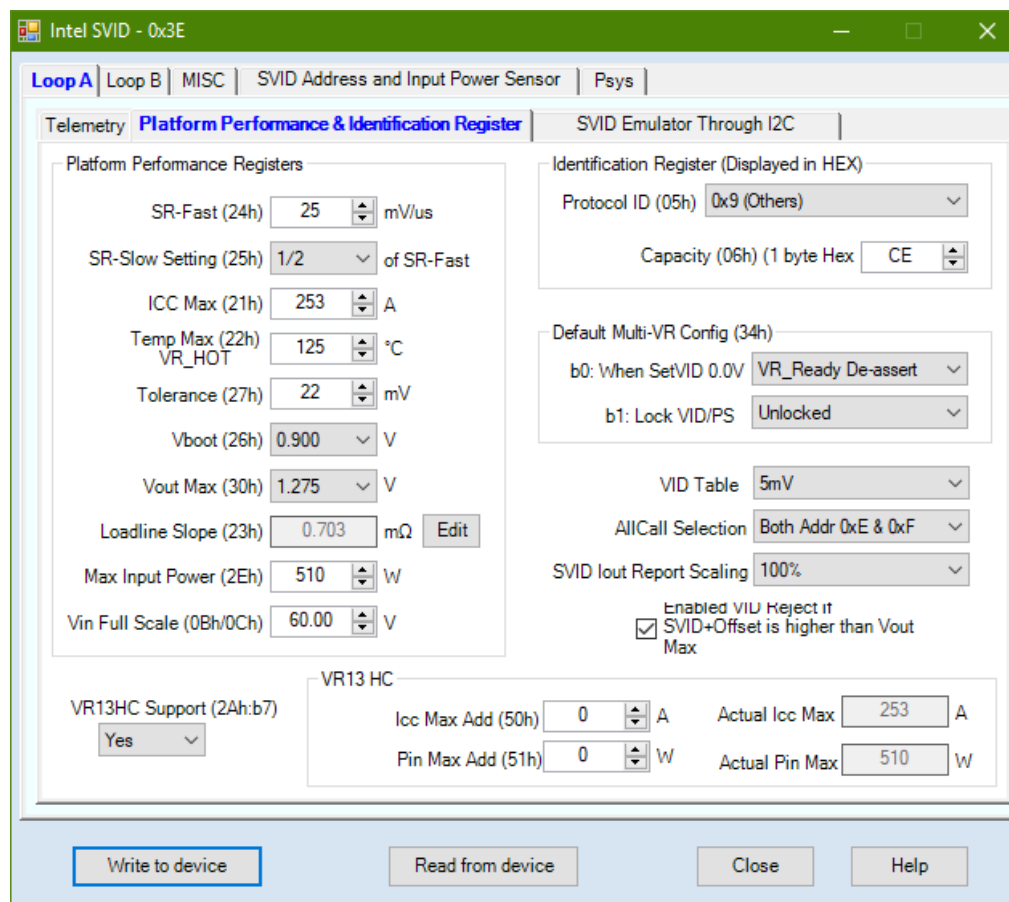
Readout of the status bits in SVID registers.

Readout of parameters in SVID registers.

SVID... Platform Performance

Allow settings of all the registers that SVID use.

Depending on settings some parameters may not be visible if not used i.e. VR13HC settings only visible when VR13HC is activated.



The screenshot shows the 'Intel SVID - 0x3E' application window. The 'Platform Performance & Identification Register' tab is active. It contains several sections for configuring SVID parameters:

- Platform Performance Registers:**
 - SR-Fast (24h): 25 mV/us
 - SR-Slow Setting (25h): 1/2 of SR-Fast
 - ICC Max (21h): 253 A
 - Temp Max (22h) VR_HOT: 125 °C
 - Tolerance (27h): 22 mV
 - Vboot (26h): 0.900 V
 - Vout Max (30h): 1.275 V
 - Loadline Slope (23h): 0.703 mΩ (with an 'Edit' button)
 - Max Input Power (2Eh): 510 W
 - Vin Full Scale (0Bh/0Ch): 60.00 V
- Identification Register (Displayed in HEX):**
 - Protocol ID (05h): 0x9 (Others)
 - Capacity (06h) (1 byte Hex): CE
- Default Multi-VR Config (34h):**
 - b0: When SetVID 0.0V: VR_Ready De-assert
 - b1: Lock VID/PS: Unlocked
- VID Table:** 5mV
- AllCall Selection:** Both Addr 0xE & 0xF
- SVID Iout Report Scaling:** 100%
- Enabled VID Reject if SVID+Offset is higher than Vout Max:** ☒
- VR13 HC:**
 - VR13HC Support (2Ah:b7): Yes
 - ICC Max Add (50h): 0 A, Actual ICC Max: 253 A
 - Pin Max Add (51h): 0 W, Actual Pin Max: 510 W

At the bottom, there are buttons for 'Write to device', 'Read from device', 'Close', and 'Help'.

SVID... Emulator through I2C

Allow a number of SVID settings to be emulated and sent via I2C to voltage regulator.

Loop B have less number of settings as it is already set in loop A

Mark the box to enable this I2C SVID emulator function

SVID... Address and Input Power Sensor

Selection of SVID address

SVID address can also be selected using external resistors. Table shows the different combinations of resistor and address

The screenshot shows the 'Intel SVID - 0x3E' application window. The 'SVID Address and Input Power Sensor' tab is selected. On the left, under 'SVID Address', there are dropdowns for 'Loop A Address Base' (0x0), 'Loop B Address Base' (0x1), and 'SVID Address Setting' (based address). On the right, under 'Input Power Sensor SVID Address', there is a dropdown set to 'Normal Rail with SVID Addr 0x0'. Below this is the 'Input Power Sensor Rail' section with a 'Platform Performance & Identification Register' containing fields for Max Input Power (2Eh), Max Input Power Add (51h), Actual Input Power, Protocol ID (05h), AICall Selection, and Capability (06h). At the bottom right is the 'SVID Emulator Through I2C' section with an 'Enable' checkbox checked, and fields for Power State (32h), Pin Alert (2Fh), Pin Alert Add (52h), and Actual Pin Alert. At the very bottom are buttons for 'Write to device', 'Read from device', 'Close', and 'Help'.

Selection of Input power sensor.

Depending on selections some of the menus may be grayed out as they are not selectable

Allow emulation of SVID via I2C commands

Power state and input power alert **Pin Alert** can be set from GUI when the enable box is marked

SVID... Psys

Intel SVID - 0x3E

Loop A | Loop B | MISC | SVID Address and Input Power Sensor | **Psys**

Psys Source: Vin x lin ✓

Input Power Signal Measurement

Gain: 0 Offset: 0.00 W

SVID Registers

Psys Warning 1 Counter (4Eh) 100 1.000 μs

Psys Warning 2 Counter (4Dh) 100 1.000 μs

Psys Critical Threshold (4Ah) 0 0 W

Psys Warning 1 Threshold (4Ch) 0 0 W

Psys Warning 2 Threshold (4Bh) 0 0 W

Psys Critical Assertion De-bounce Time (4Fh) 0 0 μs

Psys Critical De-assertion De-bounce Time (49h) 0 0 μs

☒ Enable write function

Write to device Read from device Close Help

Select which source for Psys calculations

SVID... MISC

This window have SVID settings that are not grouped into other functions.

