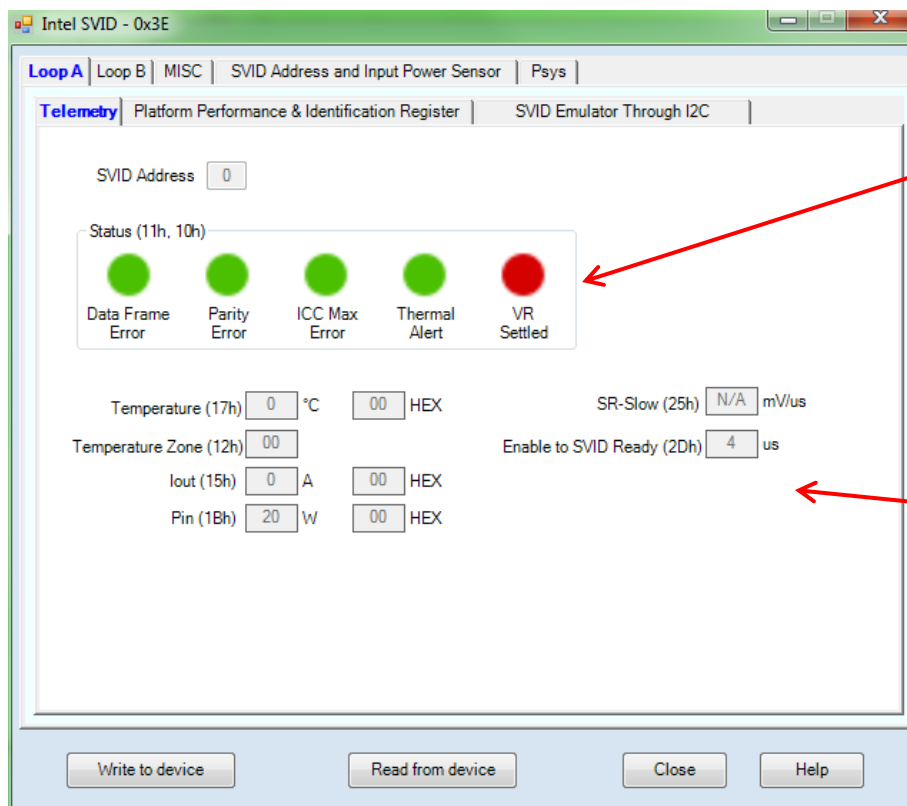


SVID... Telemetry

The Intel SVID window is only available for devices that support SVID



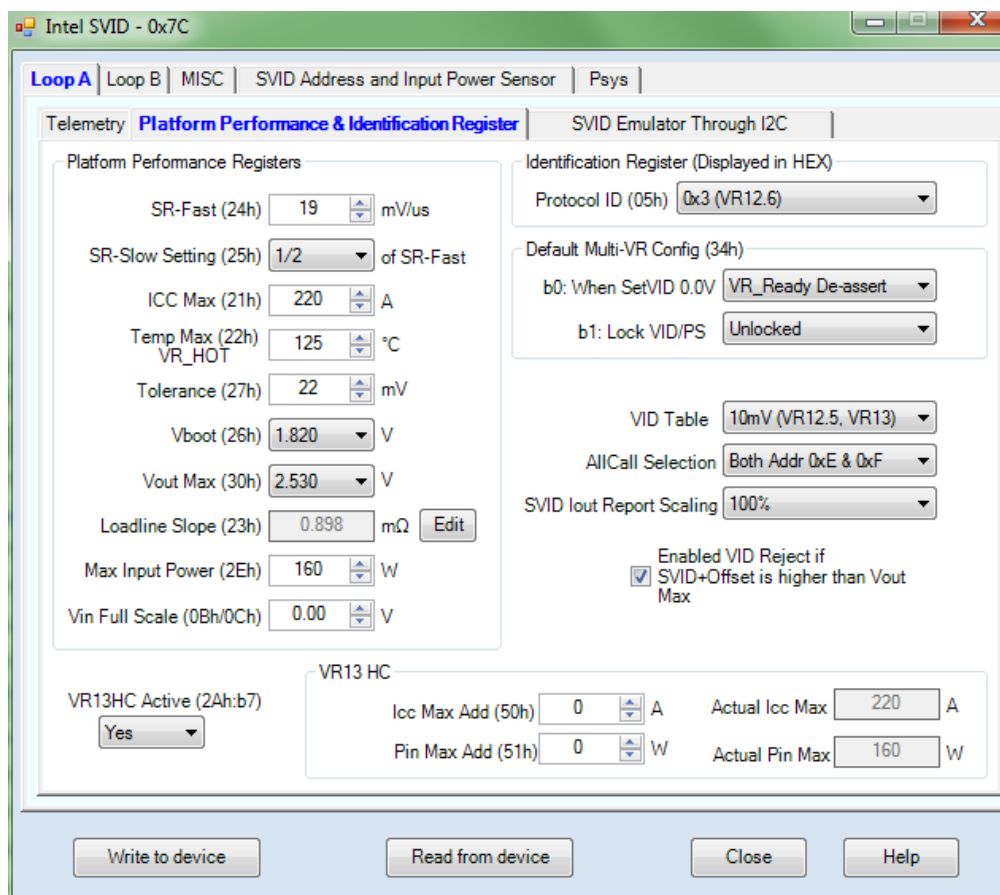
Readout of the status bits in SVID registers.

Readout of parameters in SVID registers.

SVID... Platform Performance

Allow settings of all the registers that SVID use.

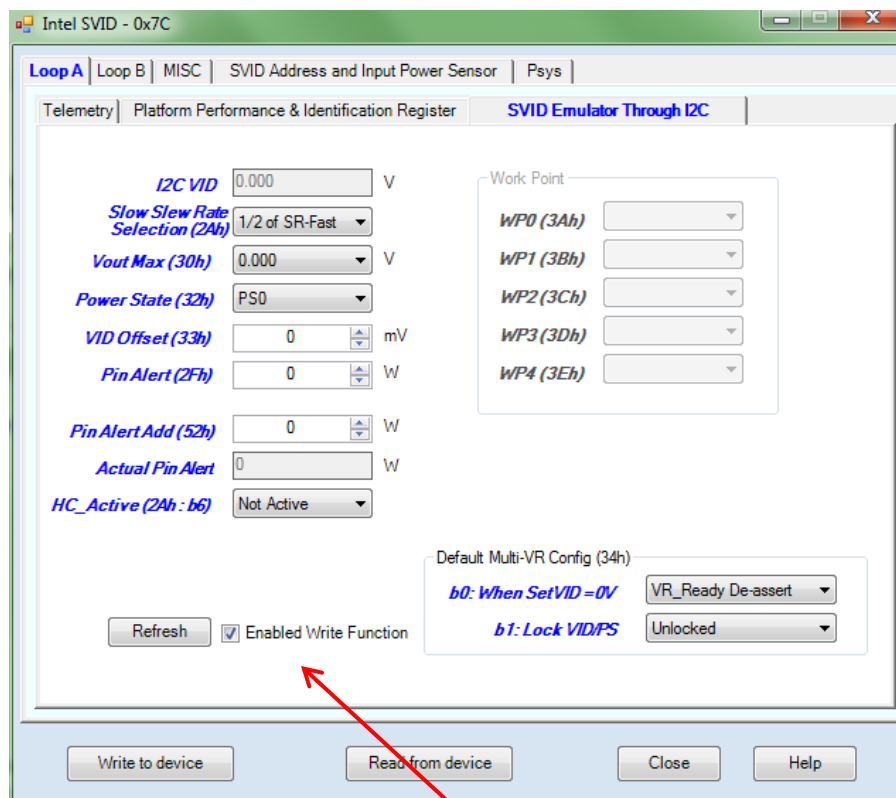
Depending on settings some parameters may not be visible if not used i.e. VR13HC settings only visible when VR13HC is activated.



SVID... Emulator through I2C

Allow a number of SVID settings to be emulated and sent via I2C to voltage regulator.

Loop B have less number of settings as it is already set in loop A



Mark the box to enable this I2C SVID emulator function

SVID... Address and Input Power Sensor

Selection of SVID address

SVID address can also be selected using external resistors. Table shows the different combinations of resistor and address

The screenshot shows the 'Intel SVID - 0x3E' application window. The 'SVID Address and Input Power Sensor' tab is active. On the left, under 'SVID Address', there are three dropdown menus: 'Loop A Address Base' set to '0x0', 'Loop B Address Base' set to 'Disabled SVID', and 'SVID Address Setting' set to 'based address'. On the right, under 'Input Power Sensor SVID Address', there is a dropdown menu set to 'Use Input Power Rail with SVID Addr 0xD'. Below this is the 'Input Power Sensor Rail' section, which includes a 'Platform Performance & Identification Register' with fields for 'Max Input Power (2Eh)' (160 W), 'Max Input Power Add (51h)' (0 W), and 'Actual Input Power' (160 W). It also has a 'Protocol ID (05h)' dropdown set to '0x0 (N/A)' and an 'AllCall Selection' dropdown set to 'Both Addr 0xE & 0xF'. At the bottom right, the 'SVID Emulator Through I2C' section has a 'Power State (32h)' dropdown set to 'PS0', 'Pin Alert (2Fh)' (0 W), 'Pin Alert Add (52h)' (0 W), and 'Actual Pin Alert' (0 W). There is an 'Enable' checkbox which is checked, and a 'Refresh' button. At the very bottom of the window are buttons for 'Write to device', 'Read from device', 'Close', and 'Help'.

Selection of Input power sensor.

Depending on selections some of the menus may be grayed out as they are not selectable

Allow emulation of SVID via I2C commands

Power state and input power alert **Pin Alert** can be set from GUI when the enable box is marked

SVID... Psys

Intel SVID - 0x3E

Loop A | Loop B | MISC | SVID Address and Input Power Sensor | **Psys**

Psys Source: Vin x lin

Input Power Signal Measurement

Gain: 0.0000 Offset: 0.00

SVID Registers

Psys Warning 1 Counter (4Eh) 100 1.000 μ s

Psys Warning 2 Counter (4Dh) 100 1.000 μ s

Psys Critical Threshold (4Ah) 0 0 W

Psys Warning 1 Threshold (4Ch) 0 0 W

Psys Warning 2 Threshold (4Bh) 0 0 W

Psys_Critical Assertion De-bounce Time (4F) 0 0 μ s

Psys_Critical De-assertion De-bounce Time (49) 0 0 μ s

☒ Enable write function Refresh

Write to device Read from device Close Help

Select which source for Psys calculations

SVID... MISC

This window have SVID settings that are not grouped into other functions.

