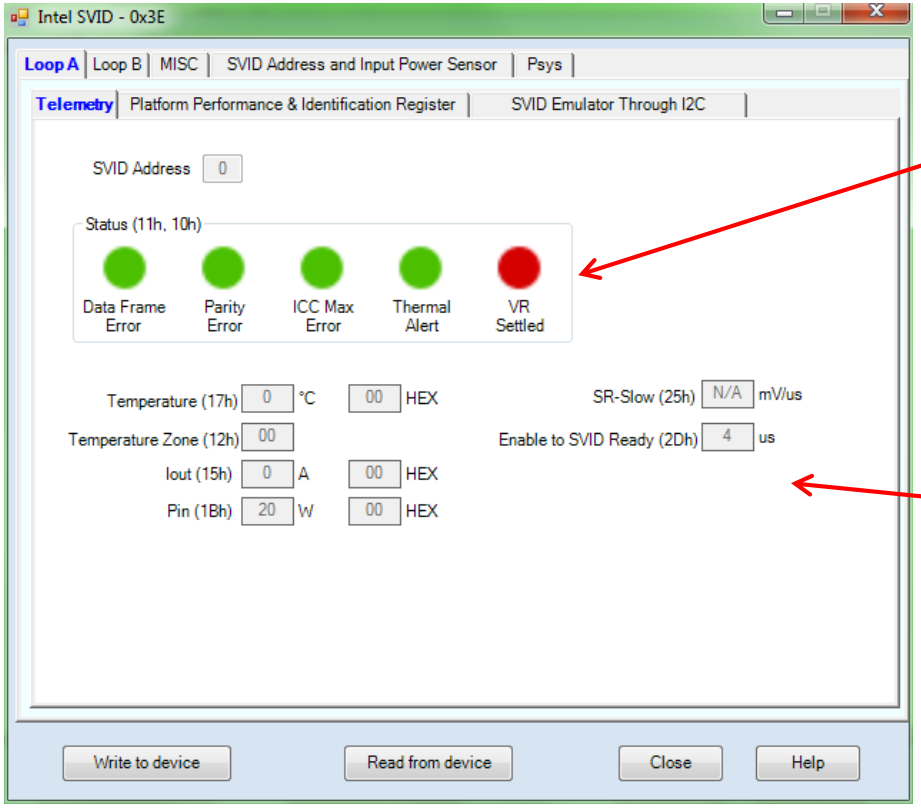
SVID... Telemetry XDPE122xx family

The Intel SVID window is only available for devices that support SVID



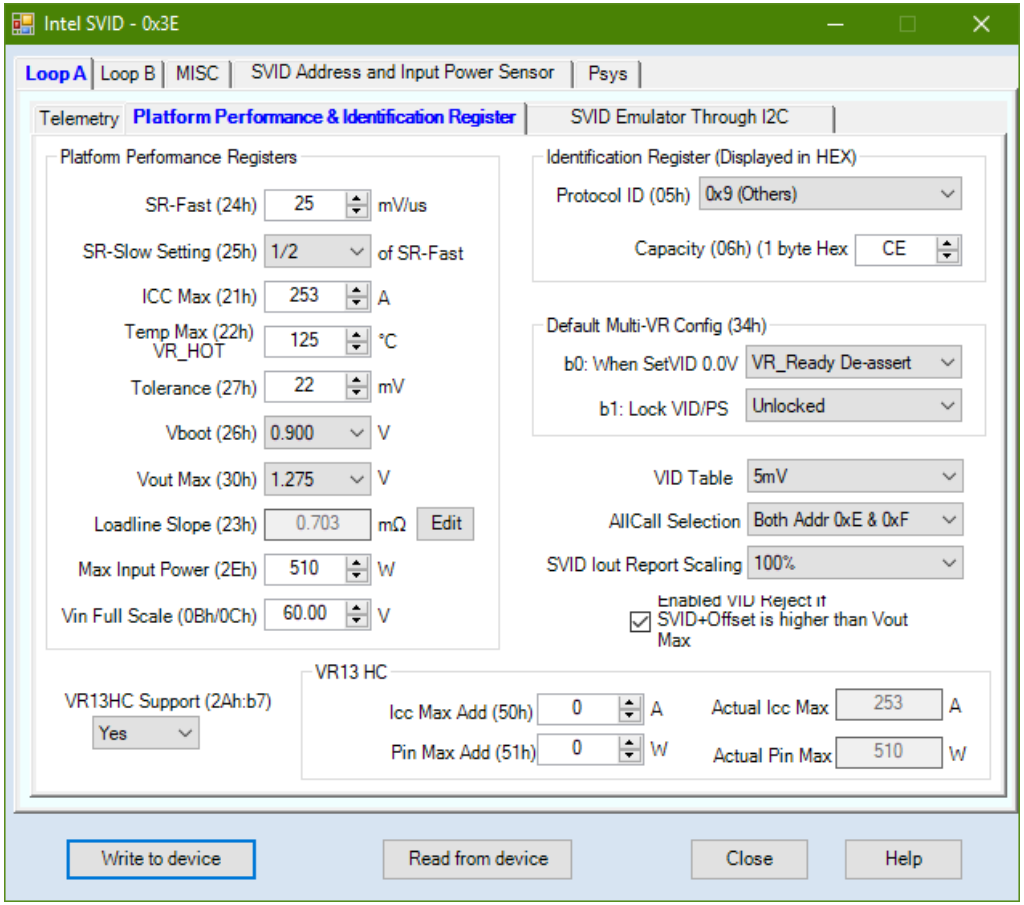
Readout of the status bits in SVID registers.

Readout of parameters in SVID registers.

SVID... Platform Performance XDPE122xx family

Allow settings of all the registers that SVID use.

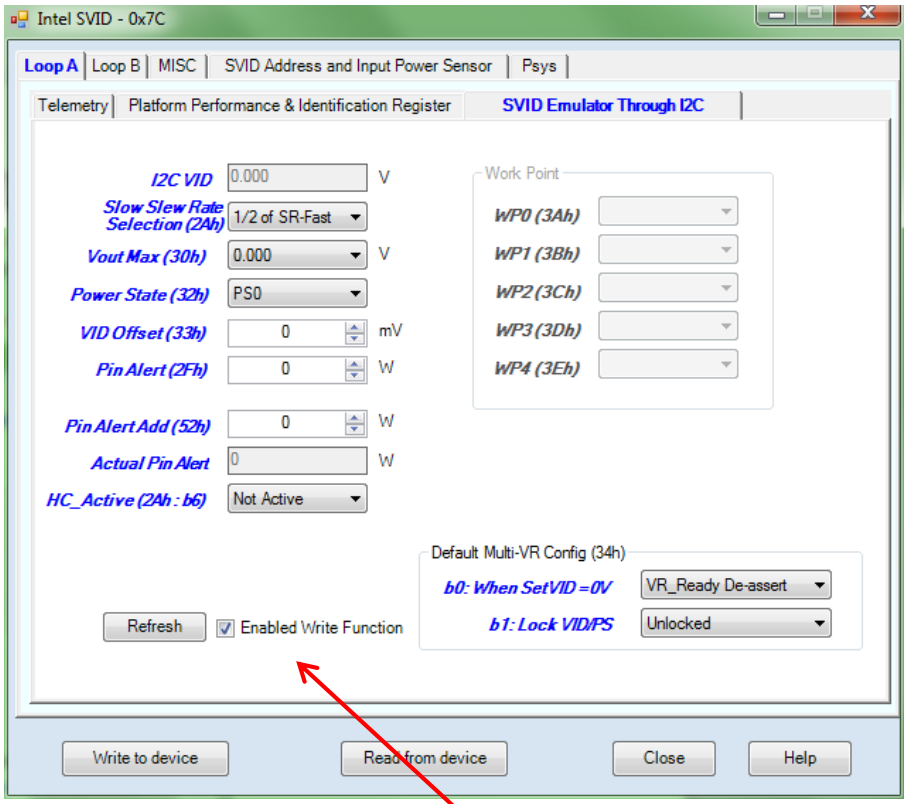
Depending on settings some parameters may not be visible if not used i.e. VR13HC settings only visible when VR13HC is activated.



SVID... Emulator through I2C XDPE122xx family

Allow a number of SVID settings to be emulated and sent via I2C to voltage regulator.

Loop B have less number of settings as it is already set in loop A

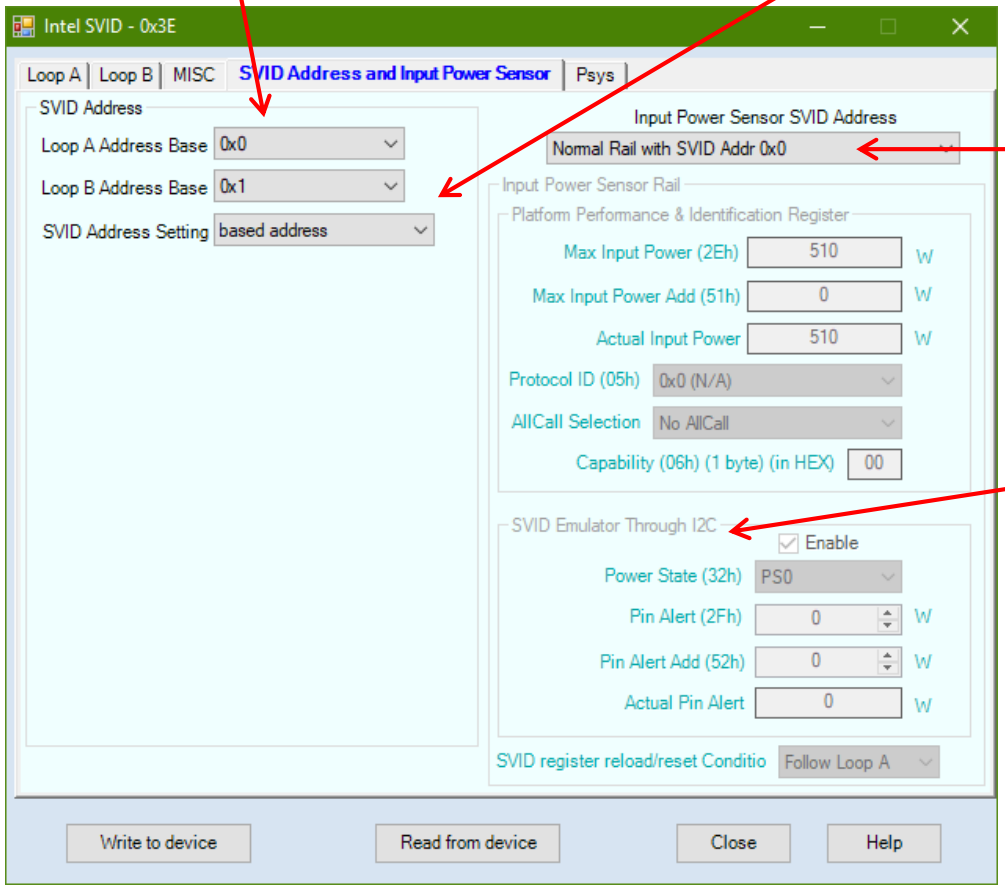


Mark the box to enable this I2C SVID emulator function

SVID... Address and Input Power Sensor XDPE122xx family

Selection of SVID address

SVID address can also be selected using external resistors. Table shows the different combinations of resistor and address



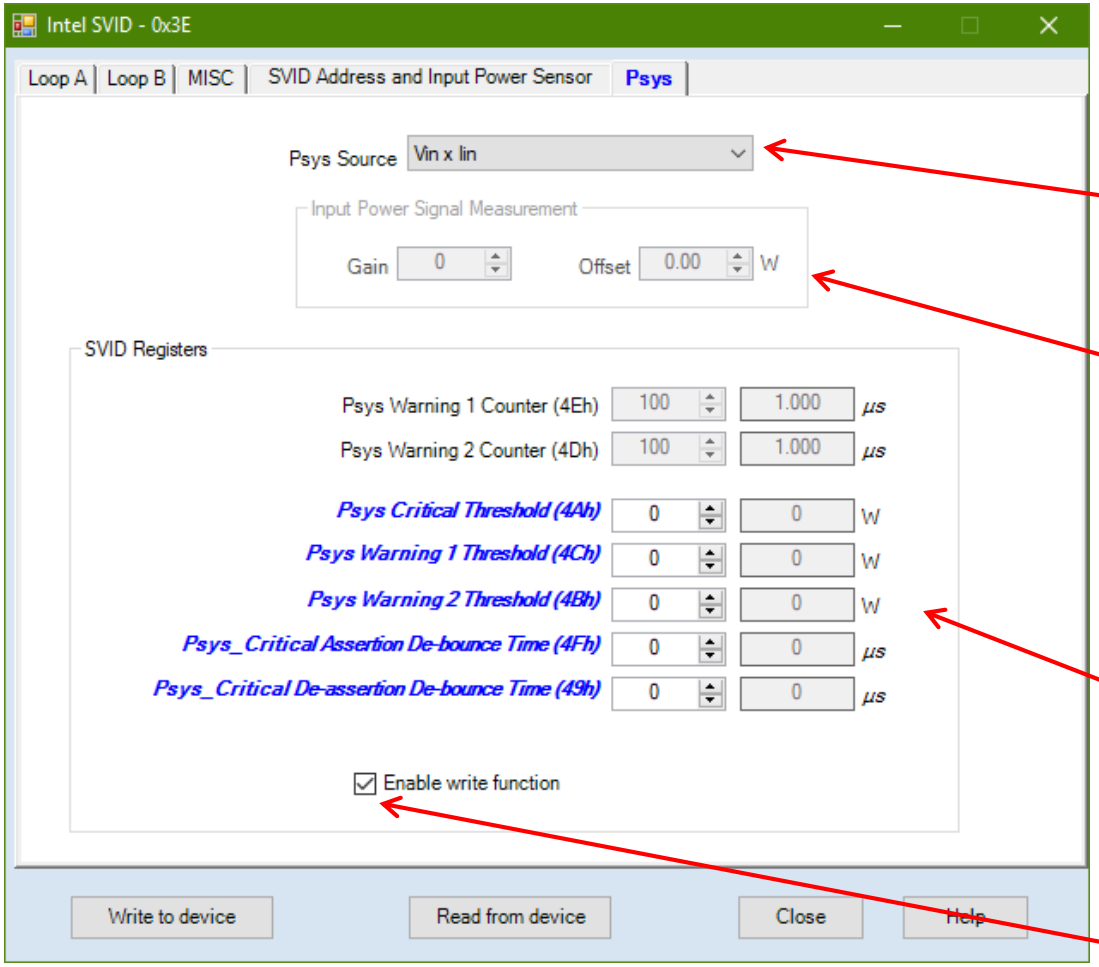
Selection of Input power sensor.

Depending on selections some of the menus may be grayed out as they are not selectable

Allow emulation of SVID via I2C commands

Power state and input power alert **Pin Alert** can be set from GUI when the enable box is marked

SVID... Psys XDPE122xx family



Select which source to use for Psys calculations.

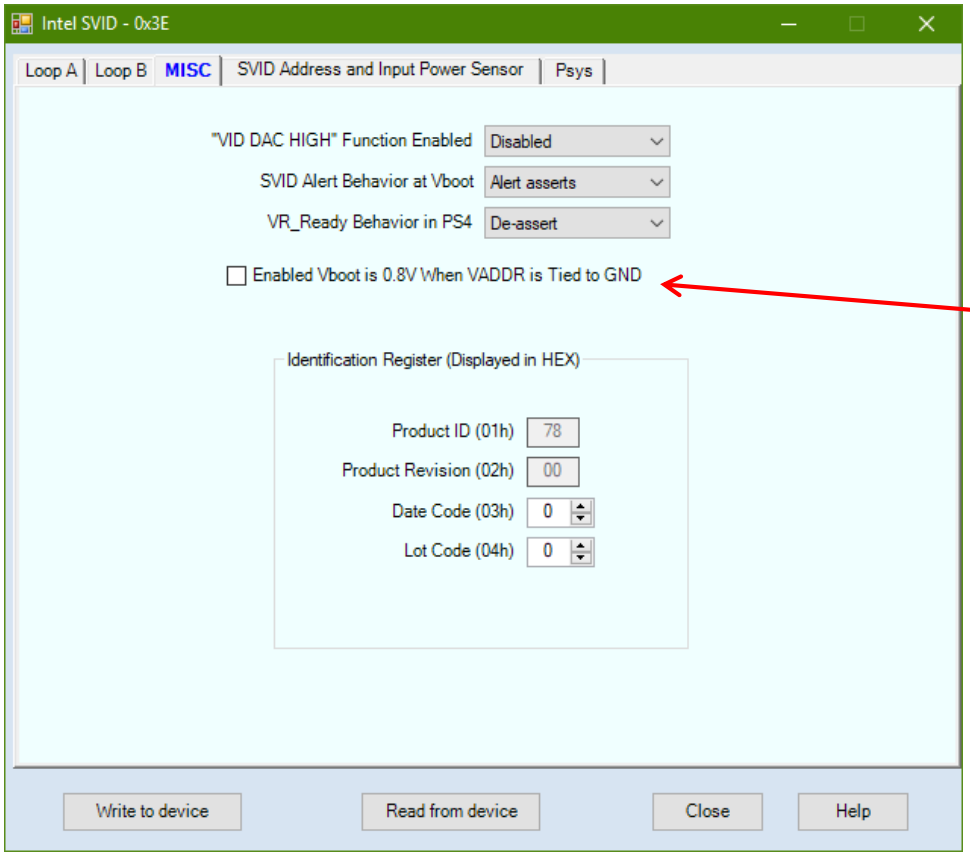
For some Source settings the Gain and offset can be trimmed to make reported input Power match measured values.

Settings for PSYS registers

To be able to write to the registers check this box.

SVID... MISC XDPE122xx family

This window have SVID settings that are not grouped into other functions.



Enable Vboot It is used for testing purpose in production test. In a Vboot=0 application it is not easy to know if the 0Volt means there is an error or not. With function enabled and short vaddr pin to ground, output will go to 0.8V when VREN is asserted.

Using this function, the production can short vaddr pin to GND and measure Vout to determine if the board has any issues before they ship the board.