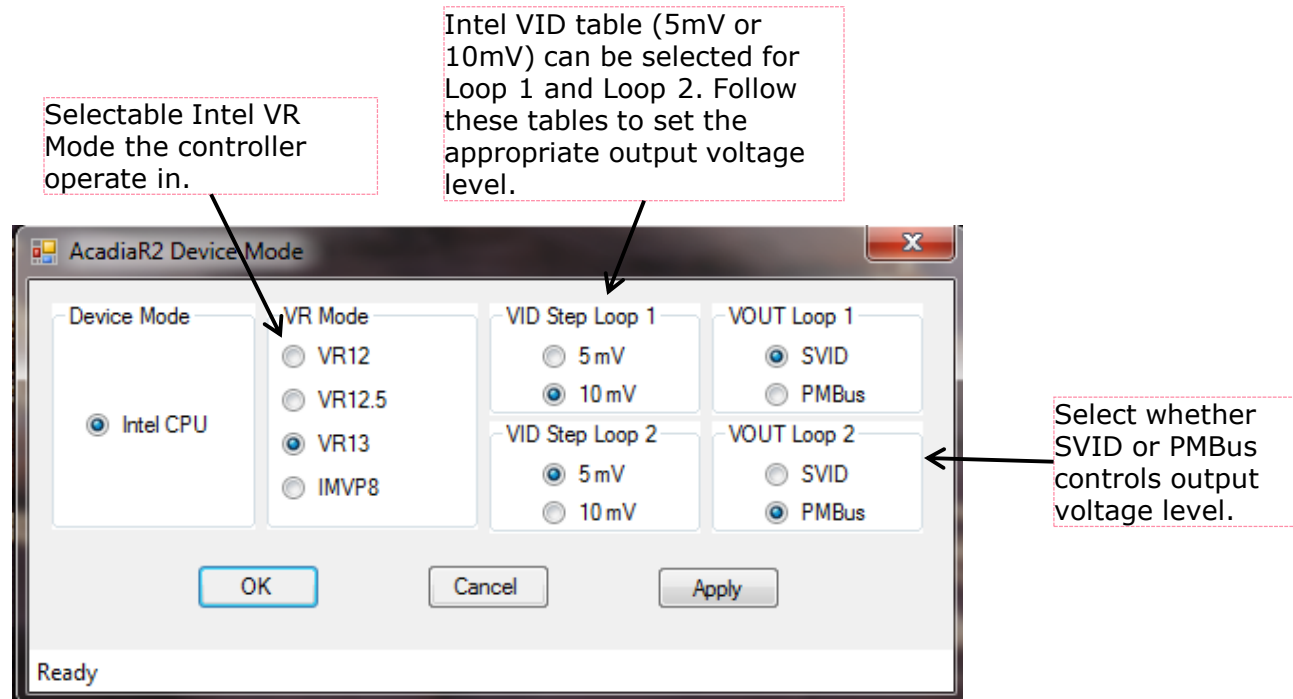# Design Tools
Device Operating Mode

1. Device Operating Mode
CPU or MPOL

Acadia can be used for Intel VR 13, VR12, VR12.5, IMPVP8 designs and DDR Memory. Remember to press and **Apply** and **Ok** to program the settings.

Intel VID table (5mV or 10mV) can be selected for Loop 1 and Loop 2. Follow these tables to set the appropriate output voltage level.

Selectable Intel VR Mode the controller operate in.

Select whether SVID or PMBus controls output voltage level.

Intel SVID interface is a three-wire interface between the Intel processor and VR through clock, data, and alert# signals. Acadia is compliance with all the required SVID registers and commands and most of the optional SVID registers and commands; the Intel CPU is able to detect these functionality.

VR12.5 Mode – the controller is automatically locked to 10mV VID step for both loops.
VR13 Mode – user can configure the boot voltage in 5mV steps (VR12 mode VID table) or 10mV steps (VR12.5 mode VID table).
IMVP8 Mode - user can configure the boot voltage in 5mV steps (VR12 mode VID table) or 10mV steps (VR12.5 mode VID table).
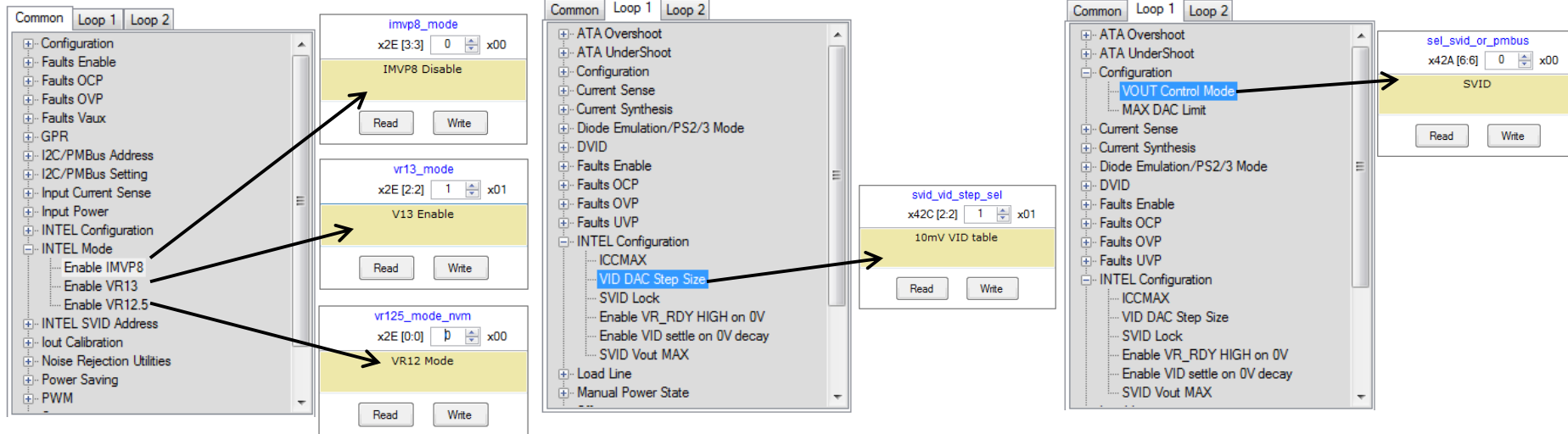
СКАЧАНО С WWW.SW.BAND – ПРИСОЕДИНЯЙСЯ!

Design Tools
Device Operating Mode

1. Device Operating Mode
CPU or MPOL

The options for **Device Operative Mode** in the Design Tools can also be programmed in the commands in tree view
Register map

Under **INTEL Mode** in Common section, IMVP8 and VR13 can be enabled(1) or disabled(0) and VR12(0) or VR12.5(1) can be picked.

Under **INTEL Configuration** in Loop 1/2 section, **VID DAC Step Size** can be set to either 5mV VID table(0) or 10mV VID table(1).

Under **Configuration** in Loop 1/2 section, **VOUT Control Mode** can be set to either SVID(0) or PMBus(1).