

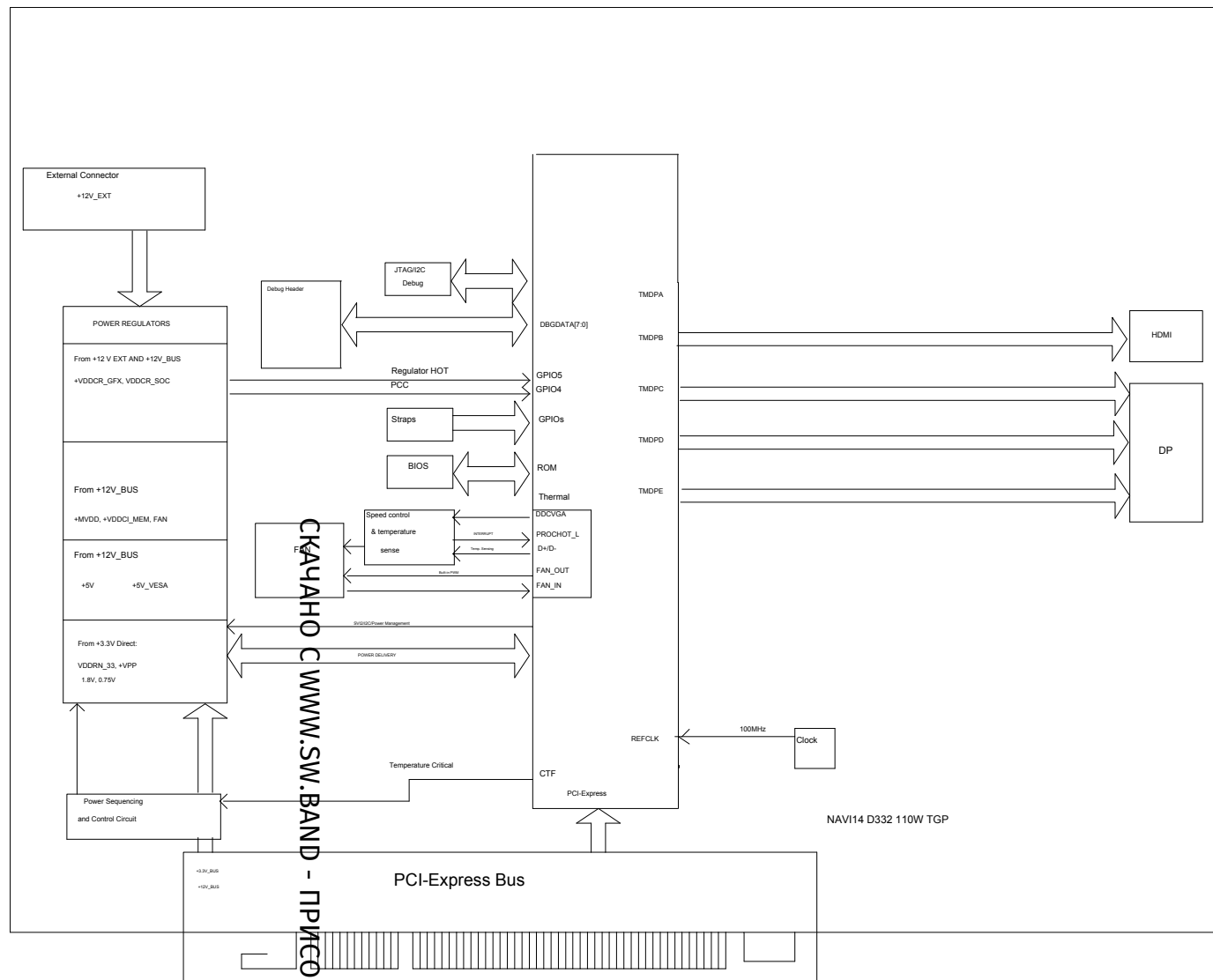
NAVI14 GDDR6 4PCS,8L 3xDPs +1xHDMI

105-D332xx-00B RevB Desktop board(110W TGP)

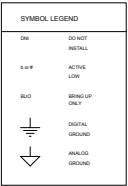
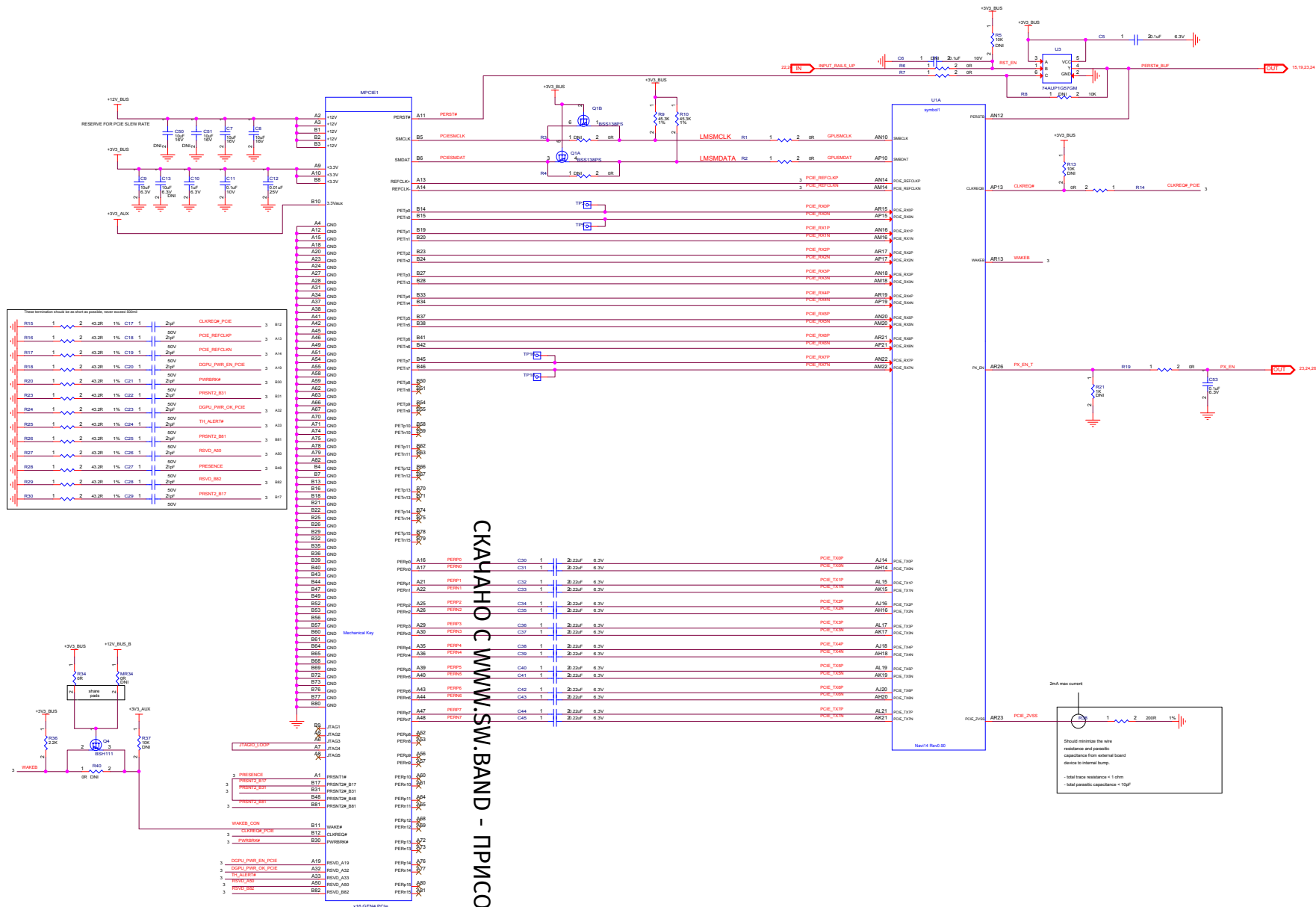
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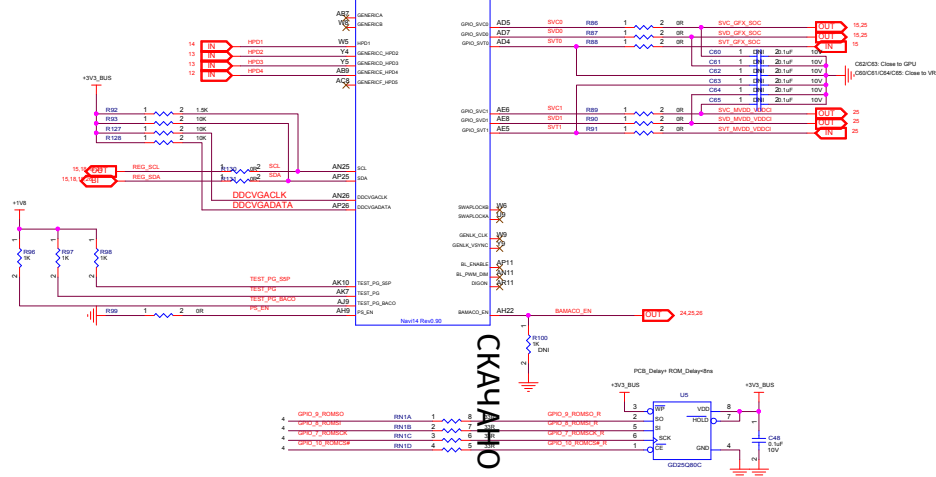
СВЯЗЬ С НАМИ
MSI BAND - ПРИСОЕДИНЯЙСЯ!



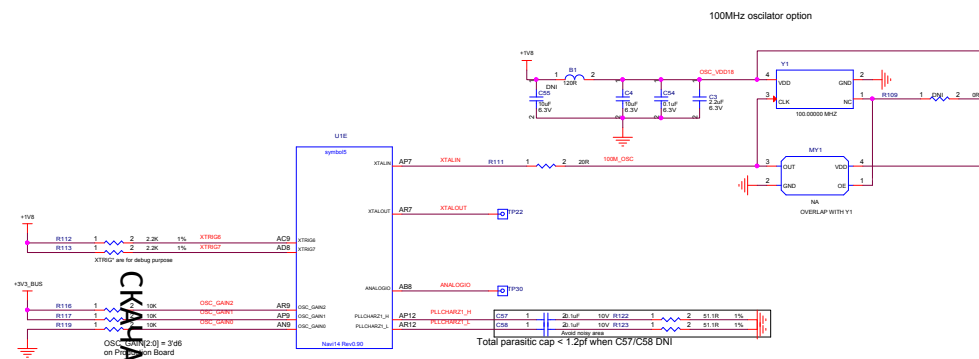
СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!



7-BIT I2C ADDRESS	8-BIT I2C ADDRESS	FUNCTION	I2C BUS	DEVICE
0x30	0x60	VDDCR_GFX/SOC	SCL/SDA	U501/R35217
0x22	0x44	MI_VDDVDDCI		U701/NCP81022
0x53	0xA6	PC_Compensated	SCL/SDA	U4601/GS8601
0x48	0x90	G8 Temperature		U4000/TMP102A
0x4C	0x98	EXT SENSOR		U4001/LM98163



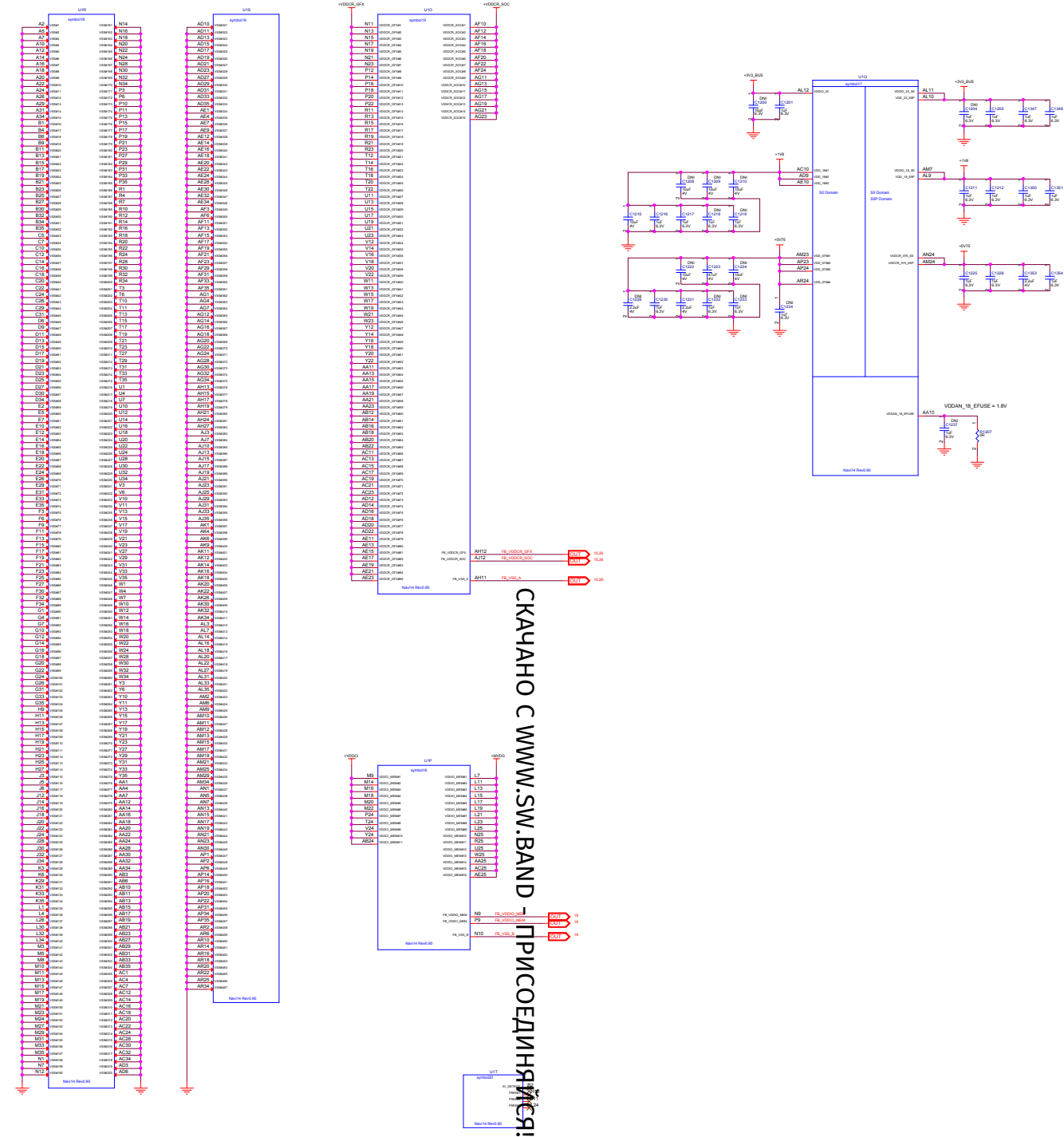
User		Internal Default Value		Definition	
BIF		0	STRAP_BIF_GEN4_DIS_A 0: PCIe Gen4 supported 1: PCIe Gen4 not supported		
		0	PINSTRAP_BIF_CLK_PM_EN 0: CLC0REF power management capability is disabled 1: CLC0REF power management capability is enabled		
		0	PINSTRAP_BIF_LC_TX_SWING 0: Full swing mode 1: Reduced swing mode(half swing)		
		0	PINSTRAP_BIF_VGA_DIS 0: VGA controller capacity enabled 1: The device won't be recognized as the system's VGA controller		
DCE		0	PINSTRAP_AUD_PORT_CONN[2:0] Number of audio-capable display outputs	0: All endpoints connected 1: 5 endpoints connected 2: 4 endpoints connected 3: 4 endpoints connected 4: 4 endpoints connected Default(PINSTRAPS.PINSTRAP4.PINSTRAP3) = 000	4: 3 endpoints connected 5: 2 endpoints connected 6: 2 endpoints connected 7: 4 endpoints connected
		0	PINSTRAP_AUD[0]		
		0			
		0	PINSTRAP_FIG[1:0] - No definition on NV		
		0	PINSTRAP_MVDD_FB_DIVIDER_CONFIG 0: Divider does not exist 1: Divider exists		
		1			
		0			
SMU		1	PINSTRAP_BFI_MEM_AP_SIZE[0] Or PINSTRAP_ROM_CONFIG[2:0] 100: 0x2007 (ST1) M2P0ISA 101: 0x0007 (ST1) M2P0ISA 102: 0x0007 (ST1) M2P0ISA 103: 2x0007 (ST1) M2P0ISA 104: 0x0007 (ST1) M2P0ISA 105: 0x0007 (ST1) M2P0ISA 106: 0x0007 (ST1) M2P0ISA 107: 0x0007 (ST1) M2P0ISA 108: 0x0007 (ST1) M2P0ISA 109: 0x0007 (ST1) M2P0ISA 110: 0x0007 (ST1) M2P0ISA 111: 0x0007 (ST1) M2P0ISA 112: 0x0007 (ST1) M2P0ISA 113: 0x0007 (ST1) M2P0ISA 114: 0x0007 (ST1) M2P0ISA 115: 0x0007 (ST1) M2P0ISA 116: 0x0007 (ST1) M2P0ISA 117: 0x0007 (ST1) M2P0ISA 118: 0x0007 (ST1) M2P0ISA 119: 0x0007 (ST1) M2P0ISA 120: 0x0007 (ST1) M2P0ISA 121: 0x0007 (ST1) M2P0ISA 122: 0x0007 (ST1) M2P0ISA 123: 0x0007 (ST1) M2P0ISA 124: 0x0007 (ST1) M2P0ISA 125: 0x0007 (ST1) M2P0ISA 126: 0x0007 (ST1) M2P0ISA 127: 0x0007 (ST1) M2P0ISA 128: 0x0007 (ST1) M2P0ISA 129: 0x0007 (ST1) M2P0ISA 130: 0x0007 (ST1) M2P0ISA 131: 0x0007 (ST1) M2P0ISA 132: 0x0007 (ST1) M2P0ISA 133: 0x0007 (ST1) M2P0ISA 134: 0x0007 (ST1) M2P0ISA 135: 0x0007 (ST1) M2P0ISA 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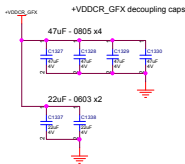
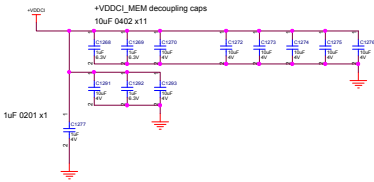
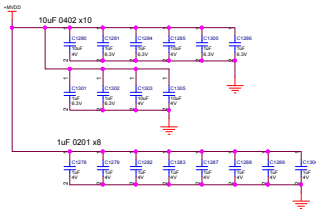
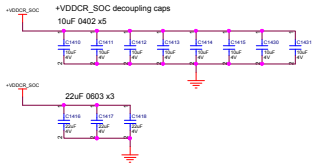
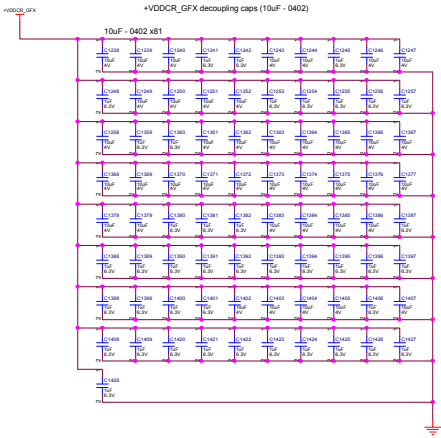


СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

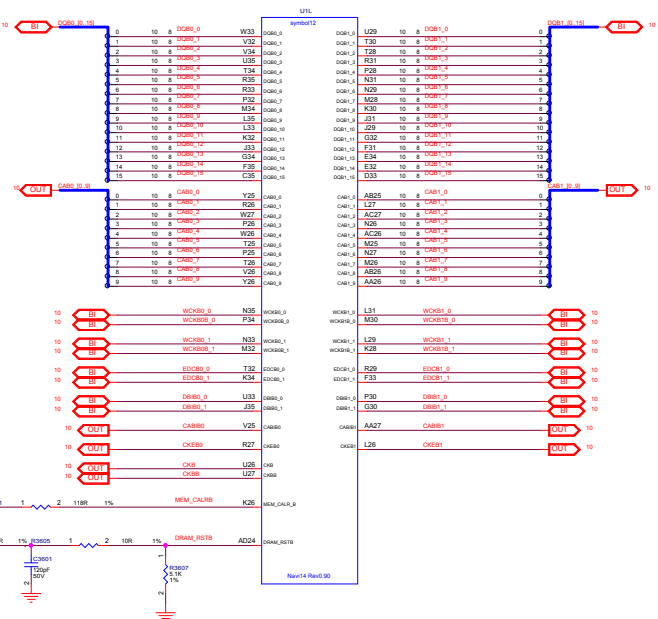
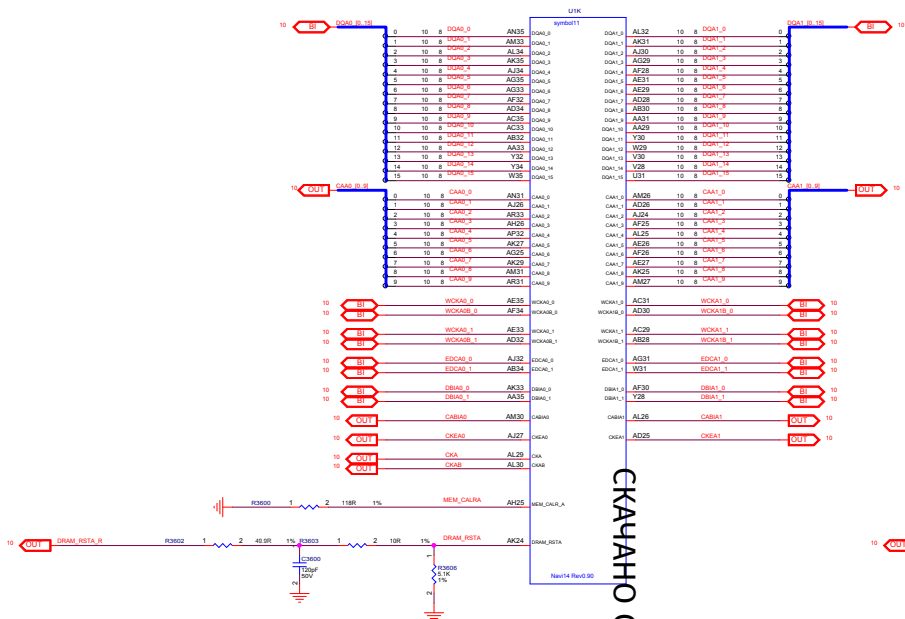


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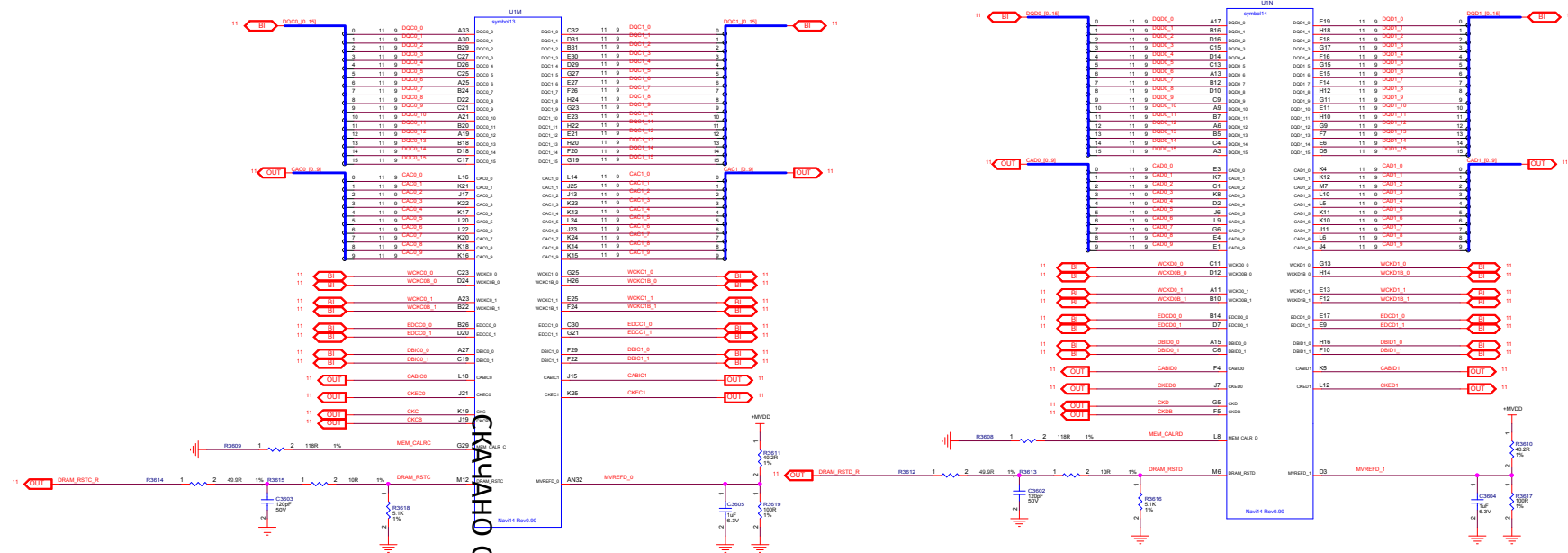


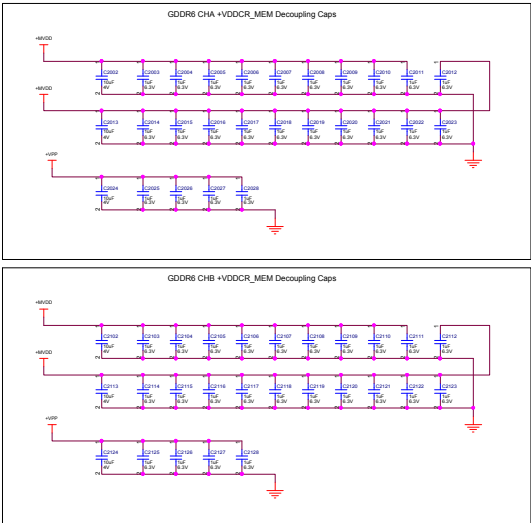


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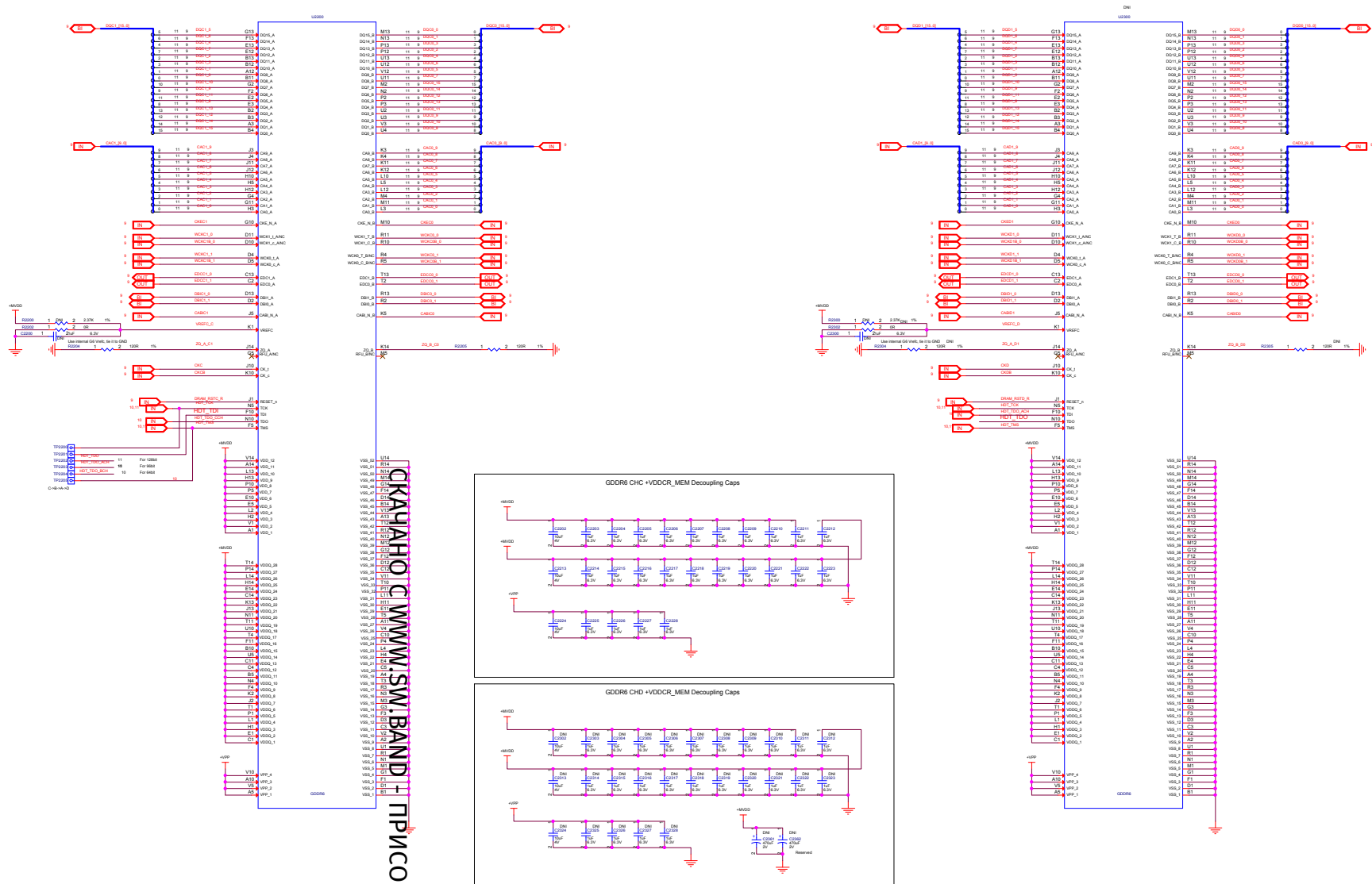


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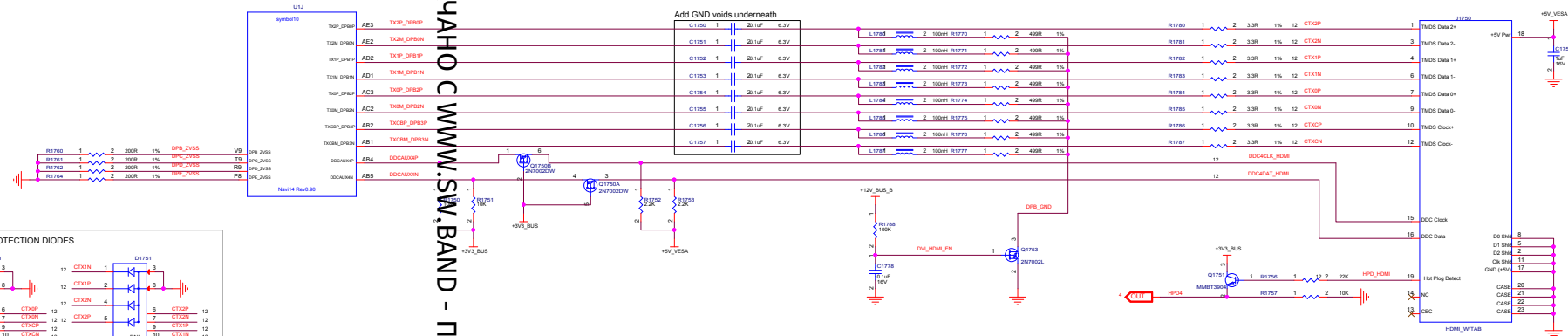
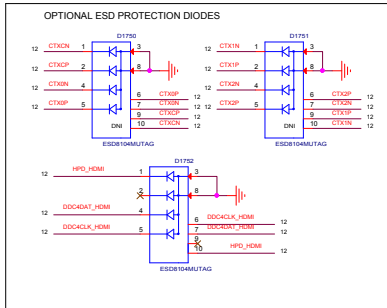
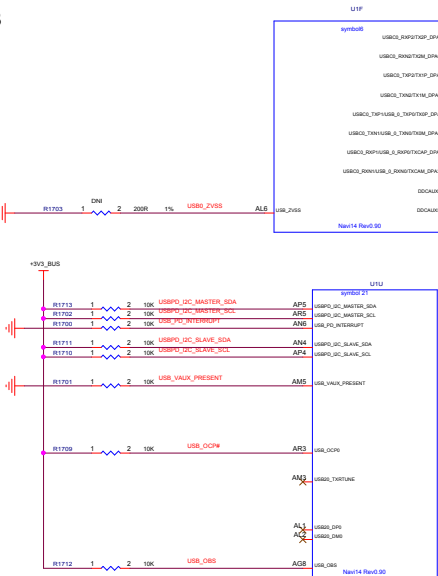


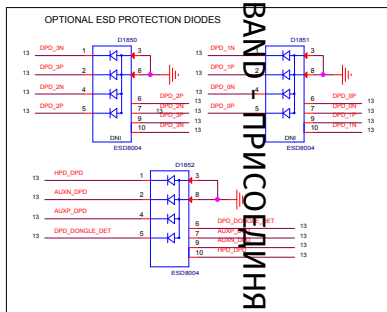
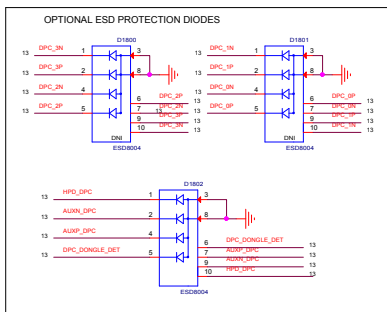
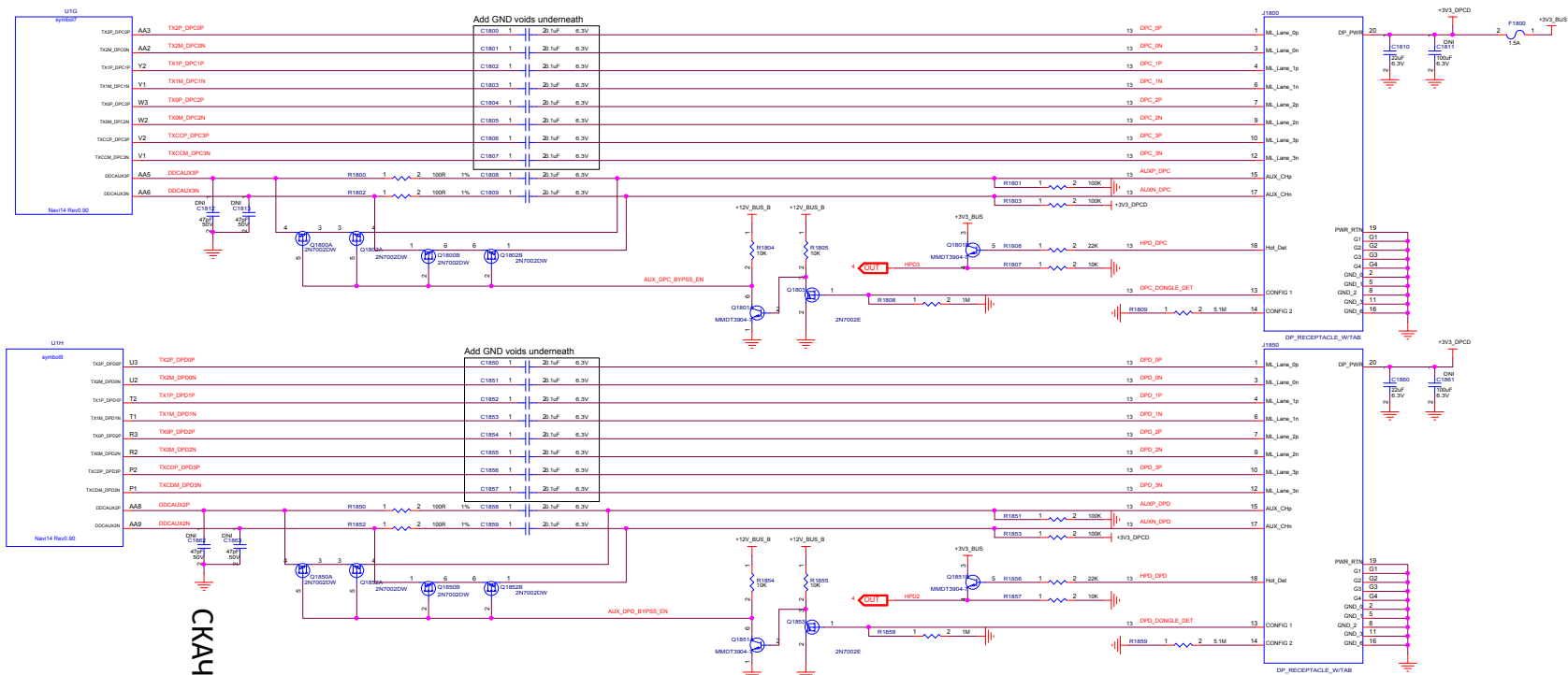


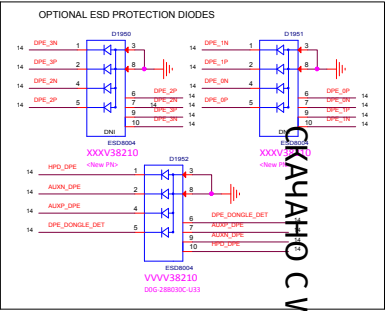
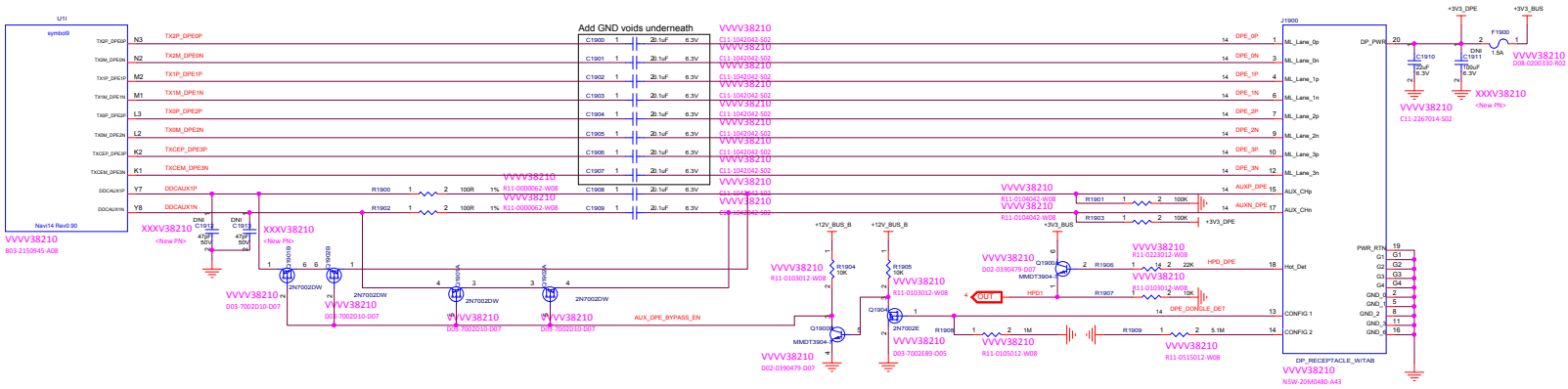
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(12) TMDP AB



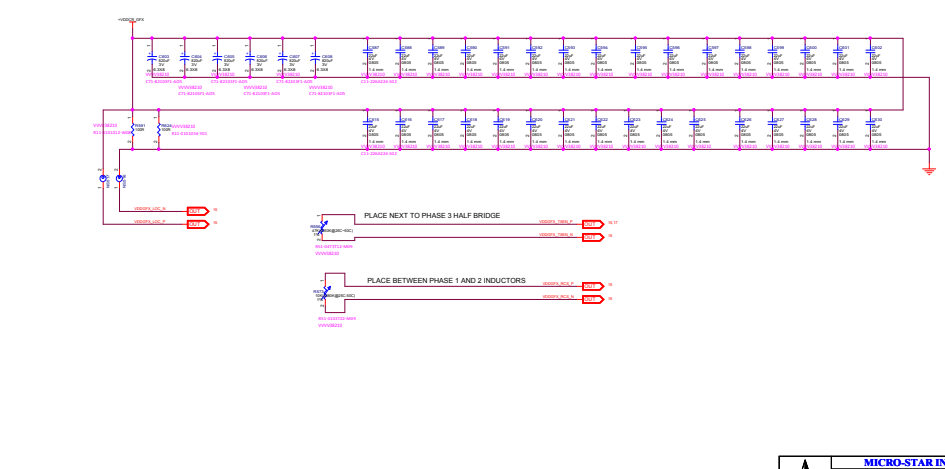
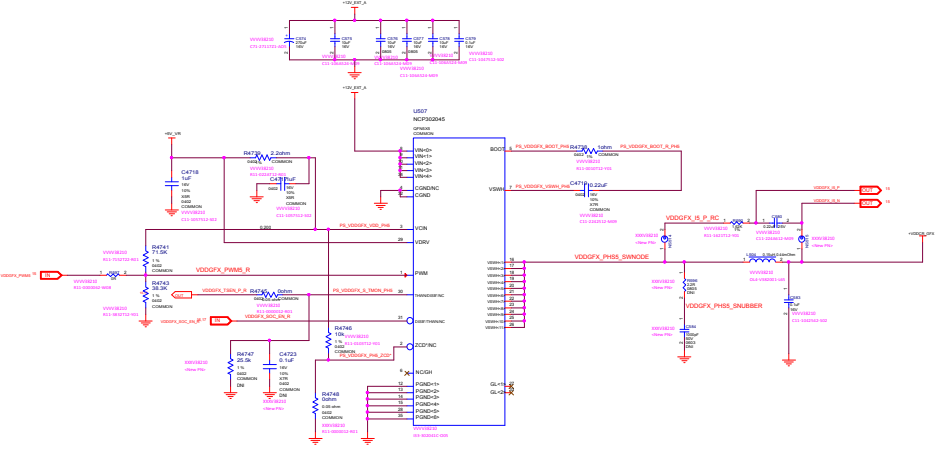
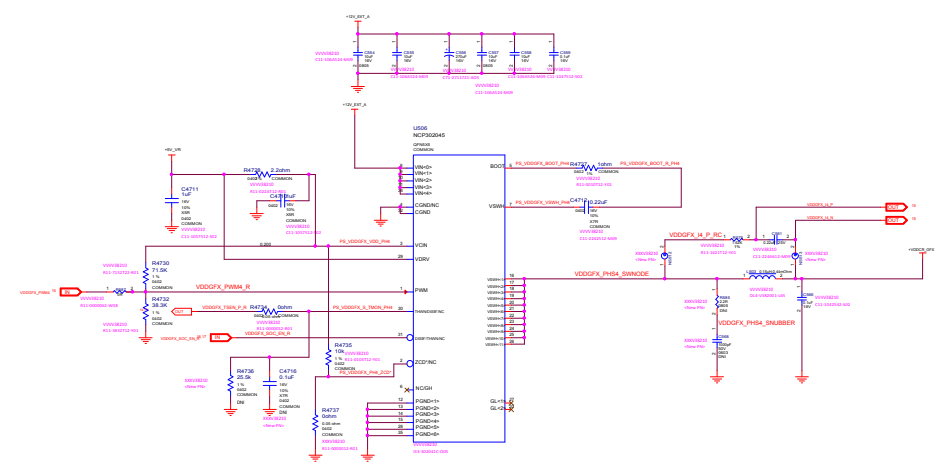
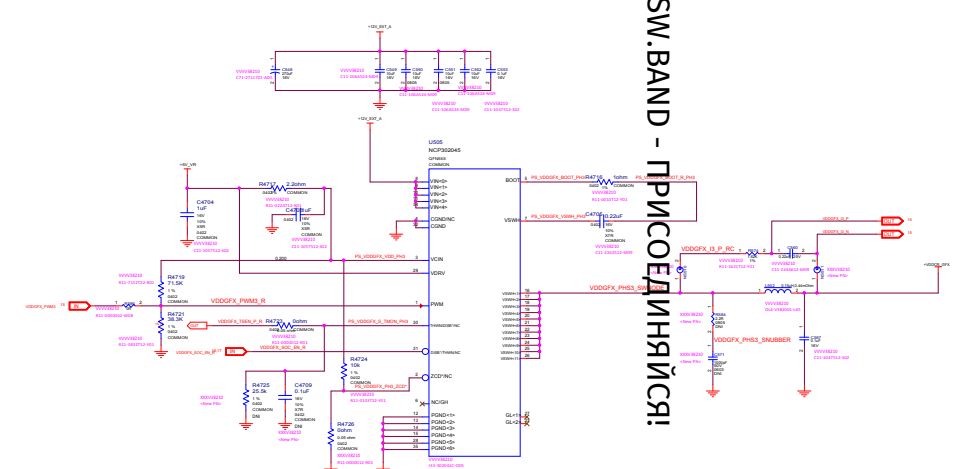
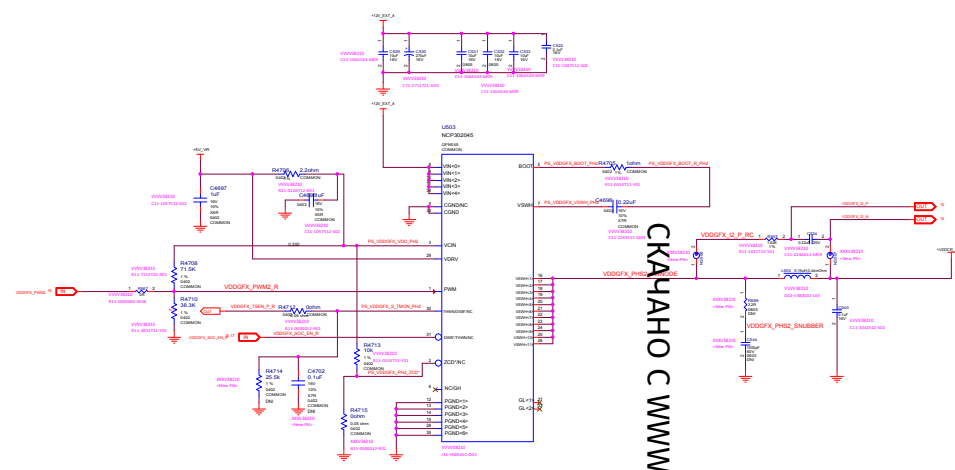
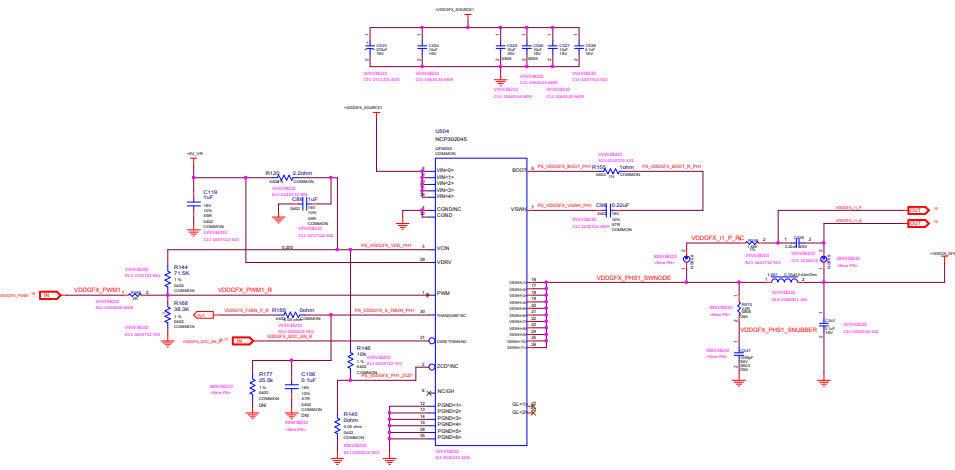




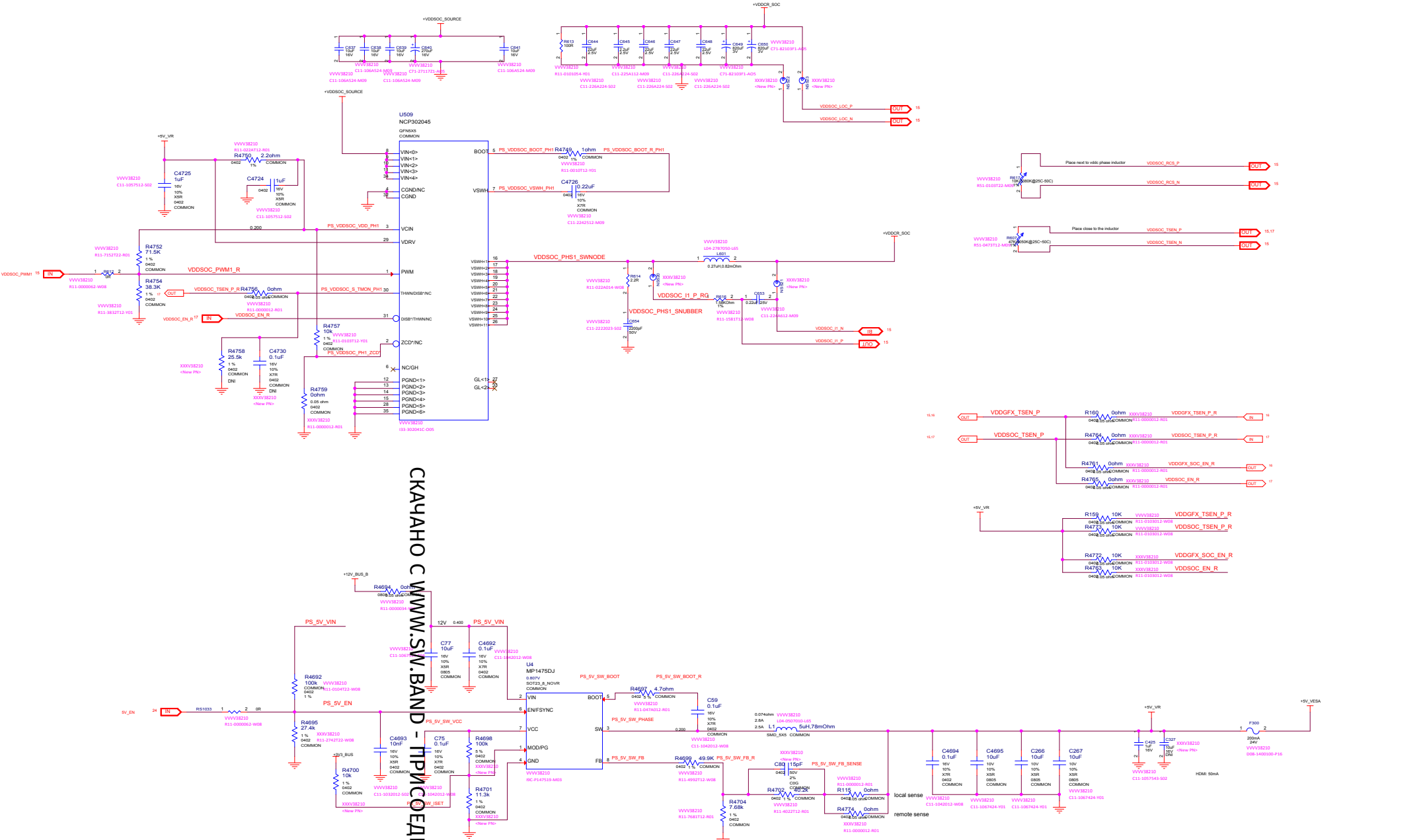
СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

СКАЧАНО С WWW.SMIBAND - ПРИСОЕДИНЯЙСЯ!



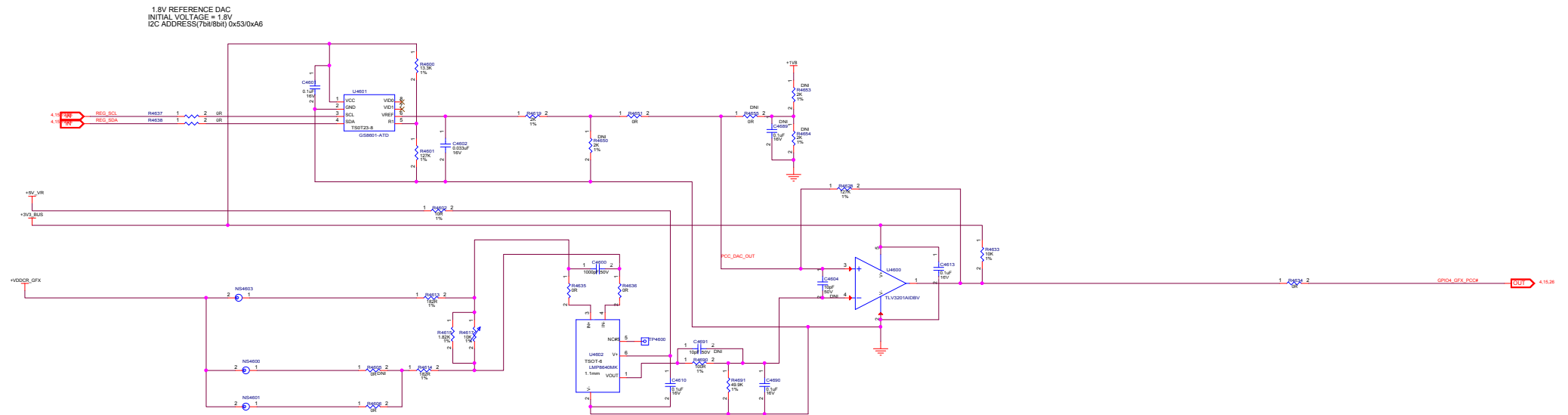


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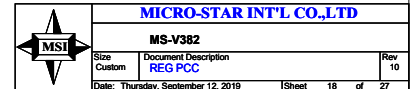


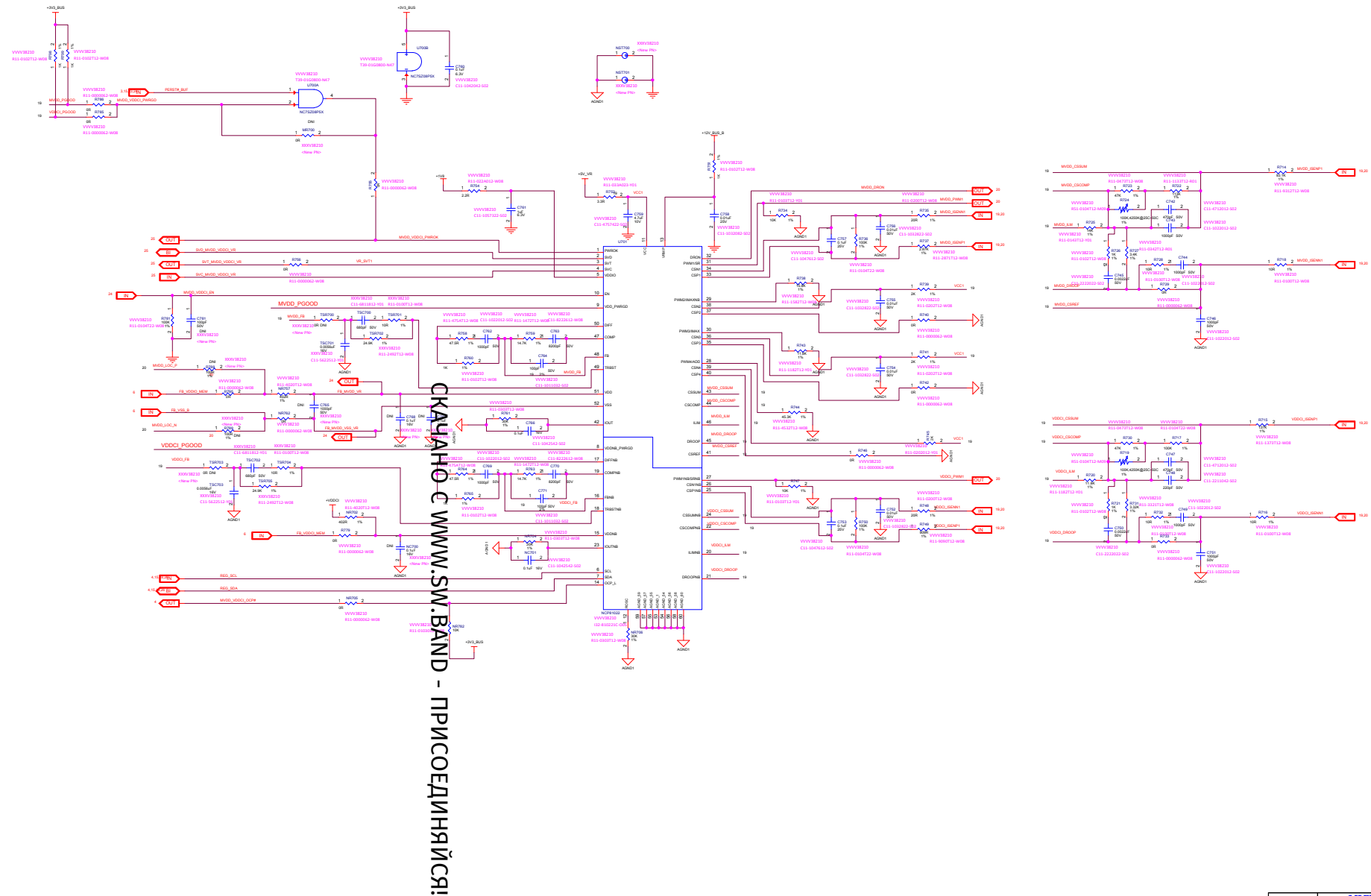
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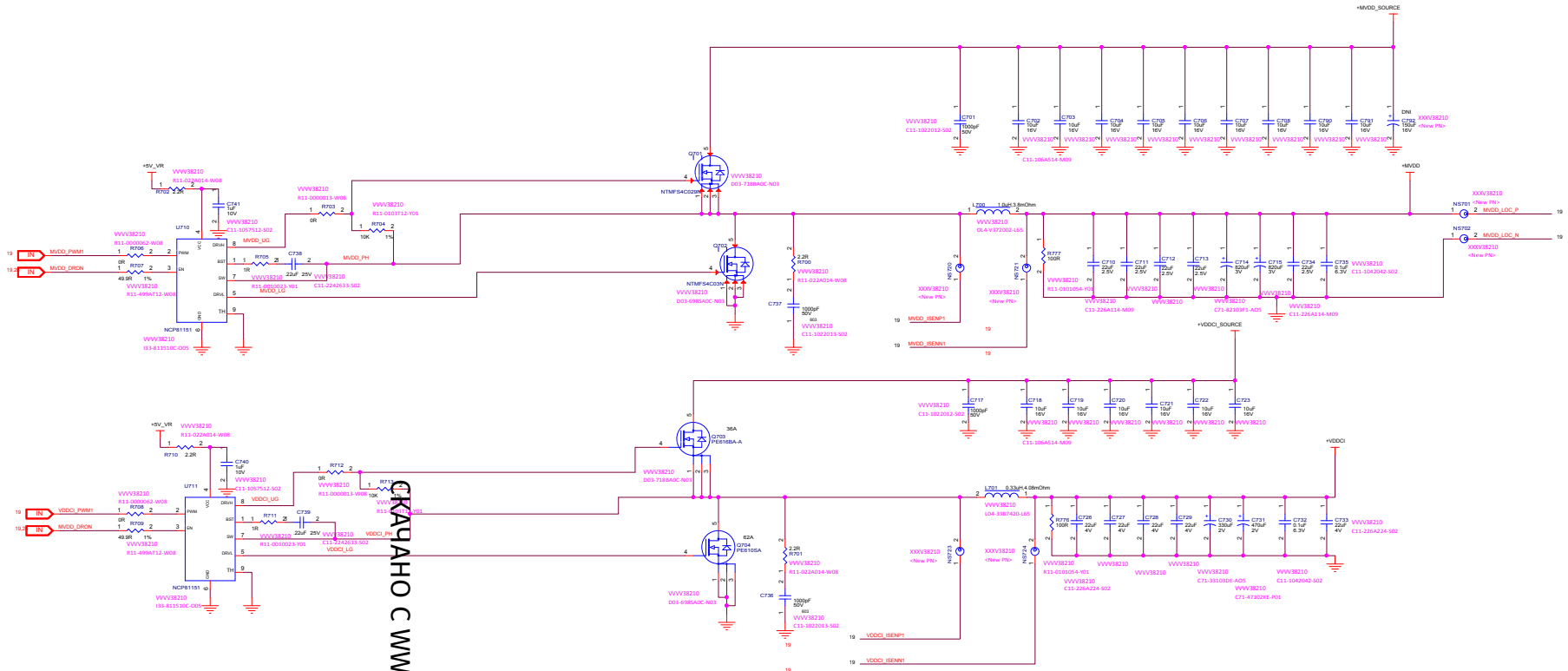
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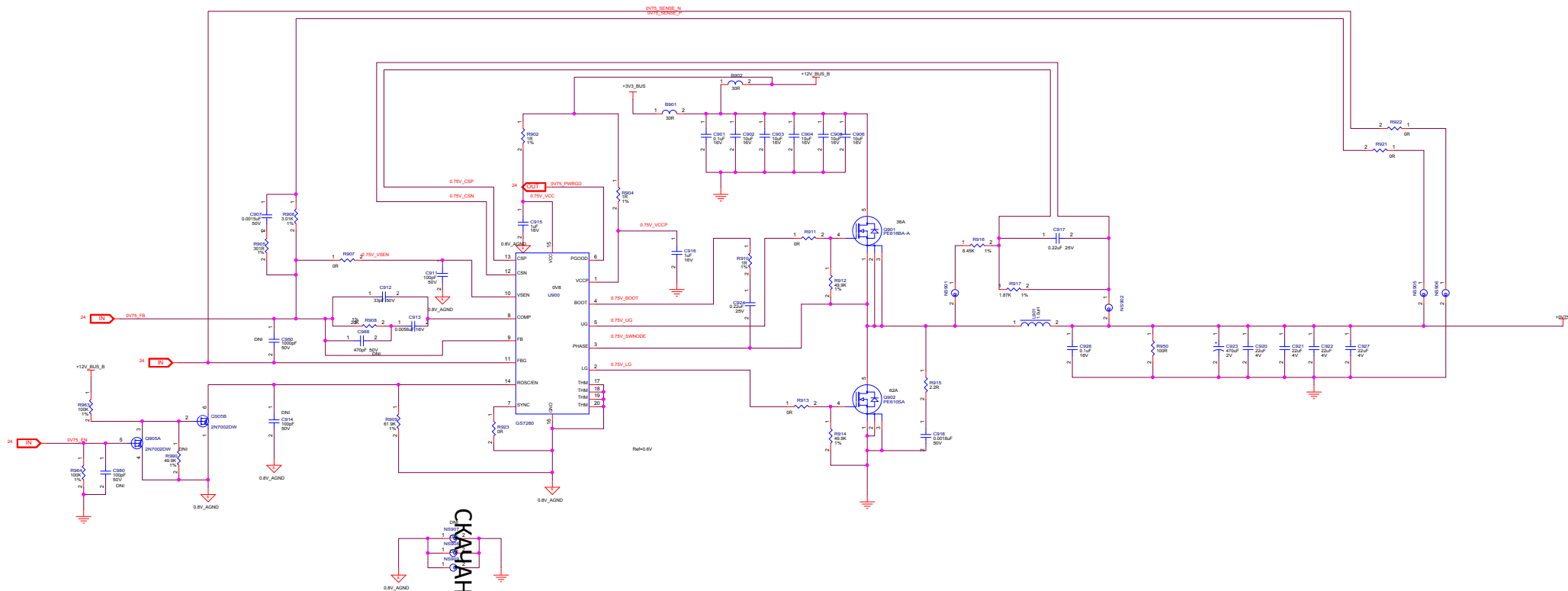
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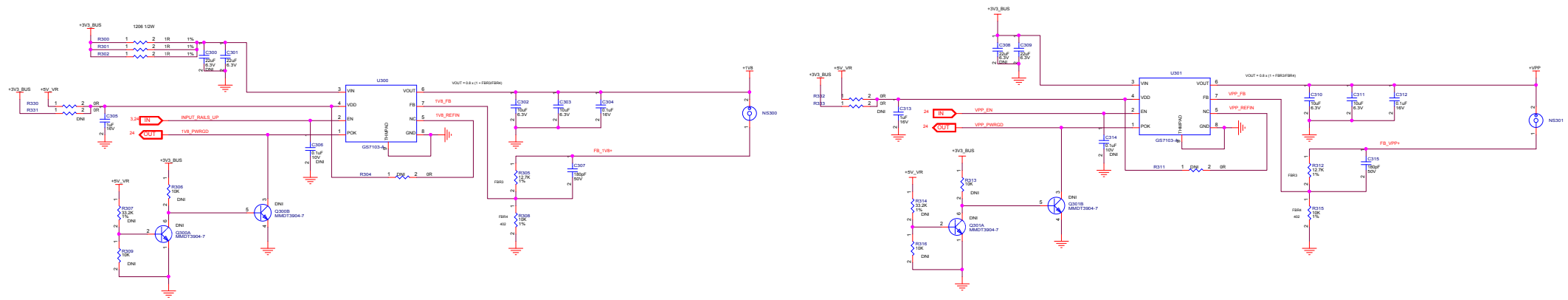


СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!



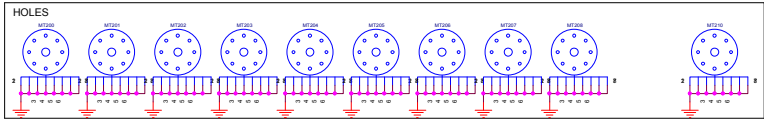
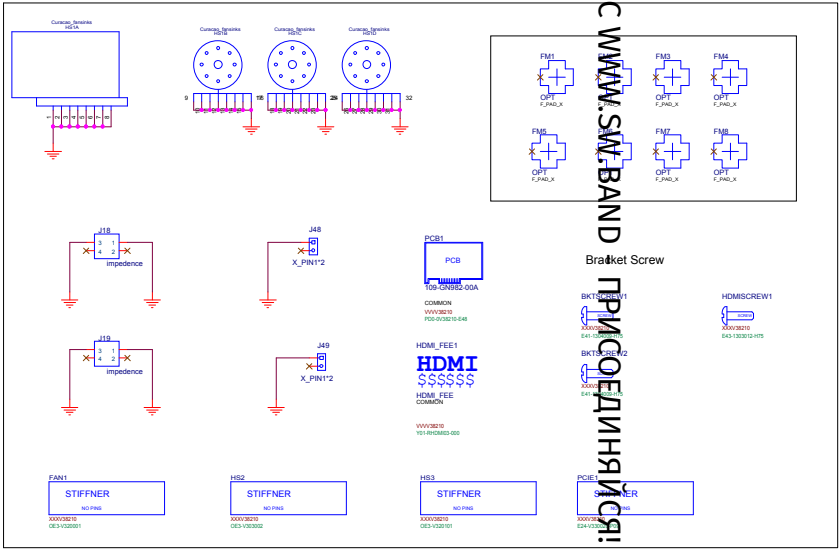
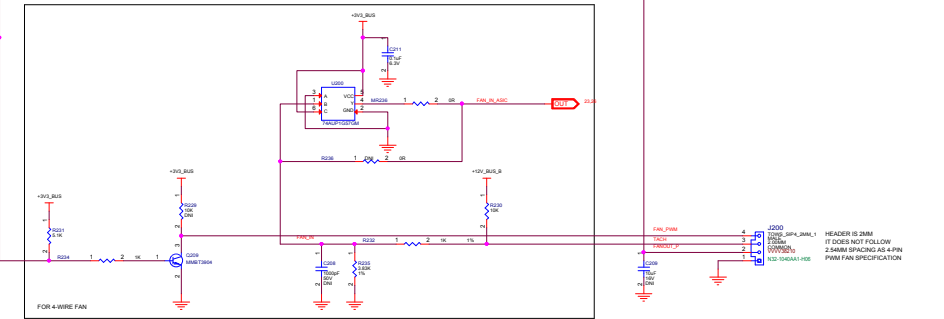
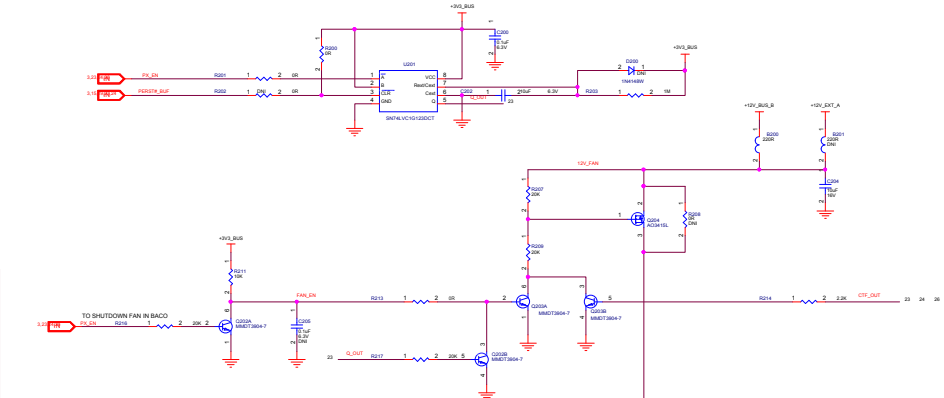
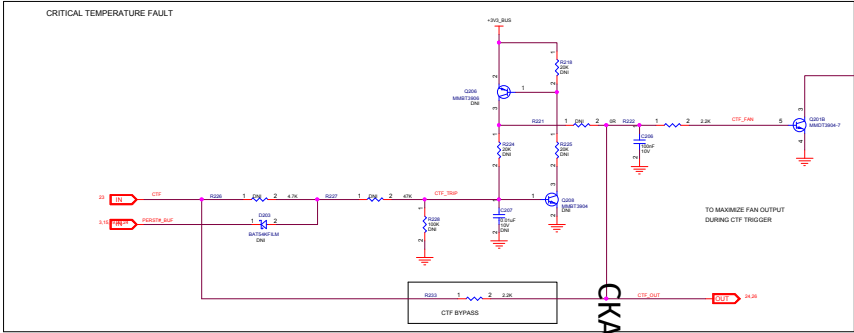
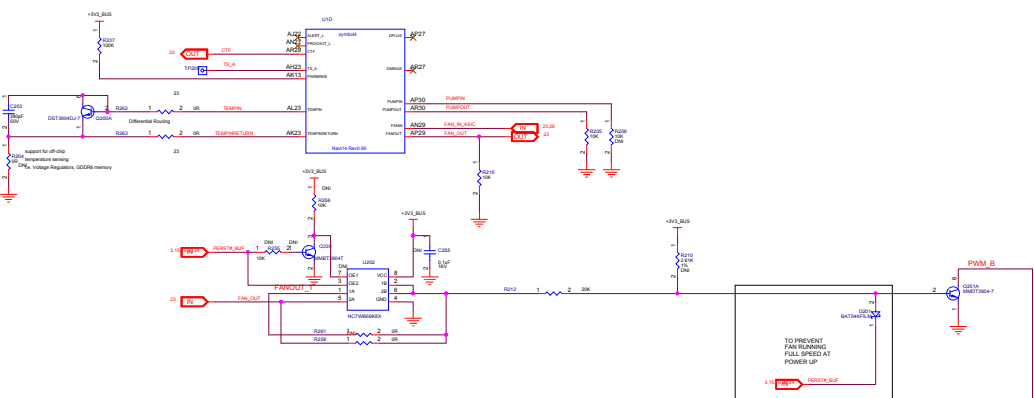
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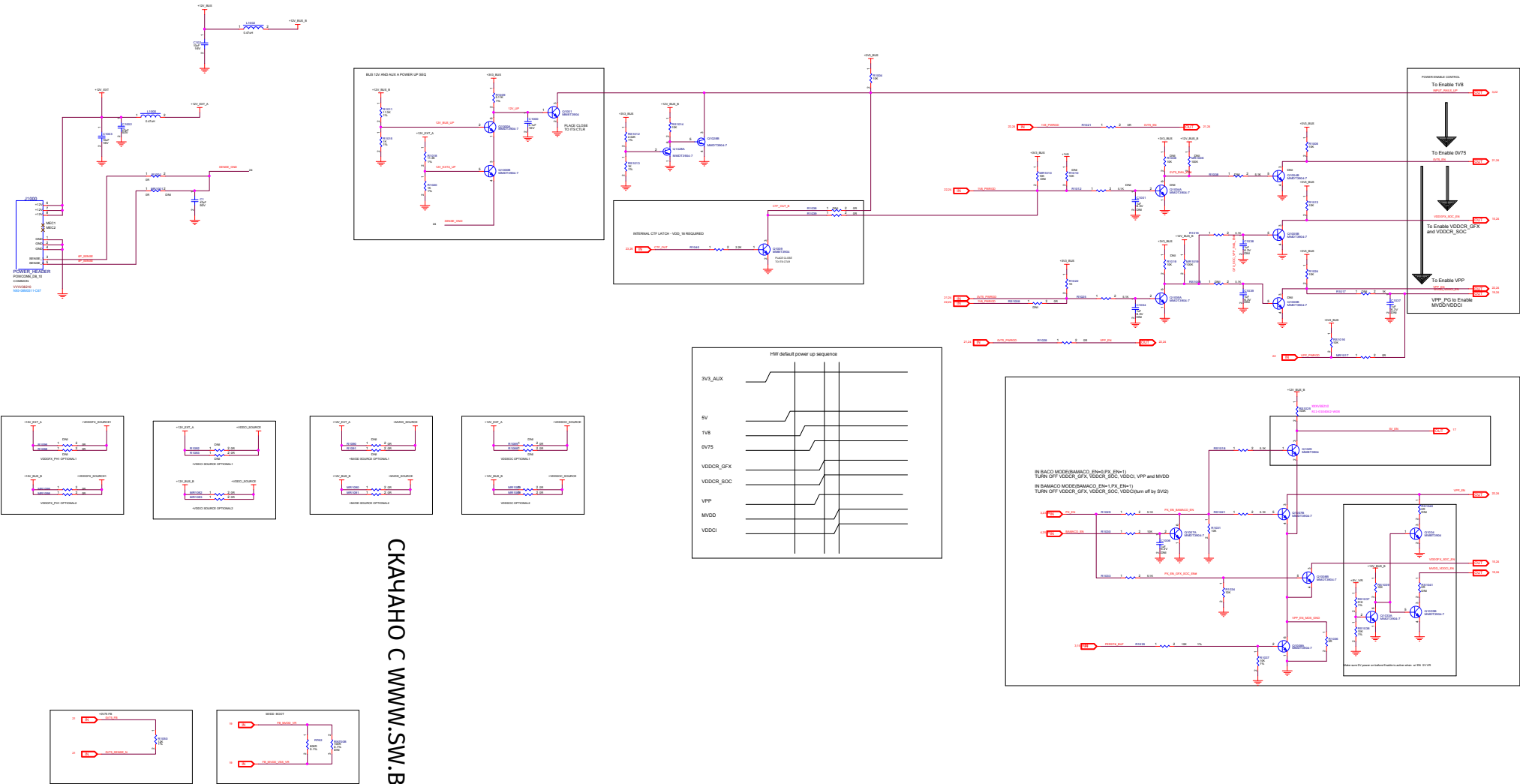
LDO - 1V8/VPP/5V



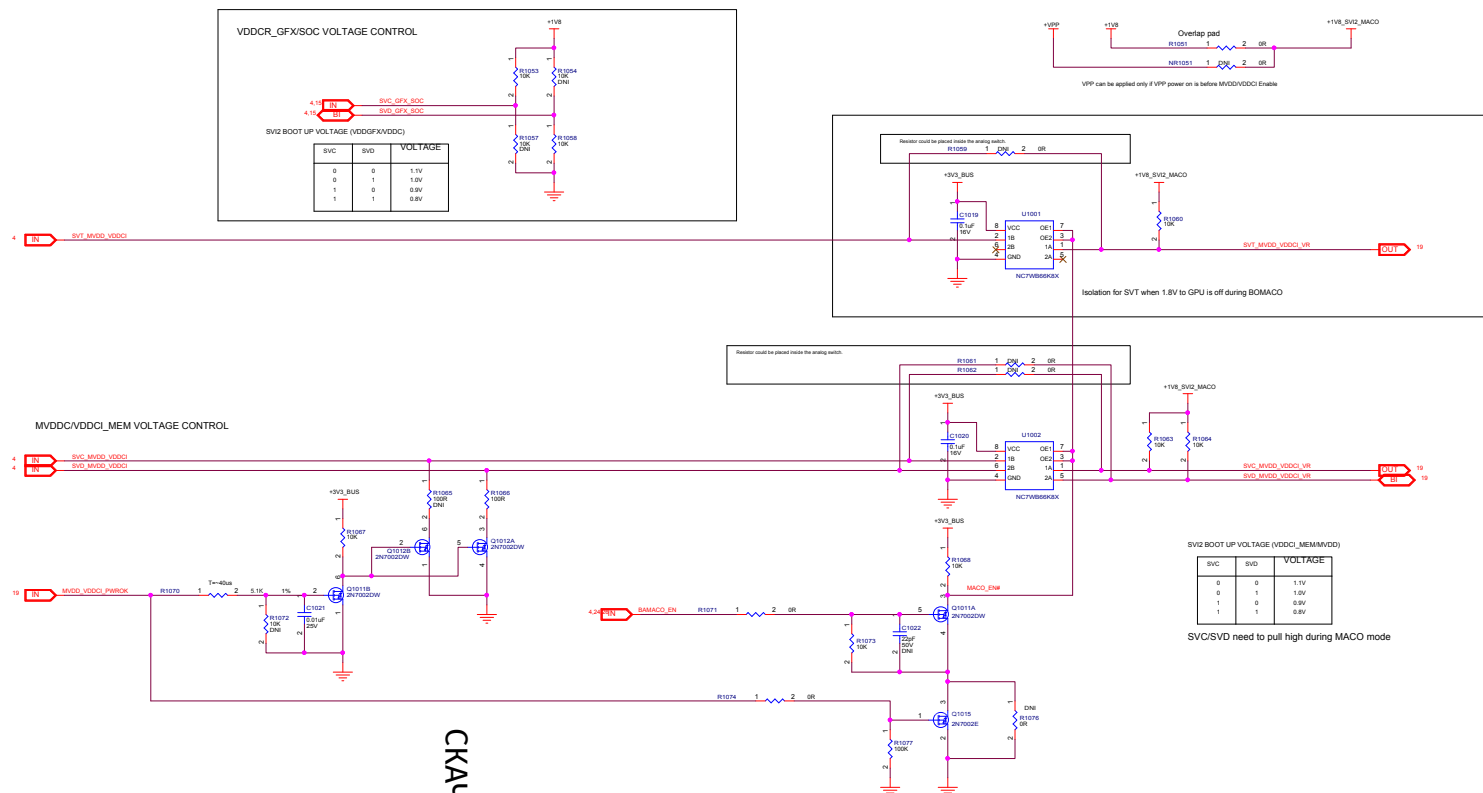
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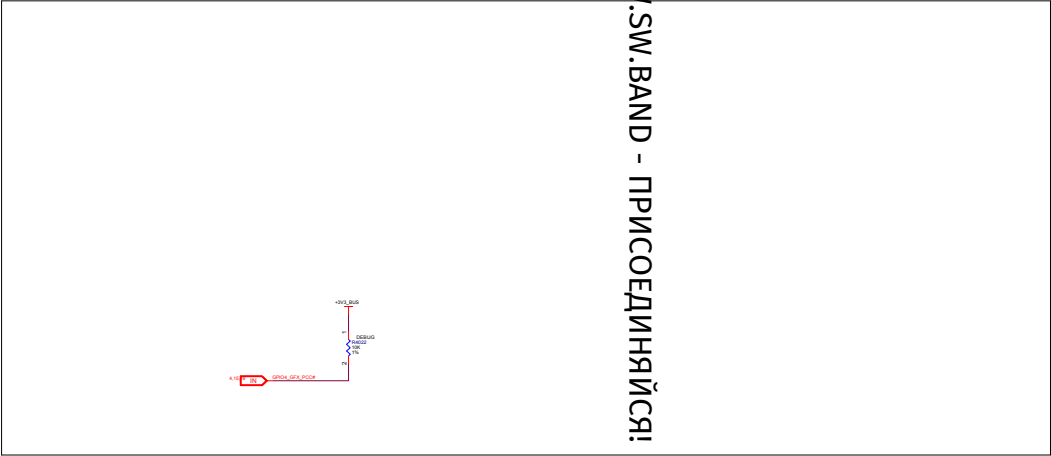
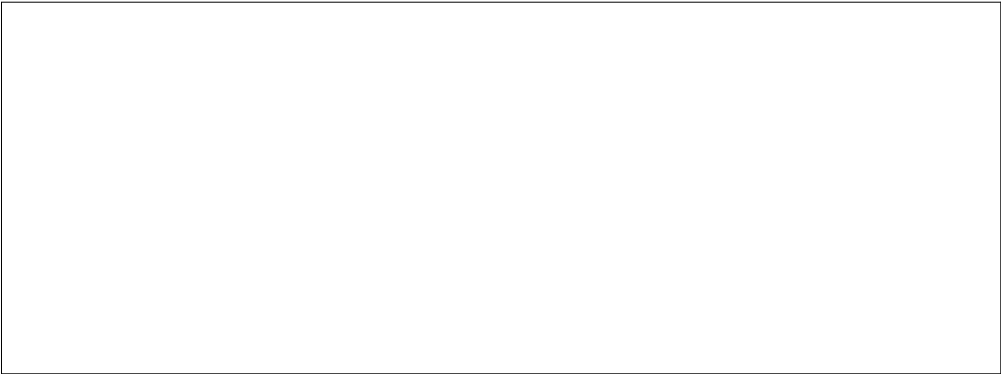
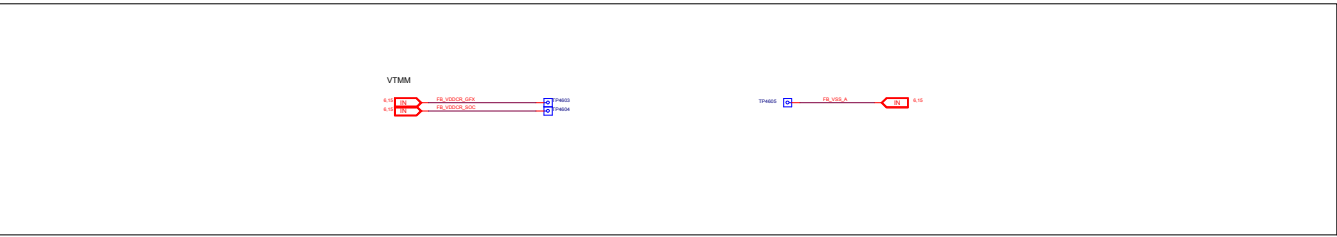
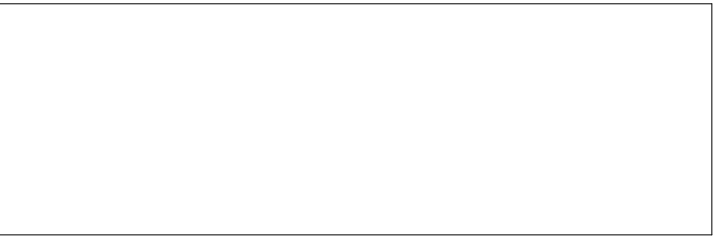
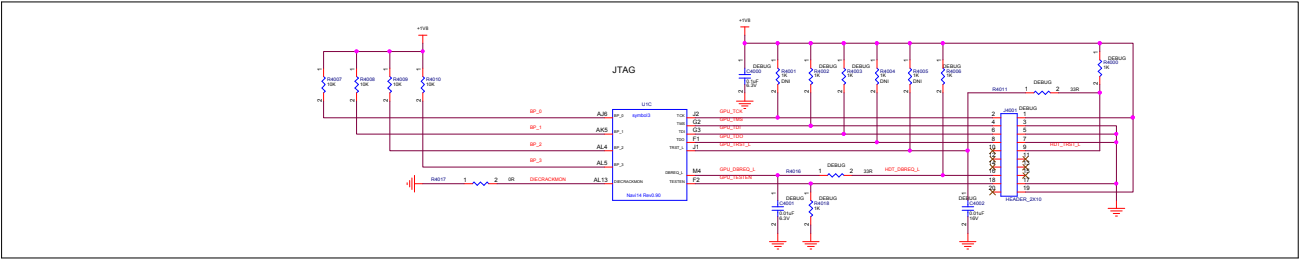




СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!



СКАЧАНО С WWW.SV.BAND - ПРИСОЕДИНЯЙСЯ!



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AMD



TITLE: NAVI14 XT GDDR6 x16 DT BOARD

DOCUMENT NUMBER: 105-D33200-00B

DATE: Mon Apr 22 08:33:14 2019

SHEET NUMBER: 27 OF 27

REV: B

REVISION HISTORY

ENGINEER: Billy Deng

NOTES: NOTE

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AMD-PLATFORM HARDWARE ENG
No.609,HUANKE ROAD
SHANGHAI,CHINA 201203

SCH Rev	PCB Rev	Date	REVISION DESCRIPTION
1.00	00A	08/09/2018	Initial schematic
1.00	00B	12/28/2018	change all 4172010501G parts to 4172010500G
		04/16/2019	ADD D1752 D1762 D1762 D1762 ADD R1705 AND CHANGE R1702 LOCAL SENSE POINT ADD R1762 R1763 MR1702 MR1703 and reassign gfa phase1-3 input source ADD R4653 R4654 R4655 C4650 Change T01: 0.53uH520118000G R749: 800R316050000G NR1704: 30K3160300200G R743: 18K3160180200G C747:470pF+4170047100G C748:220pF+4 170521100G

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REVISION HISTORY

Date: Monday, September 09, 2019

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