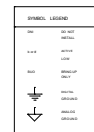
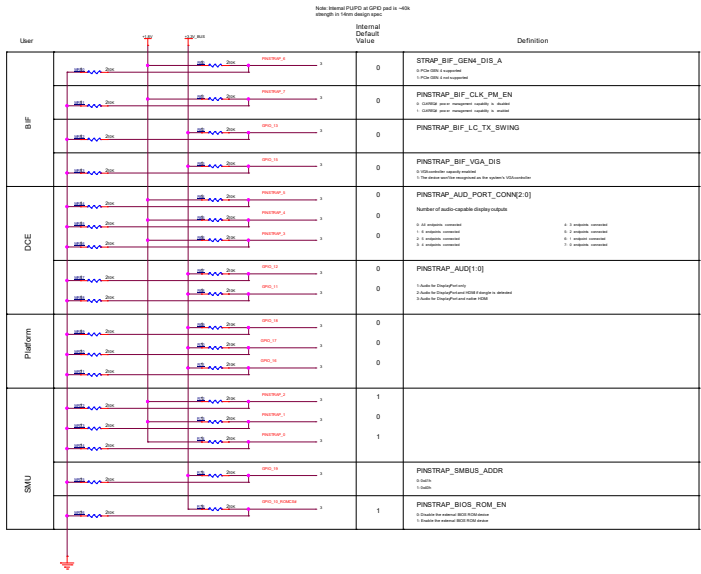
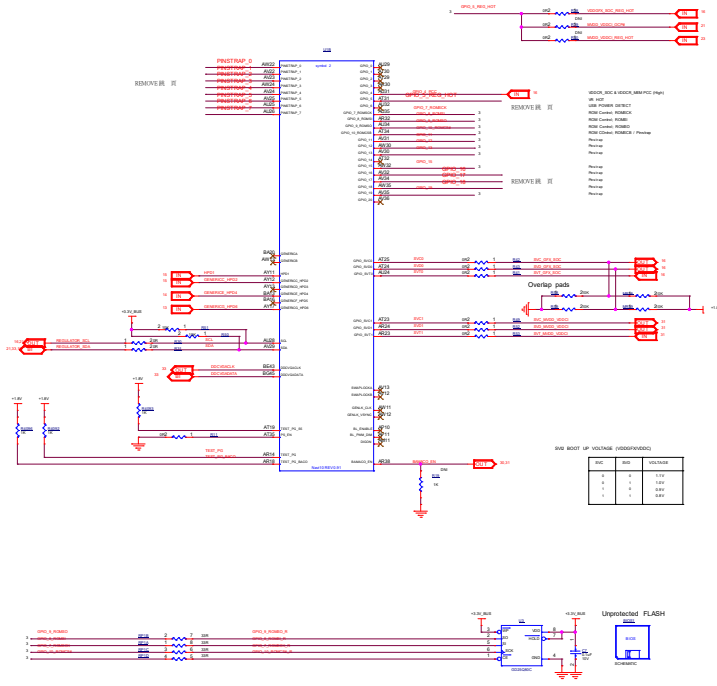
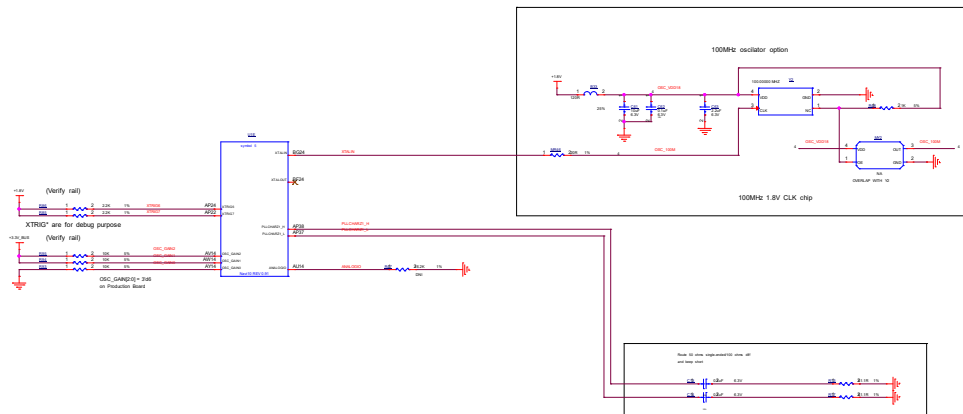


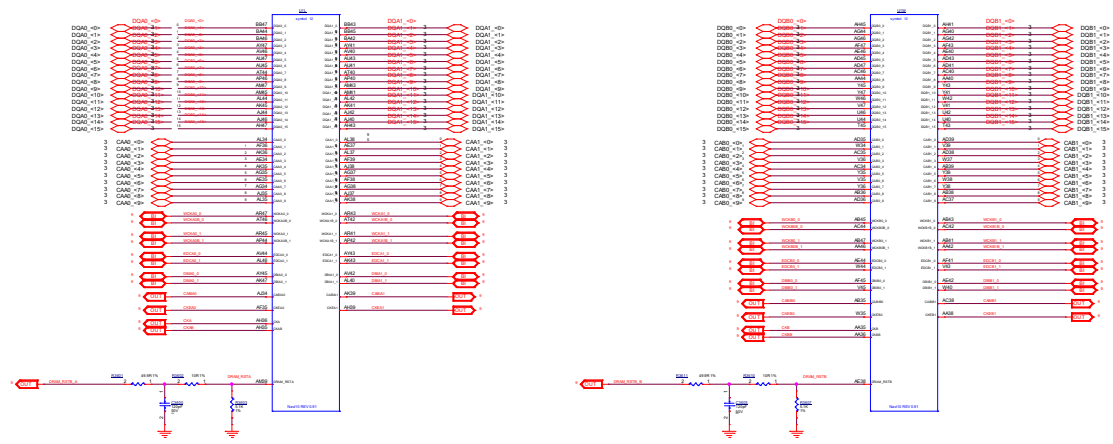
TABLE OF CONTENTS

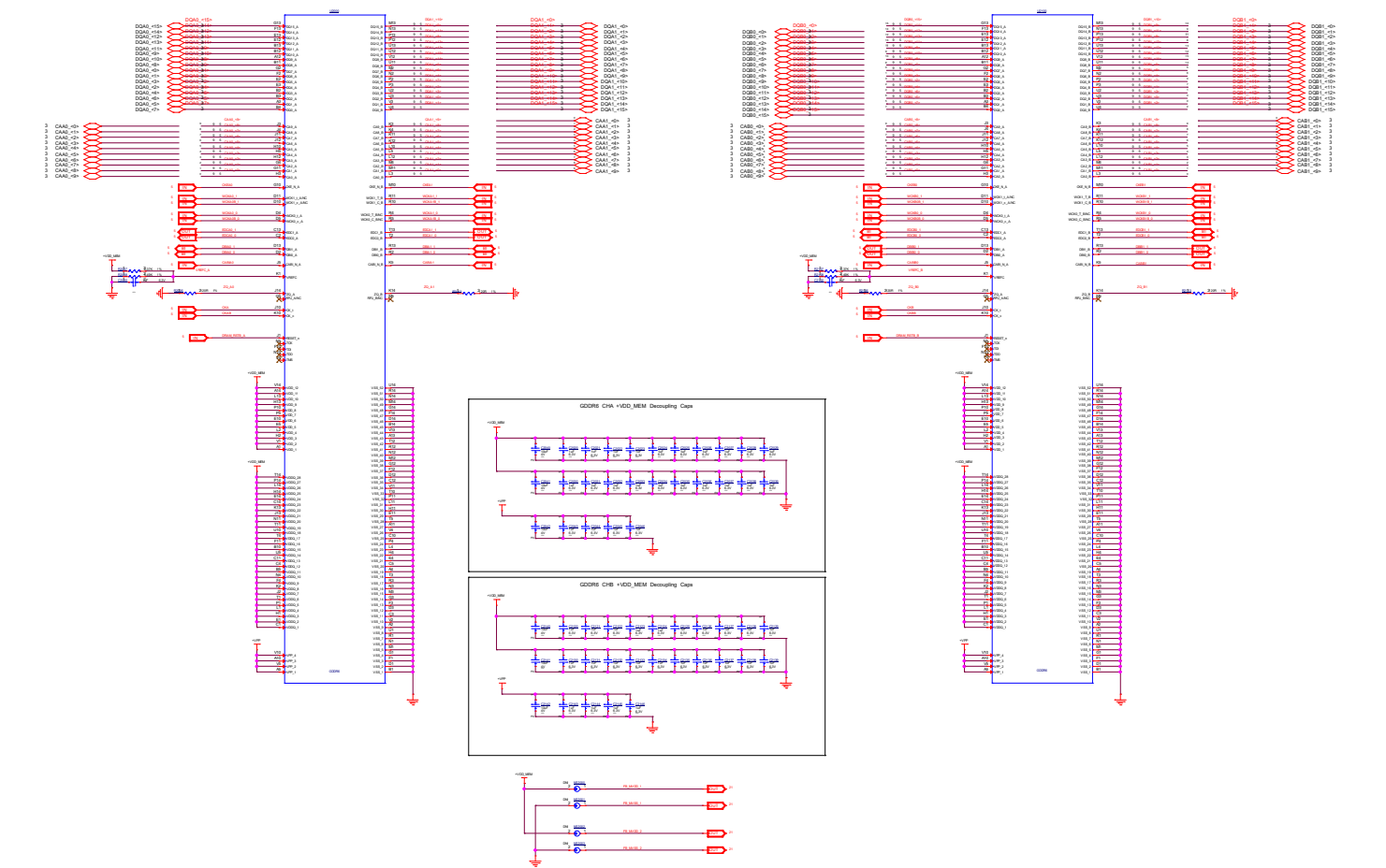
SHEET NO.	SHEET NAME	SHEET NO.	SHEET NAME
1	TOC	26	SMALL FAN REGULATORS
2	NAUTO PCW Interface	27	NAUTO DECAPS
3	NAUTO GPCH	28	NAUTO POWER
4	NAUTO STAL	29	NAUTO GND
5	NAUTO MEM CHAB	30	POWER MANAGEMENT
6	NAUTO MEM CHCD	31	SIG & SUBNOISE
7	NAUTO MEM CHEP	32	MECHANICAL & THERMAL
8	NAUTO MEM CHSH	33	DEBUG
9	GDORM MEM CHAB	34	BLOCK DIAGRAM
10	GDORM MEM CHCD	35	REVISION HISTORY
11	GDORM MEM CHEP		
12	GDORM MEM CHSH		
13	NAUTO TMCPA - GP		
14	NAUTO TMCPA - HDMI		
15	NAUTO TMCPA - DP DP		
16	GPX & SOC CONTROLLER		
17	VEDOR_GFX PHASES 1 and 5		
18	VEDOR_GFX PHASES 1 and 4		
19	VEDOR_GFX PHASES 1 and 7		
20	VEDOR_GFX PHASES 6 and VEDOR_SOC		
21	MVED_VEDD0 CONTROLLER		
22	REG VEDD0		
23	REG VEDD0		
24	REG 0.75V		
25	REG 1.8V		

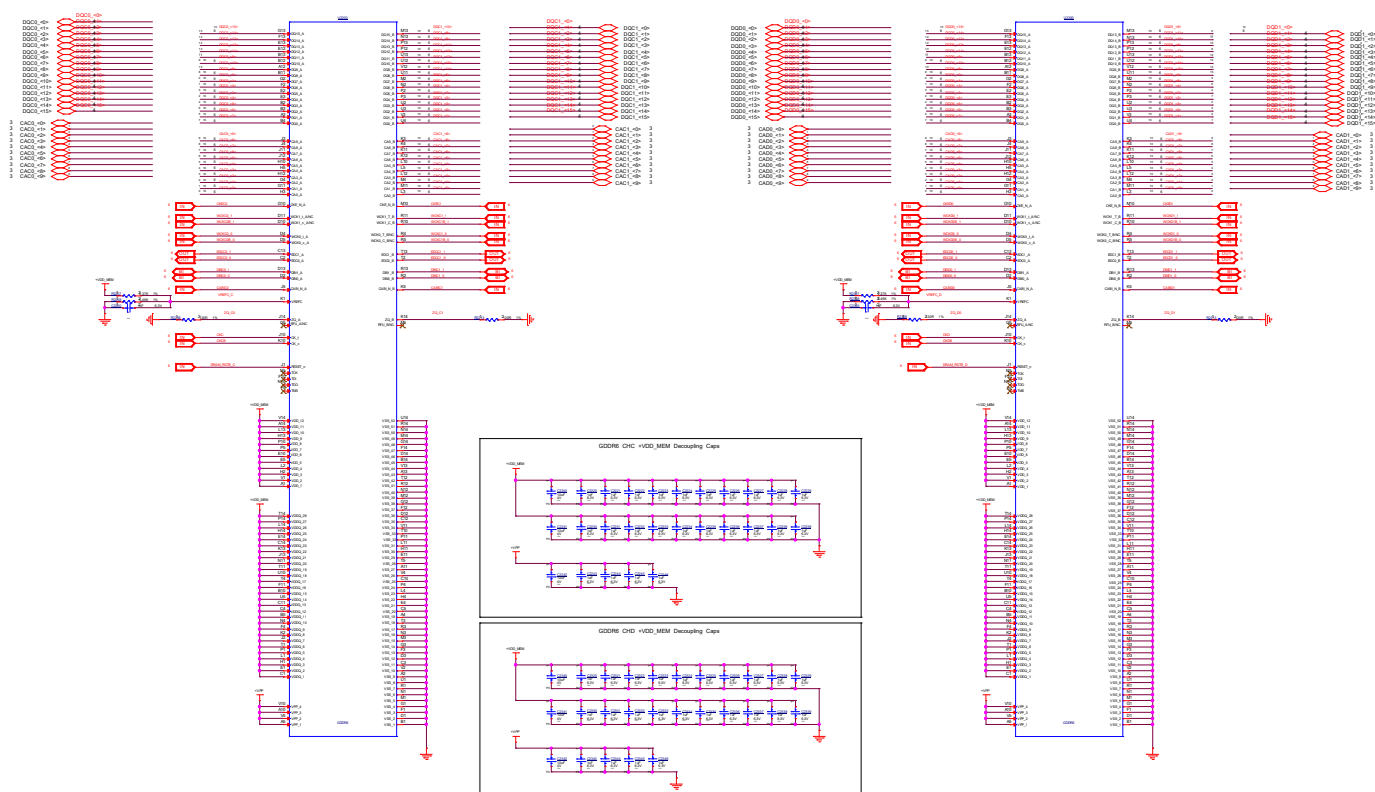


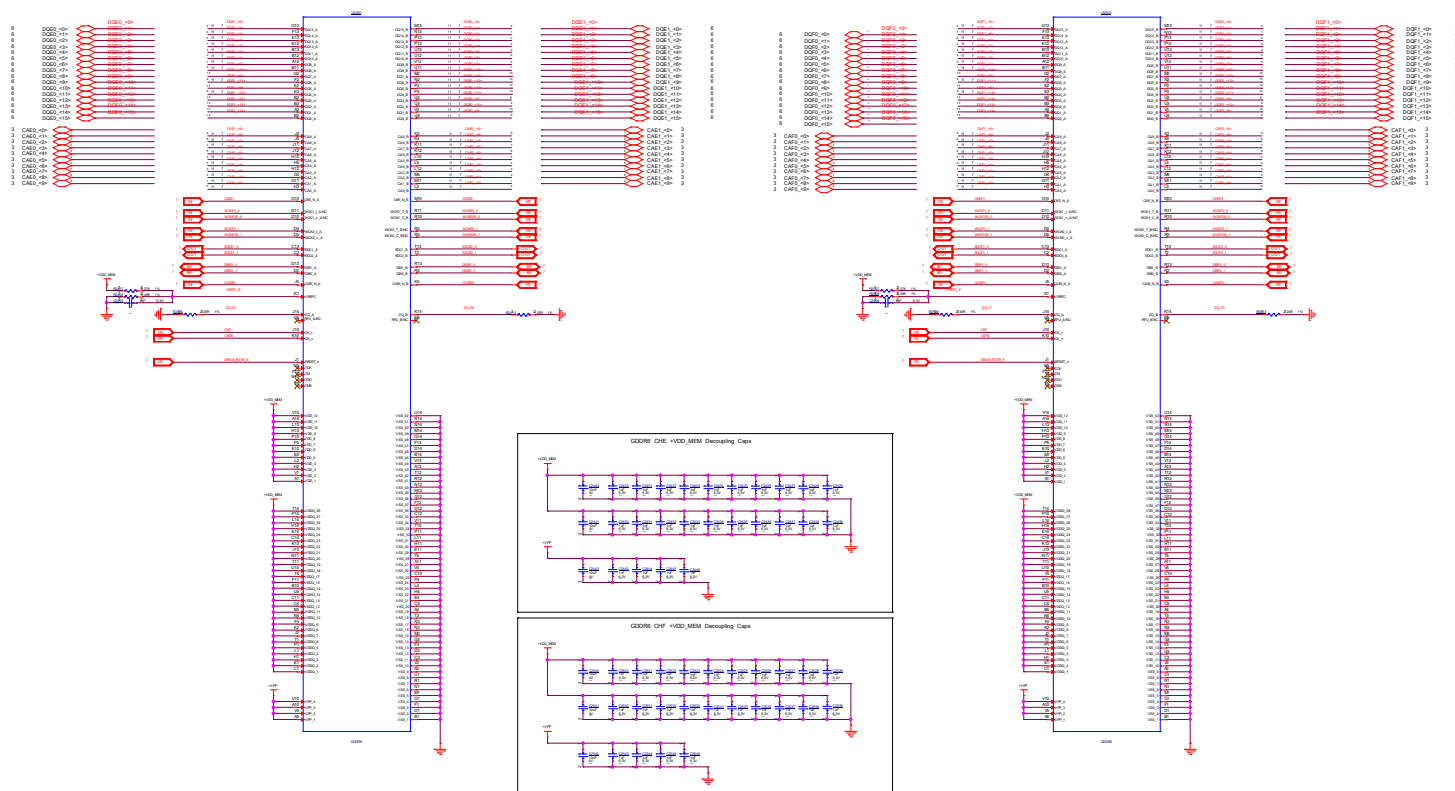


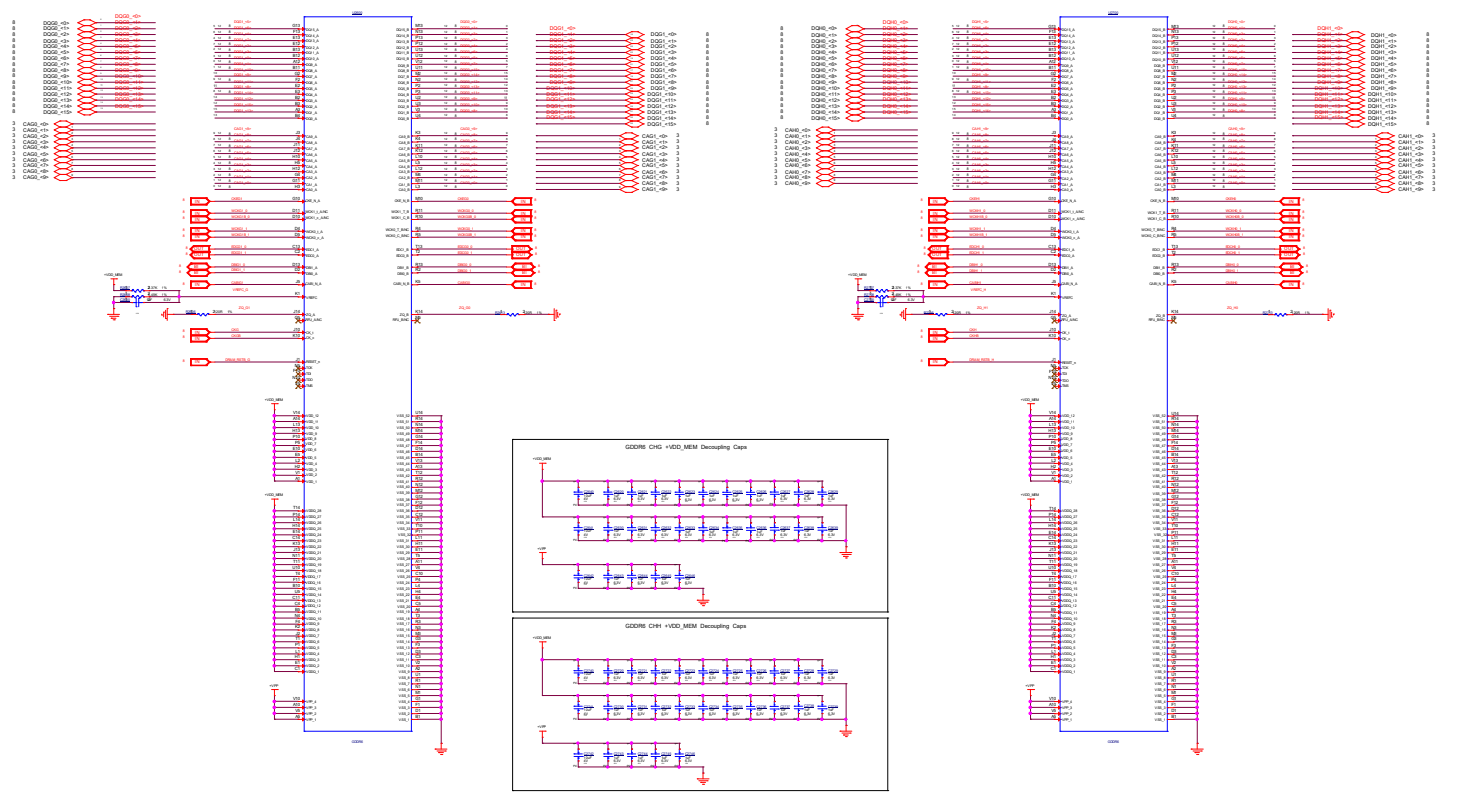


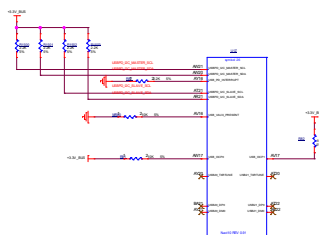




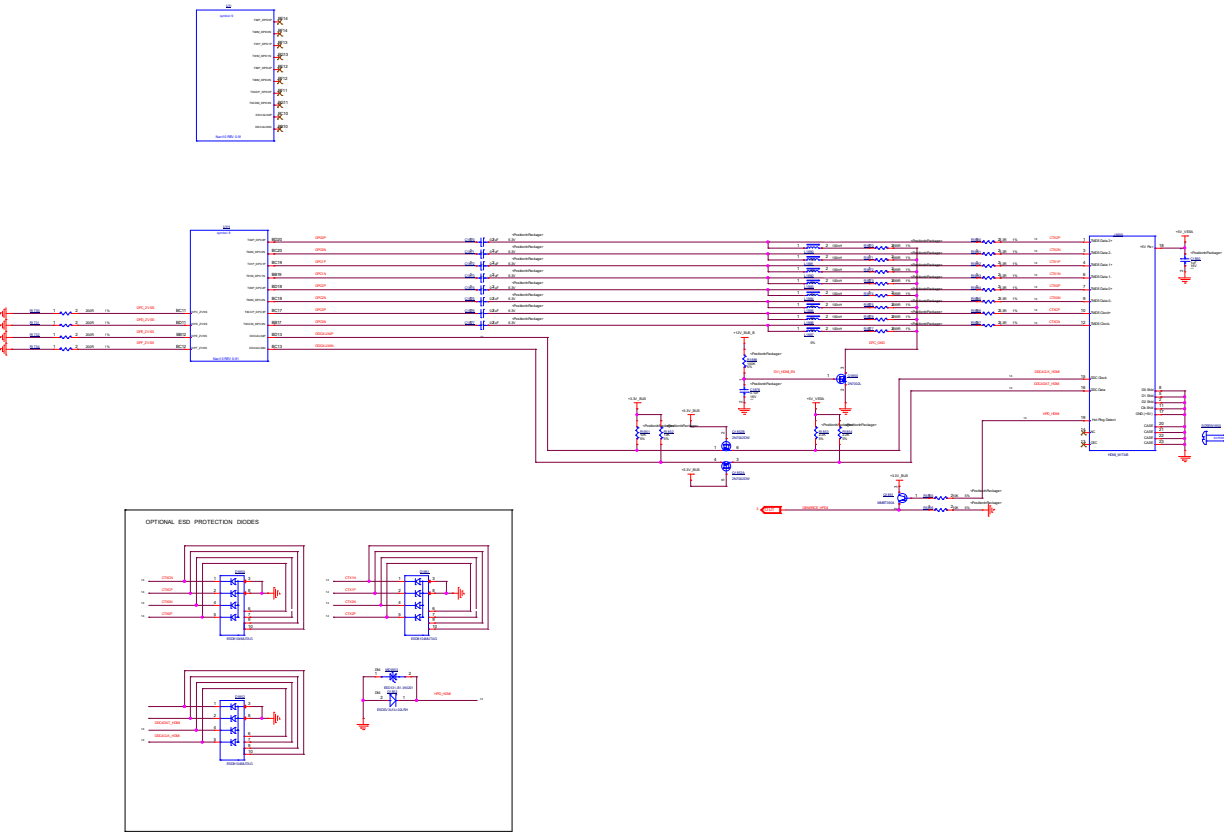




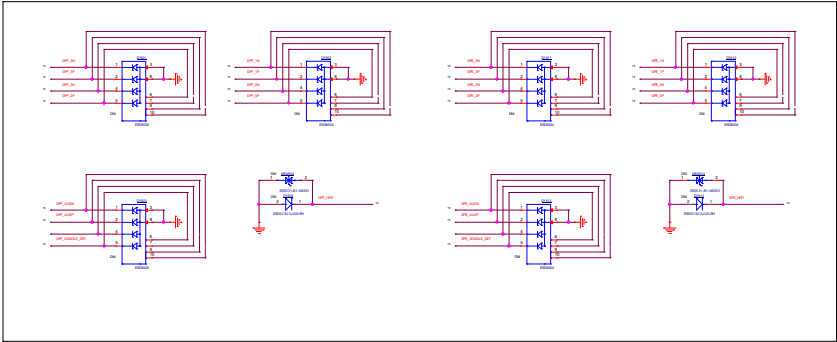
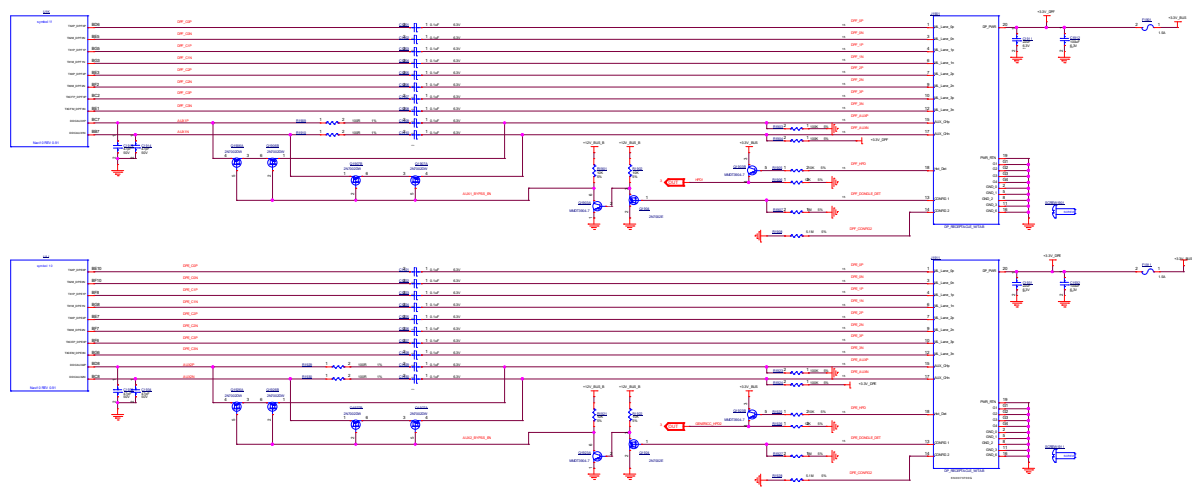




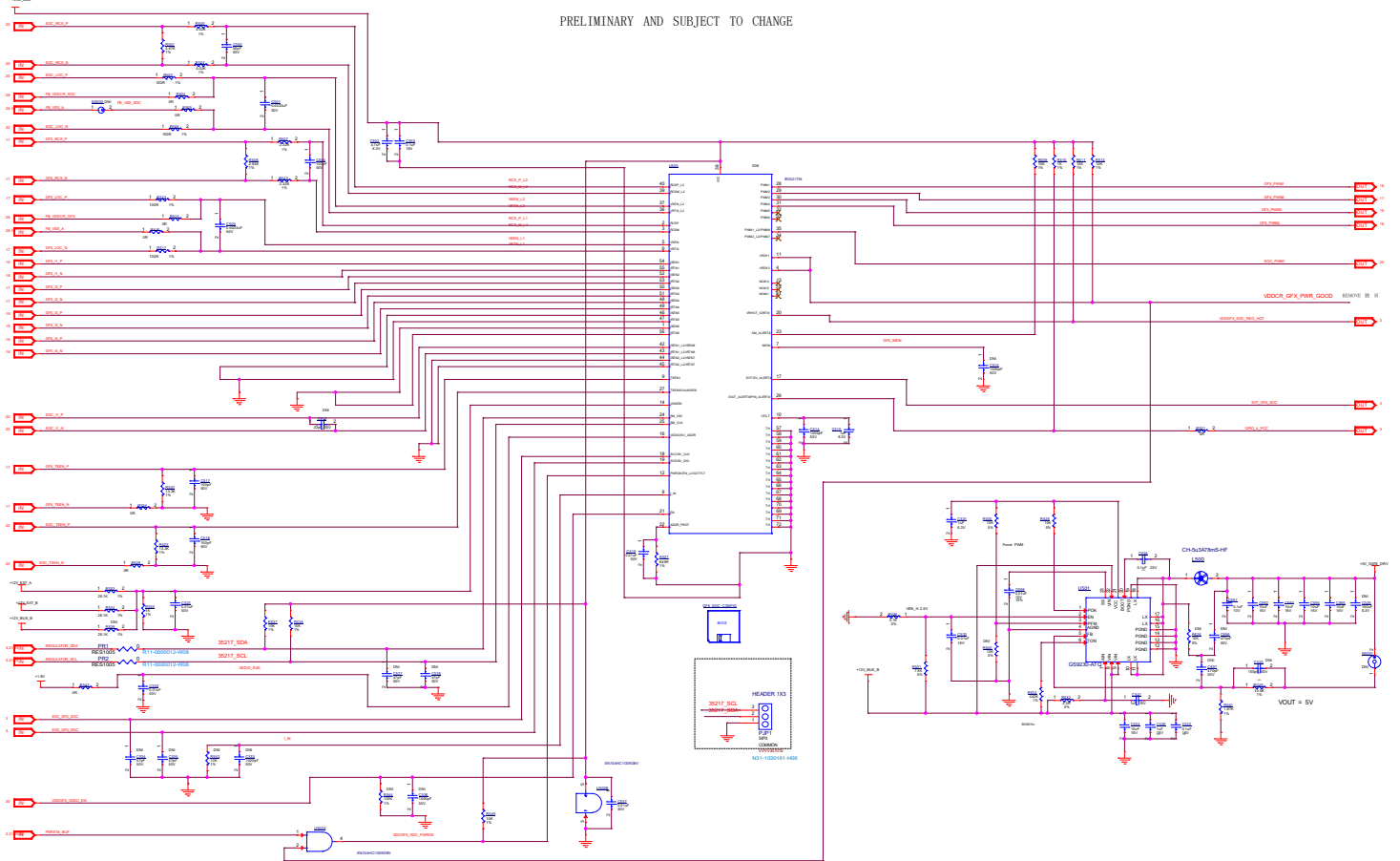
NAV110 TMDP C/D

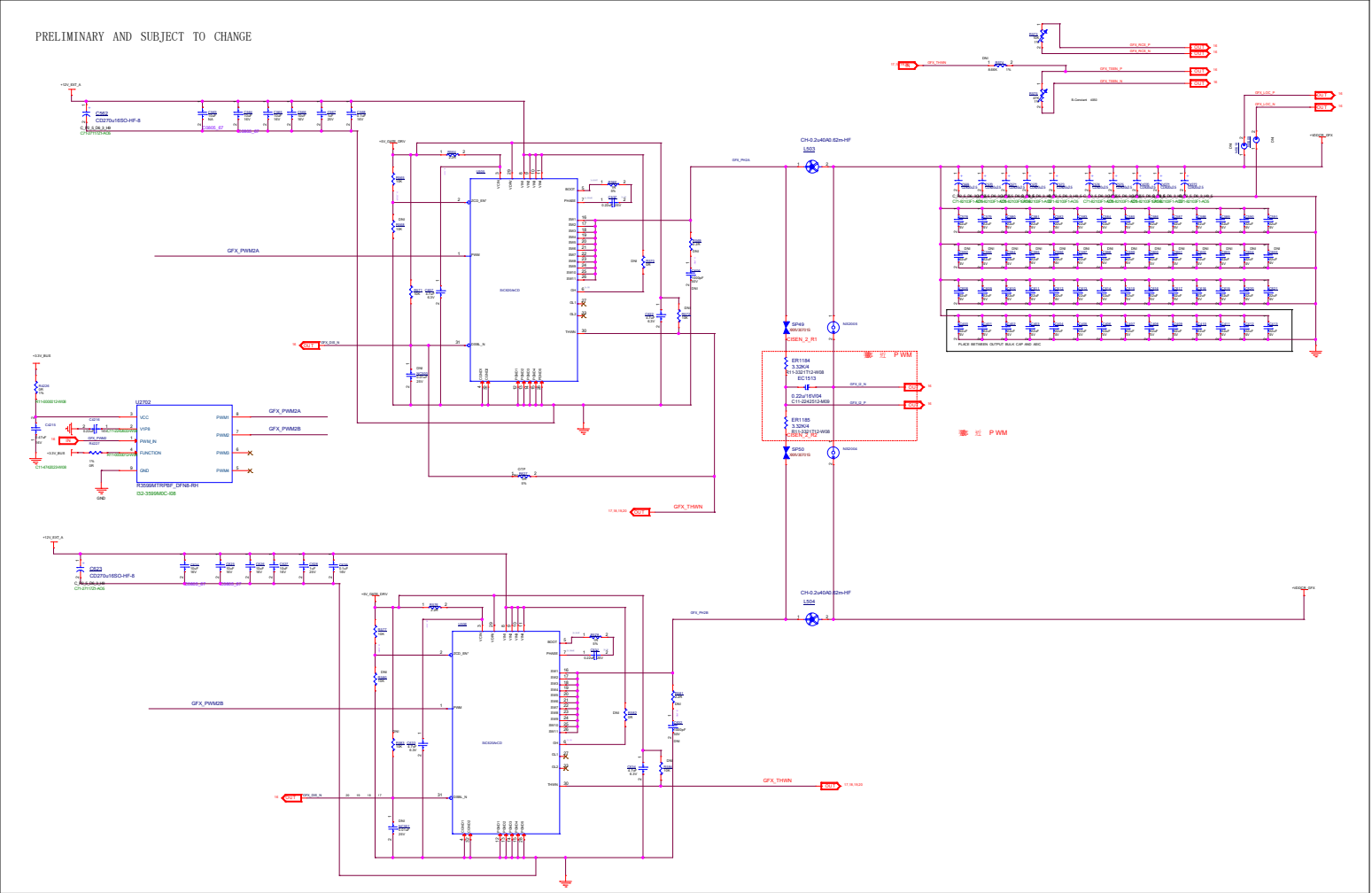


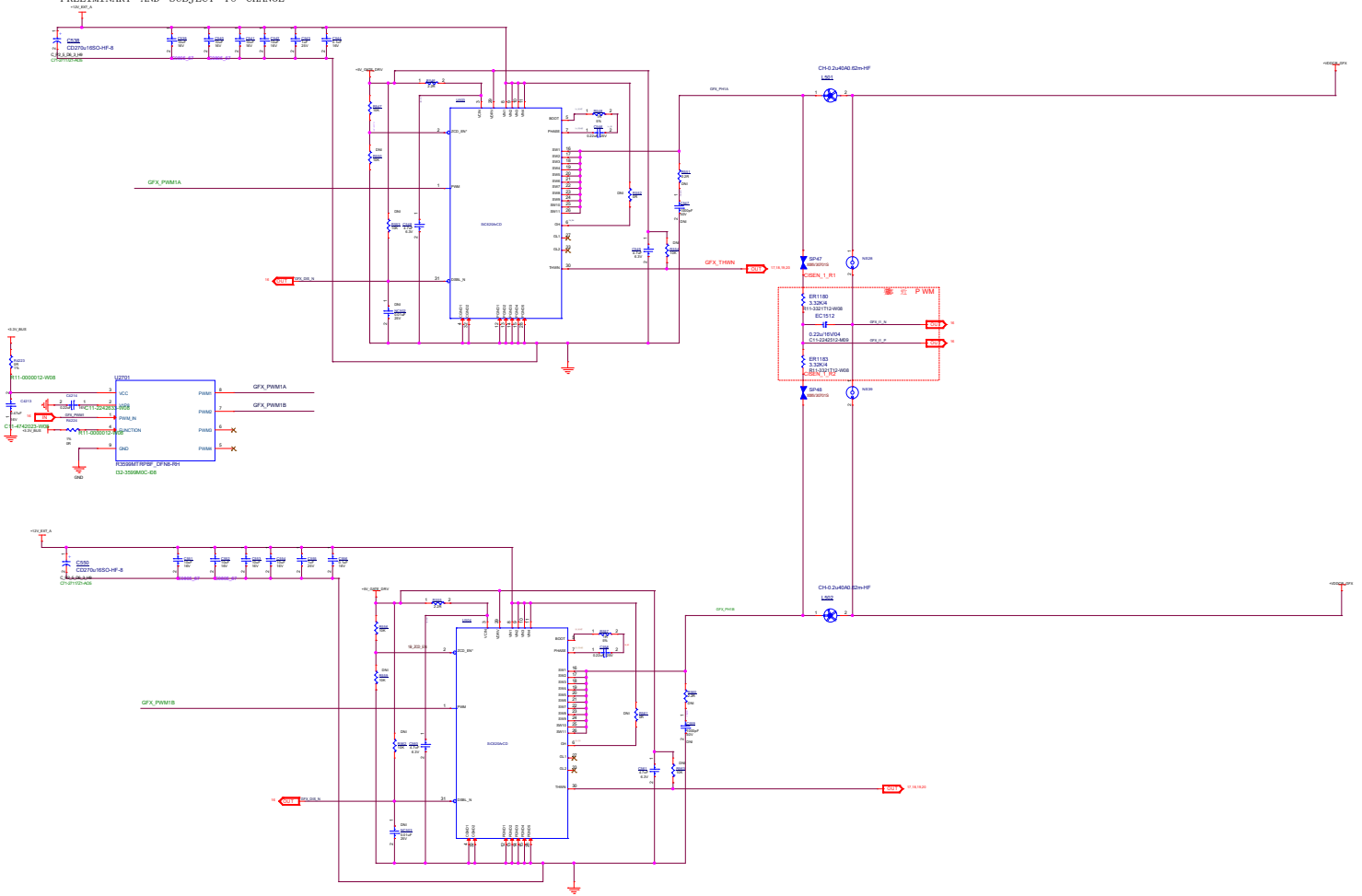
(9) NAVI10 TMDP E/F

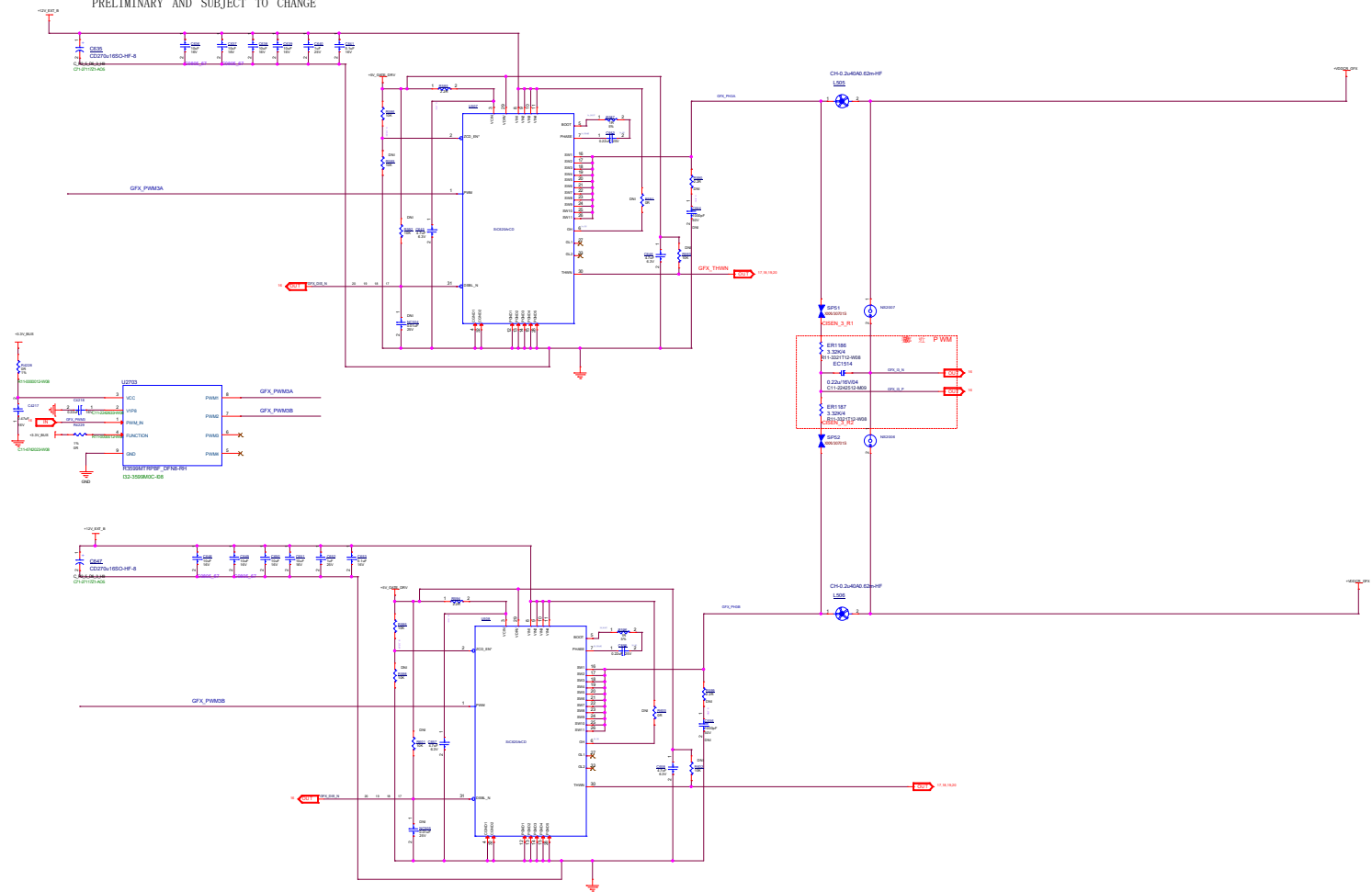


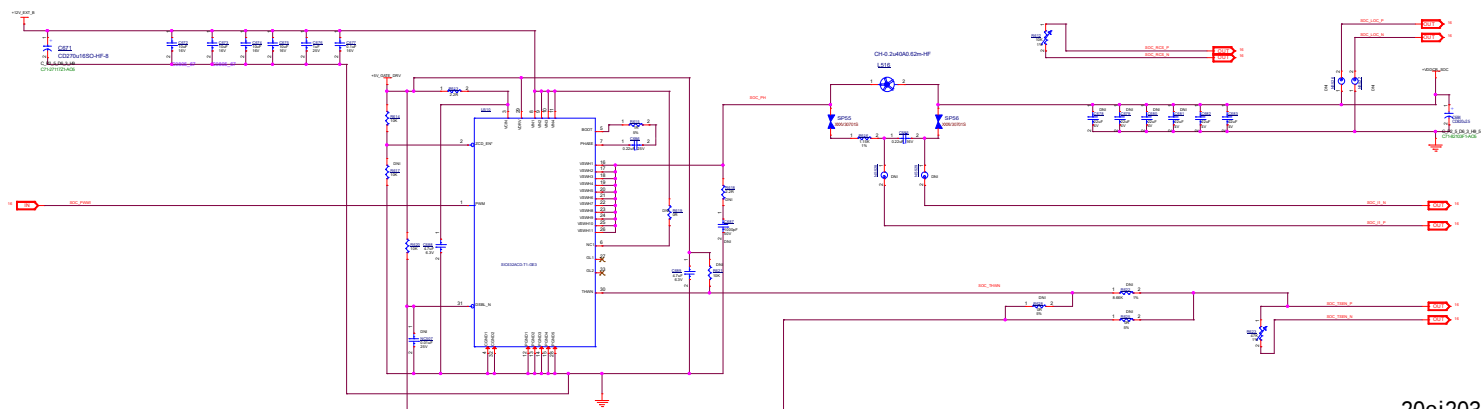
PRELIMINARY AND SUBJECT TO CHANGE



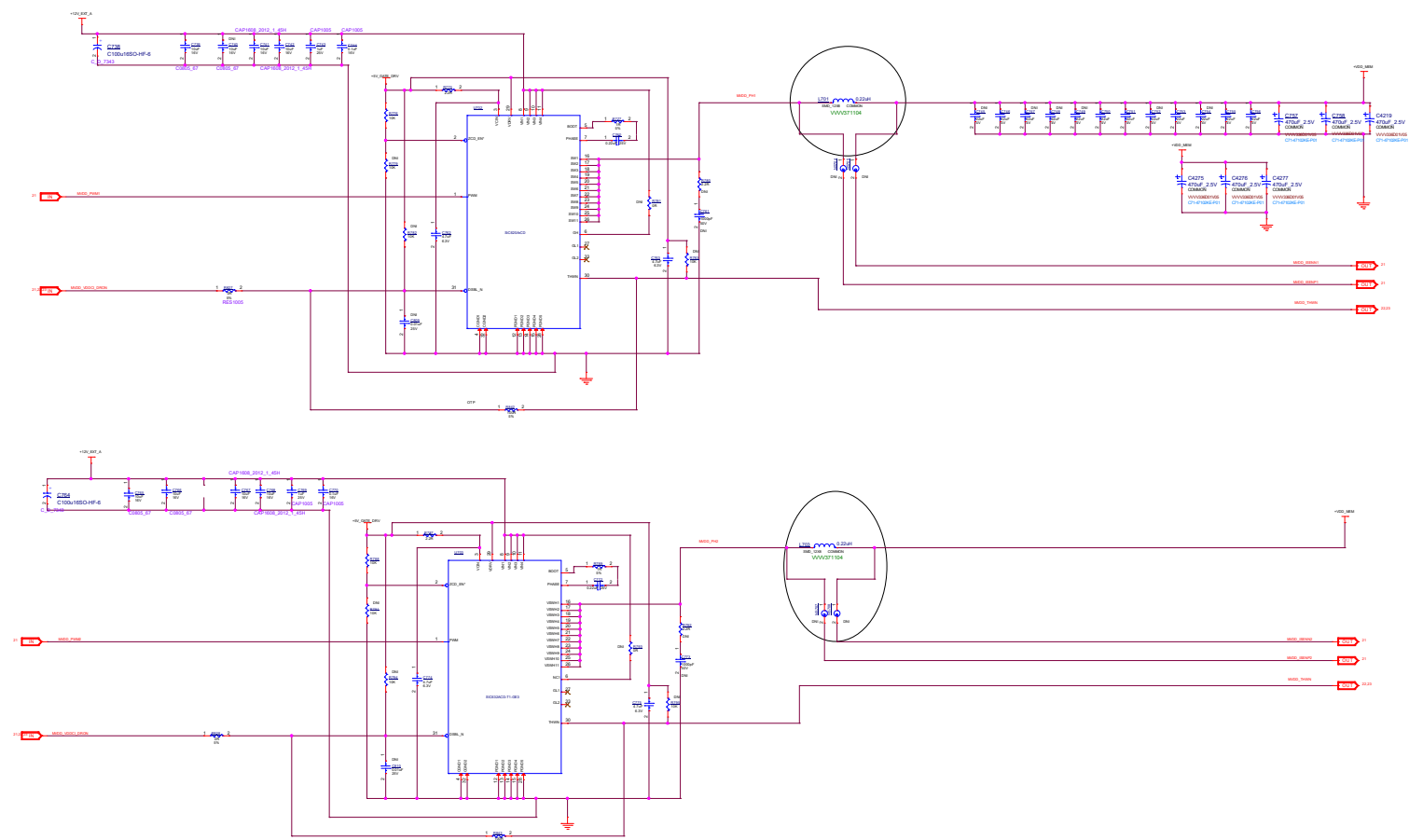




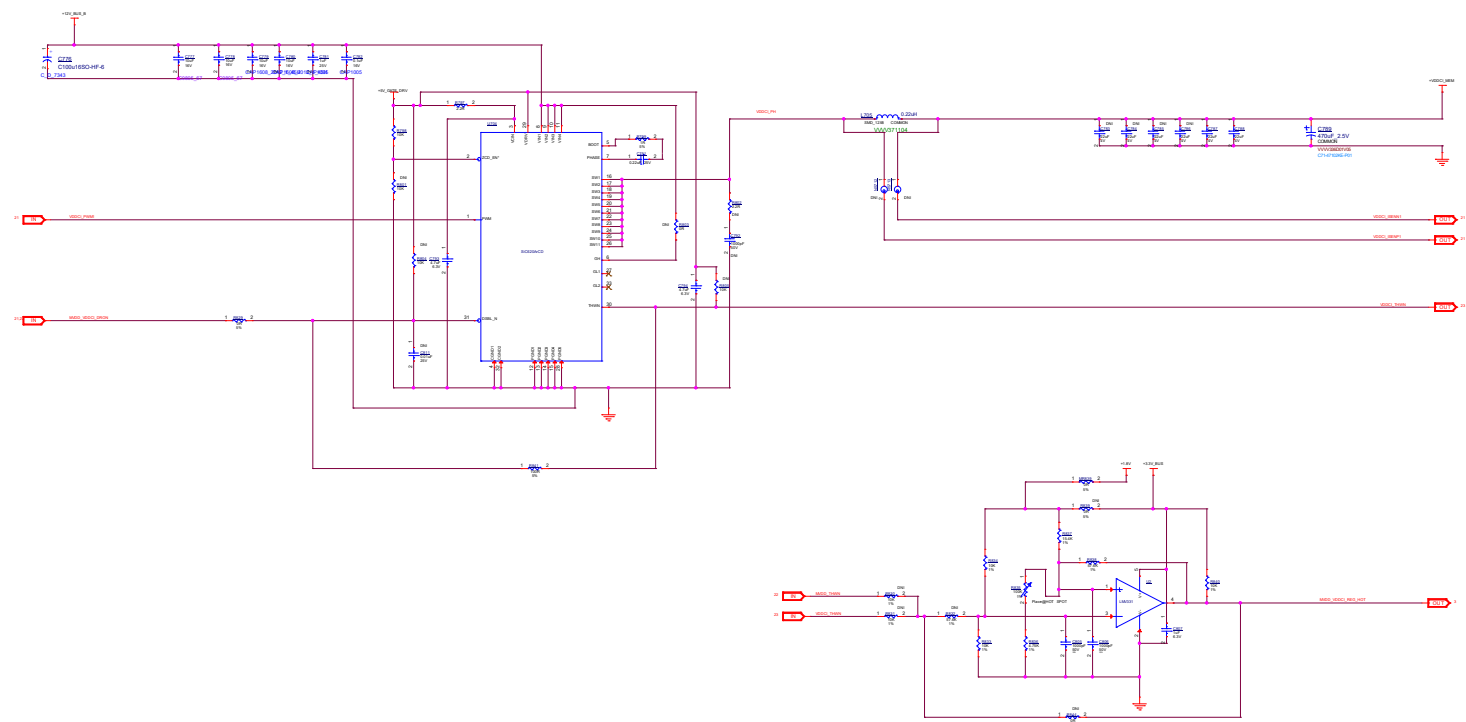


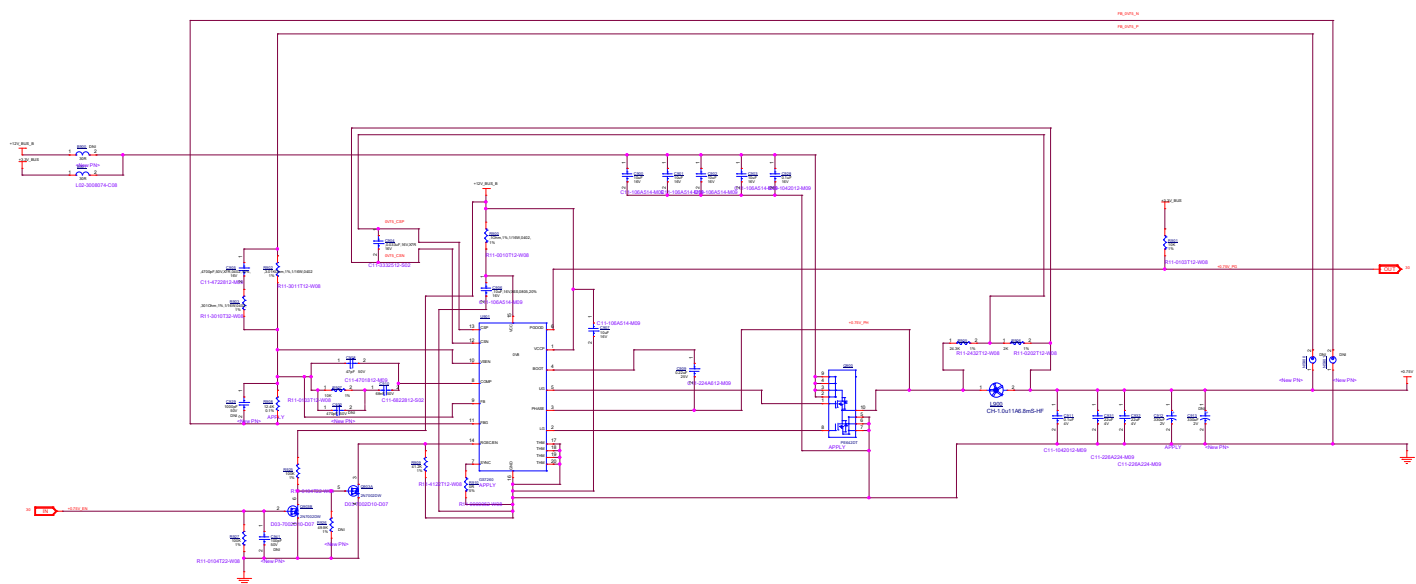


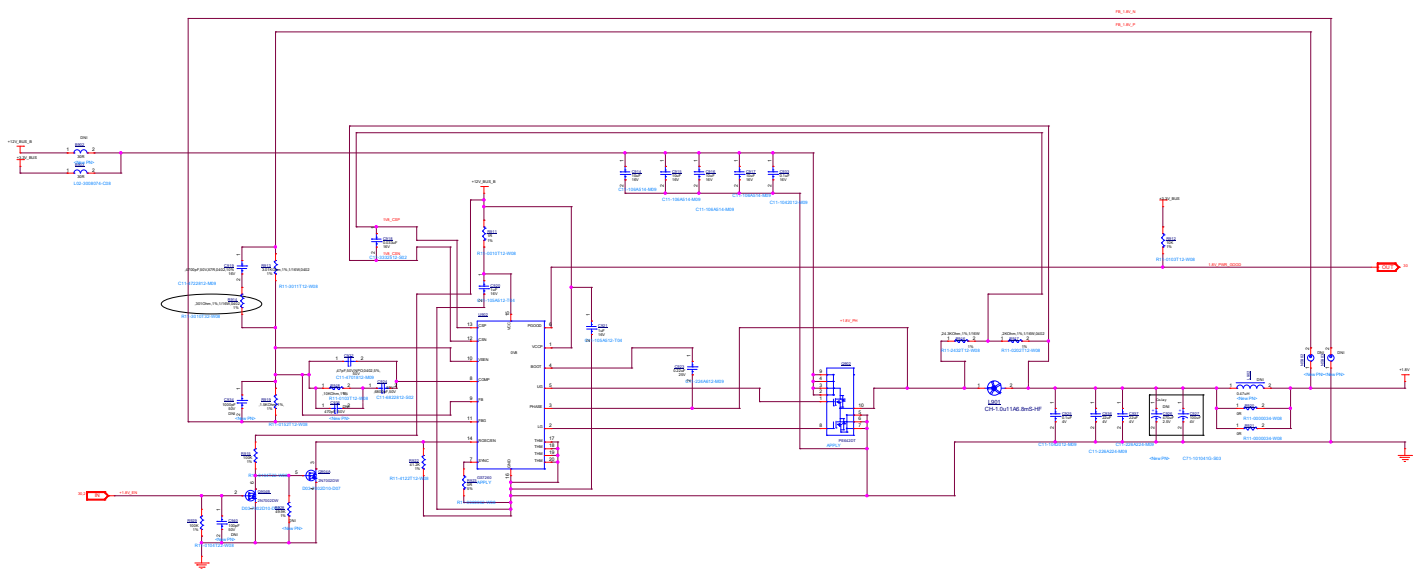
20ci203T

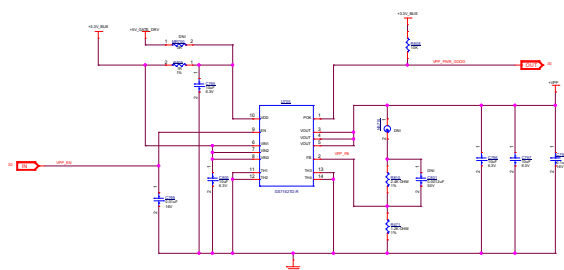
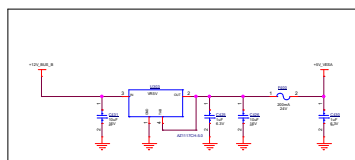
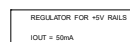


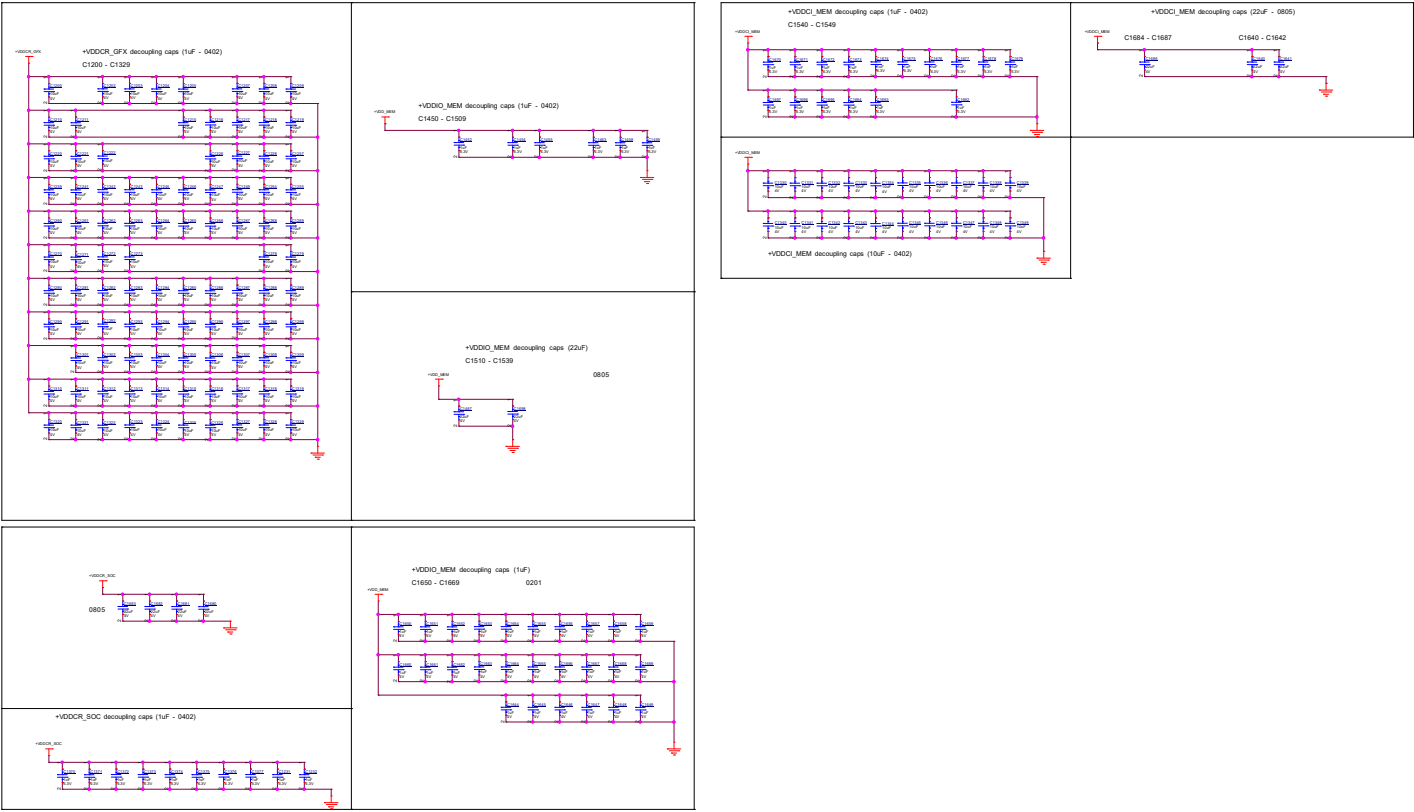
20ci203T

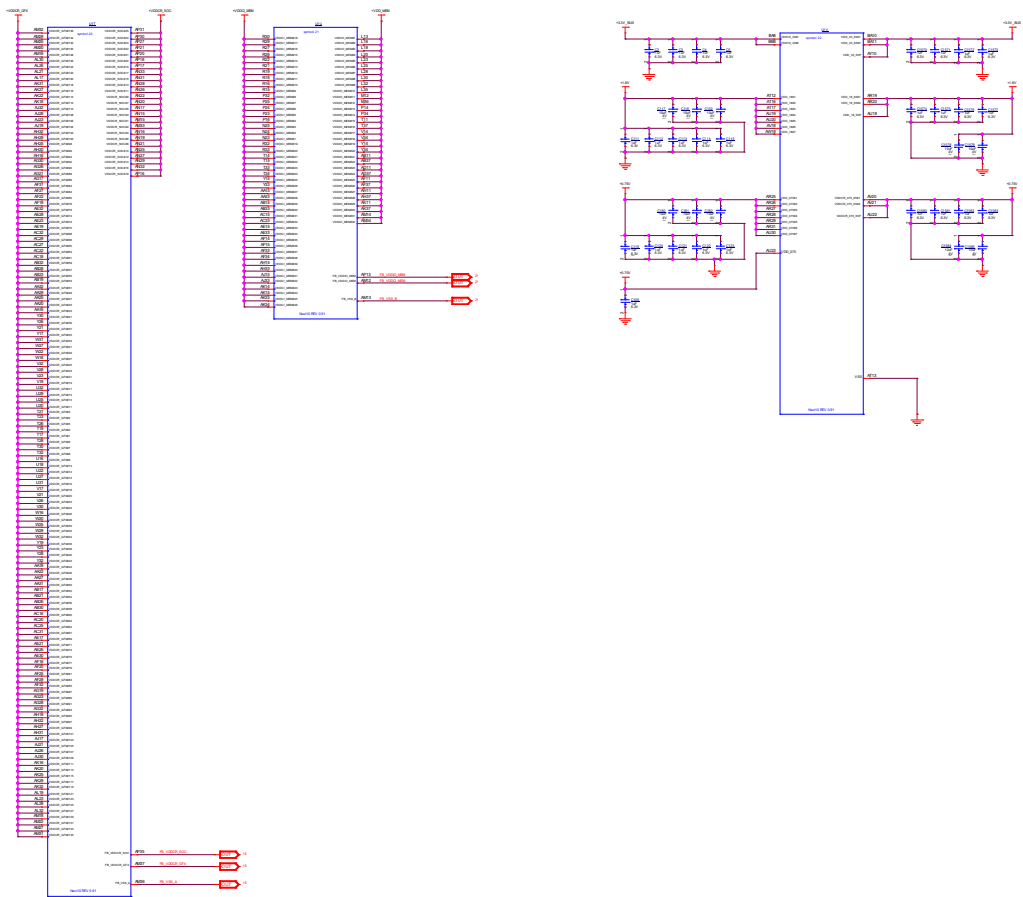


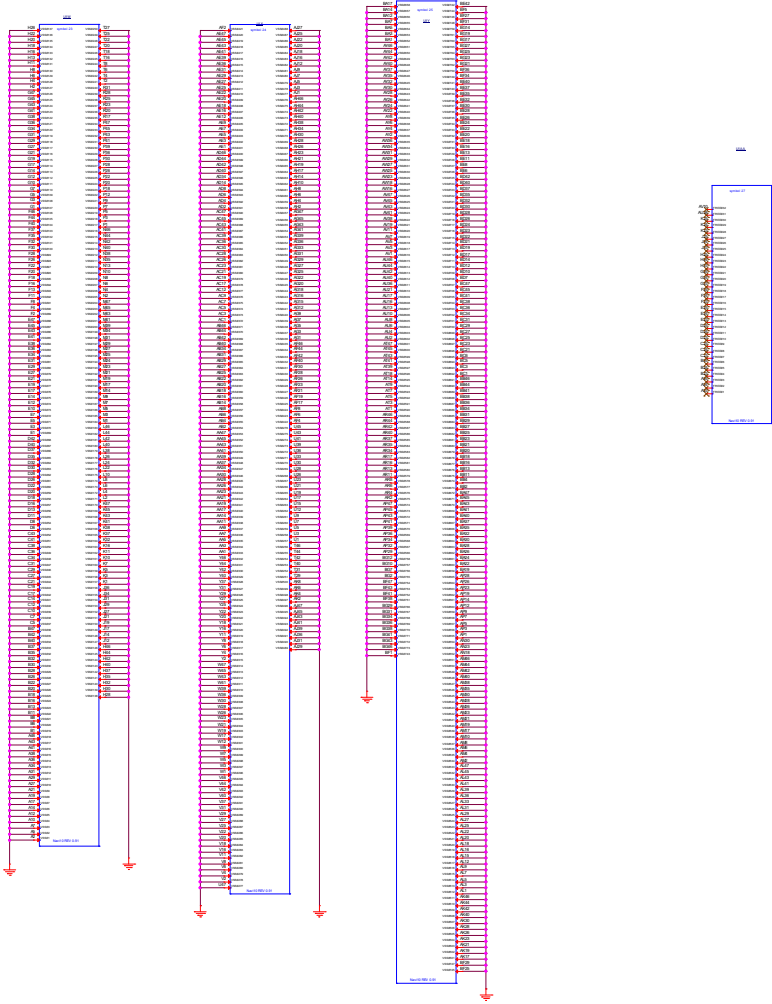


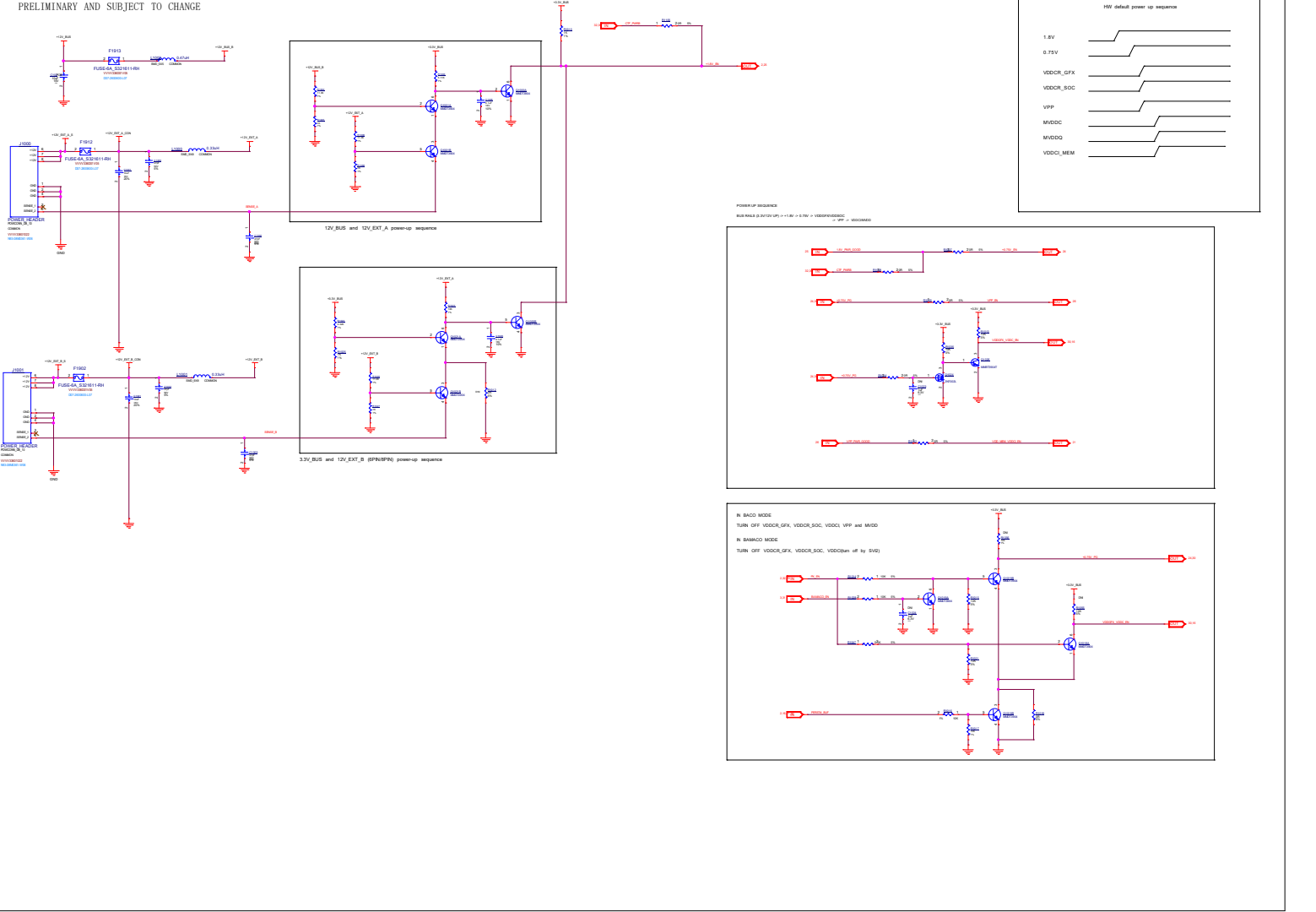


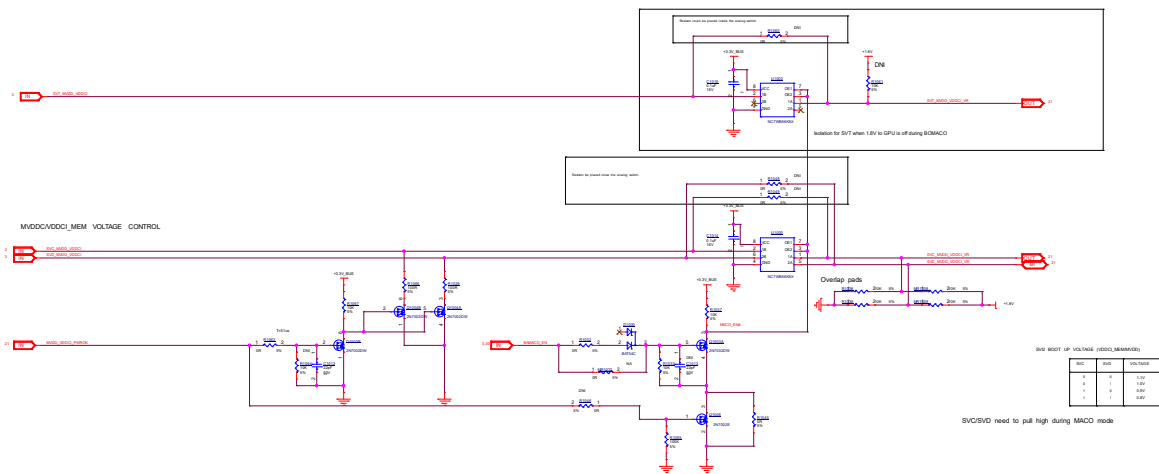


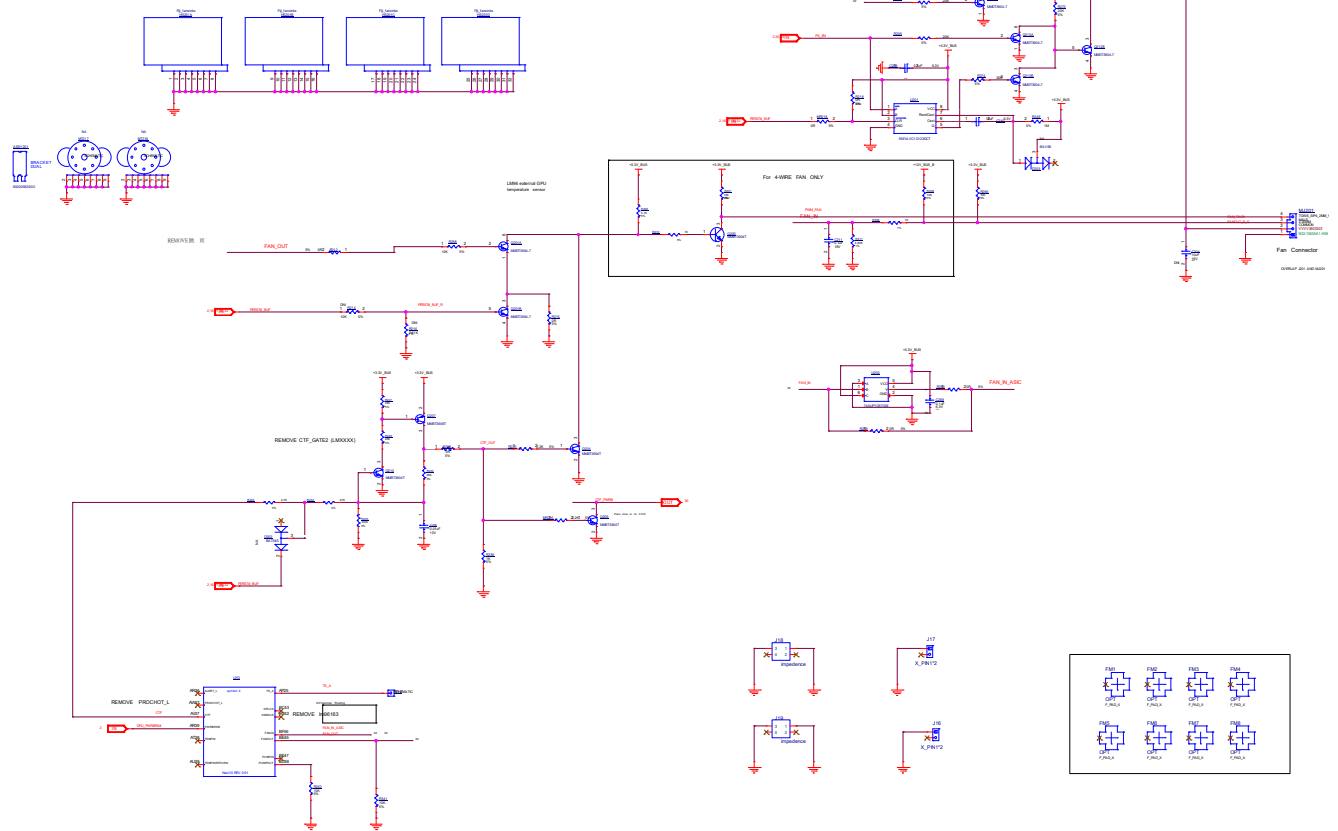


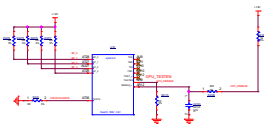




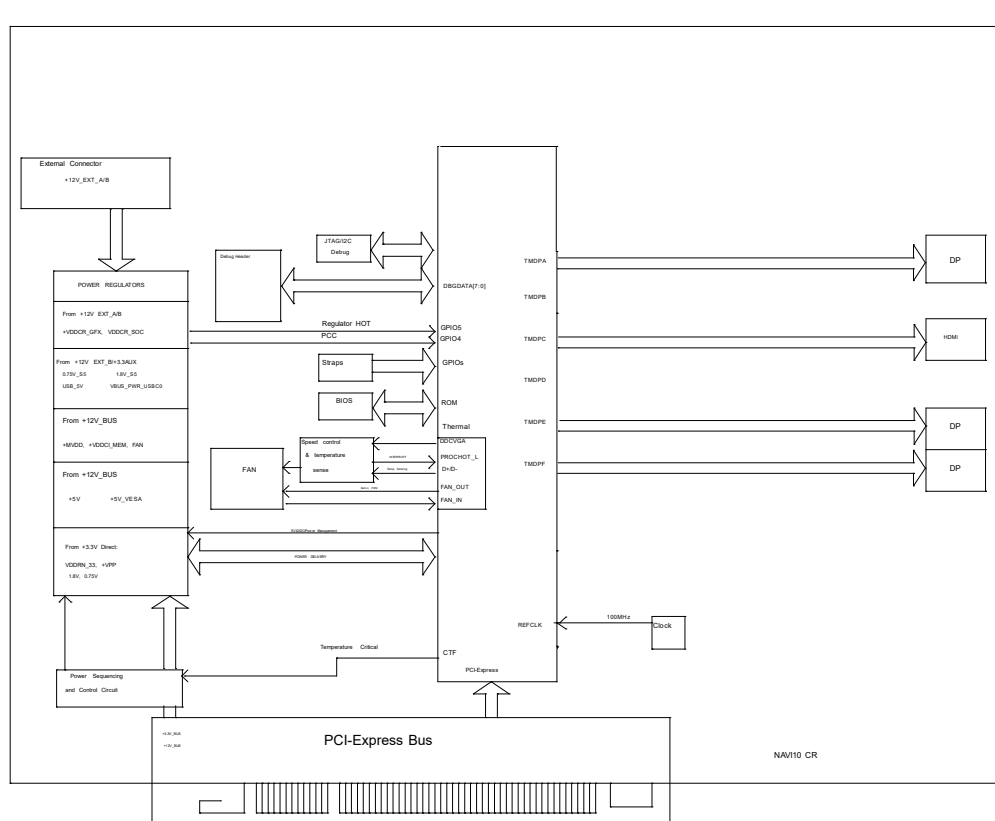


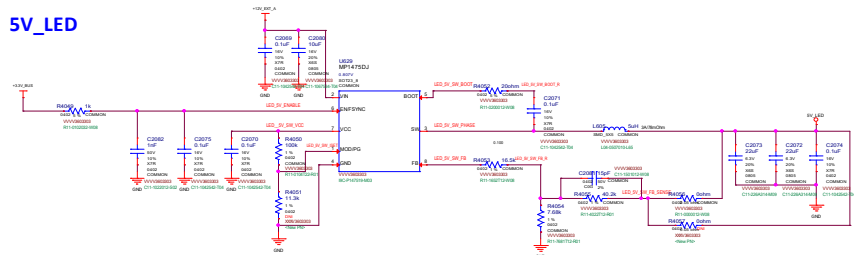
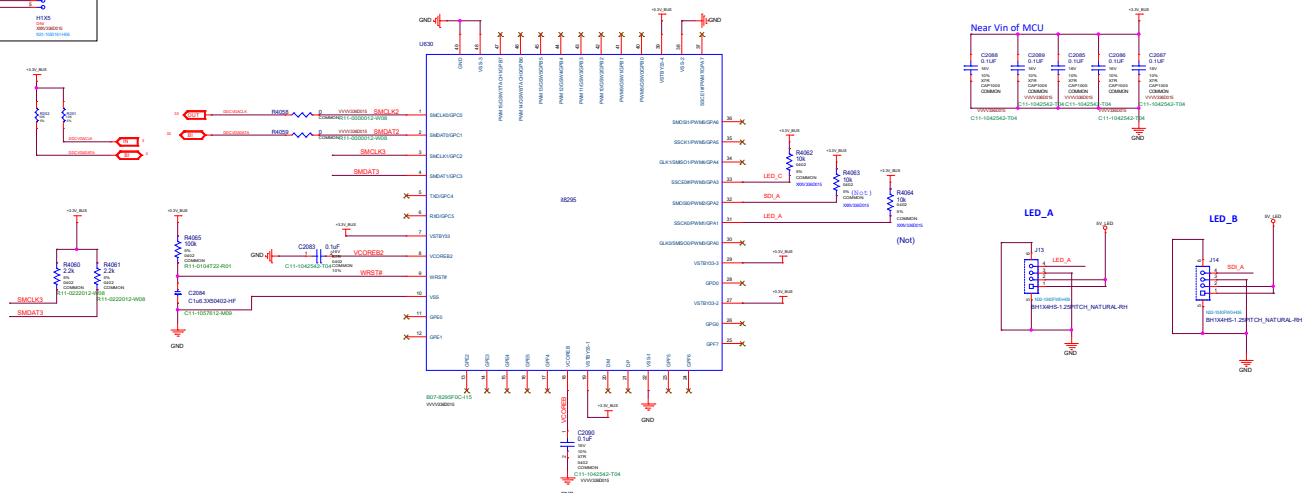
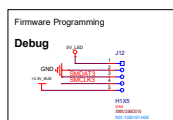


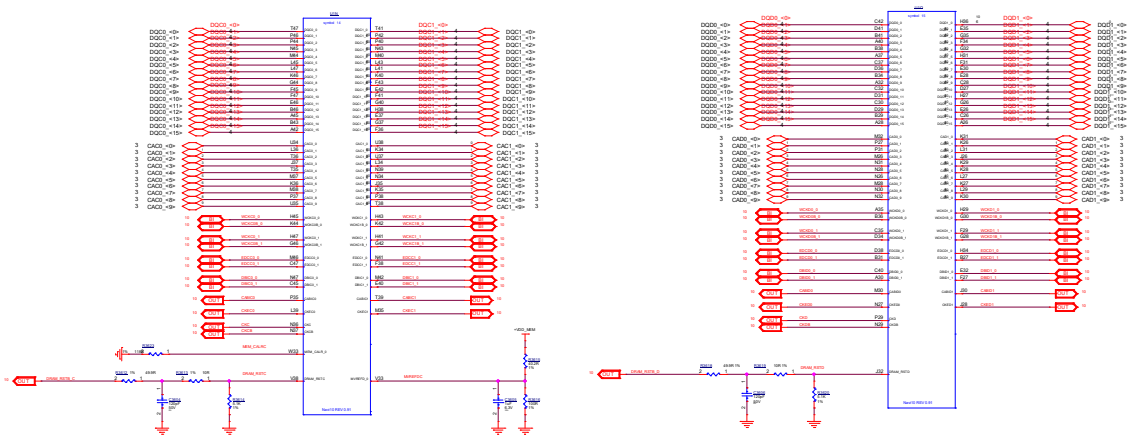


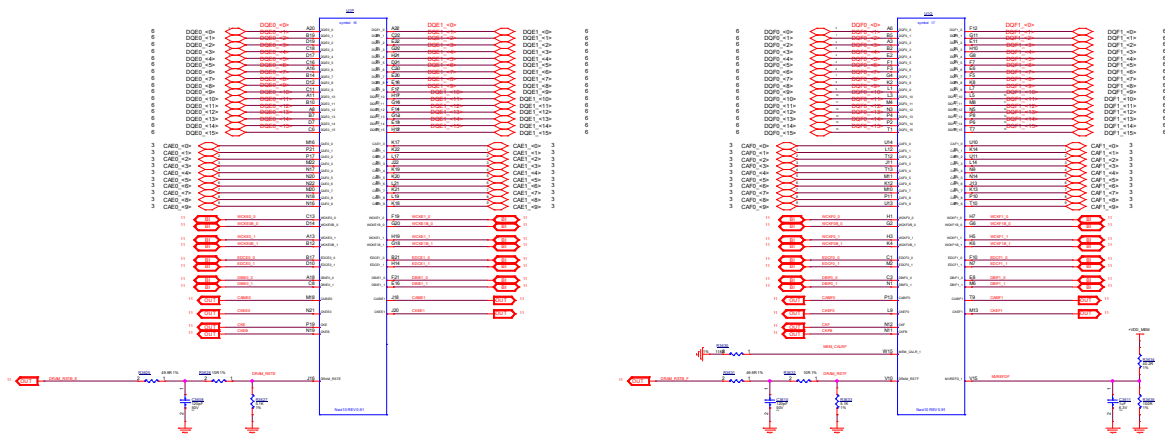


PROCON Lightbar Header

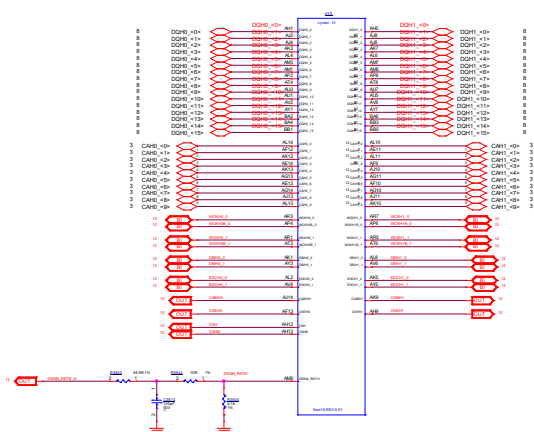
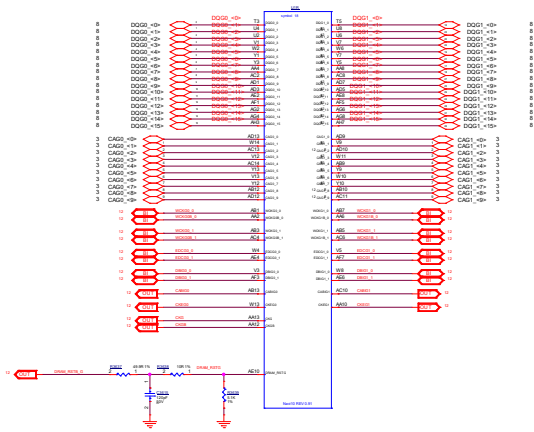








AMD - PLATFORM HARDWARE ENG #6# NO.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201209						INFORMATION: This document contains AMD, AMD and/or Intel's confidential and/or proprietary information. This document is intended for the use of the recipient party only and may contain information that is confidential and/or proprietary to AMD, Intel or its subsidiaries. This document is not to be distributed, copied, reproduced, or otherwise used in any manner without the prior written consent of AMD or Intel.	
SHEET: NAUHS MEMOCH		REV: 1.03		FILE:		NAUHS G6X16Mode DP DP HDMI DP	
DATE: Mon Apr 15 09:57:37 2019		USER:		PROJECT:		DESCRIPTION:	
SHEET NUMBER: 7		OF: 35		FILE:		DESCRIPTION:	
DOCUMENT NUMBER: 101_0196_A		USER:		PROJECT:		DESCRIPTION:	
NOTES: NOTE		USER:		PROJECT:		DESCRIPTION:	



AMD PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203					CONFIDENTIAL AND PROPRIETARY TO AMD OR ITS AFFILIATES. ALL RIGHTS RESERVED. AMD, the AMD logo, and the AMD logo with the AMD text are trademarks of AMD or its affiliates. All other trademarks are the property of their respective owners.
SHEET: NAVI10 MEM G/H			DATE: Mar-Apr 15 15:07:08 2019		REV: 1.0
SHEET NUMBER: 8 OF 35			DOCUMENT NUMBER: 100_010000_000		TITLE: NAVI10 G6X10 Mode DP DP HDMI DP
NOTE: NOTE					