

8	7	6	5	4	3	2	1
D							D
C							C
B							B
A							A

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24	REG 0.75V		
25	REG 1.8V		

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SHEET: TOC

DATE: Mon Apr 15 05:57:55 2019REV: 1.00

SHEET NUMBER: 1 OF 35

DOCUMENT NUMBER: 105_D199w_00A

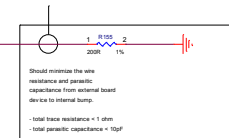
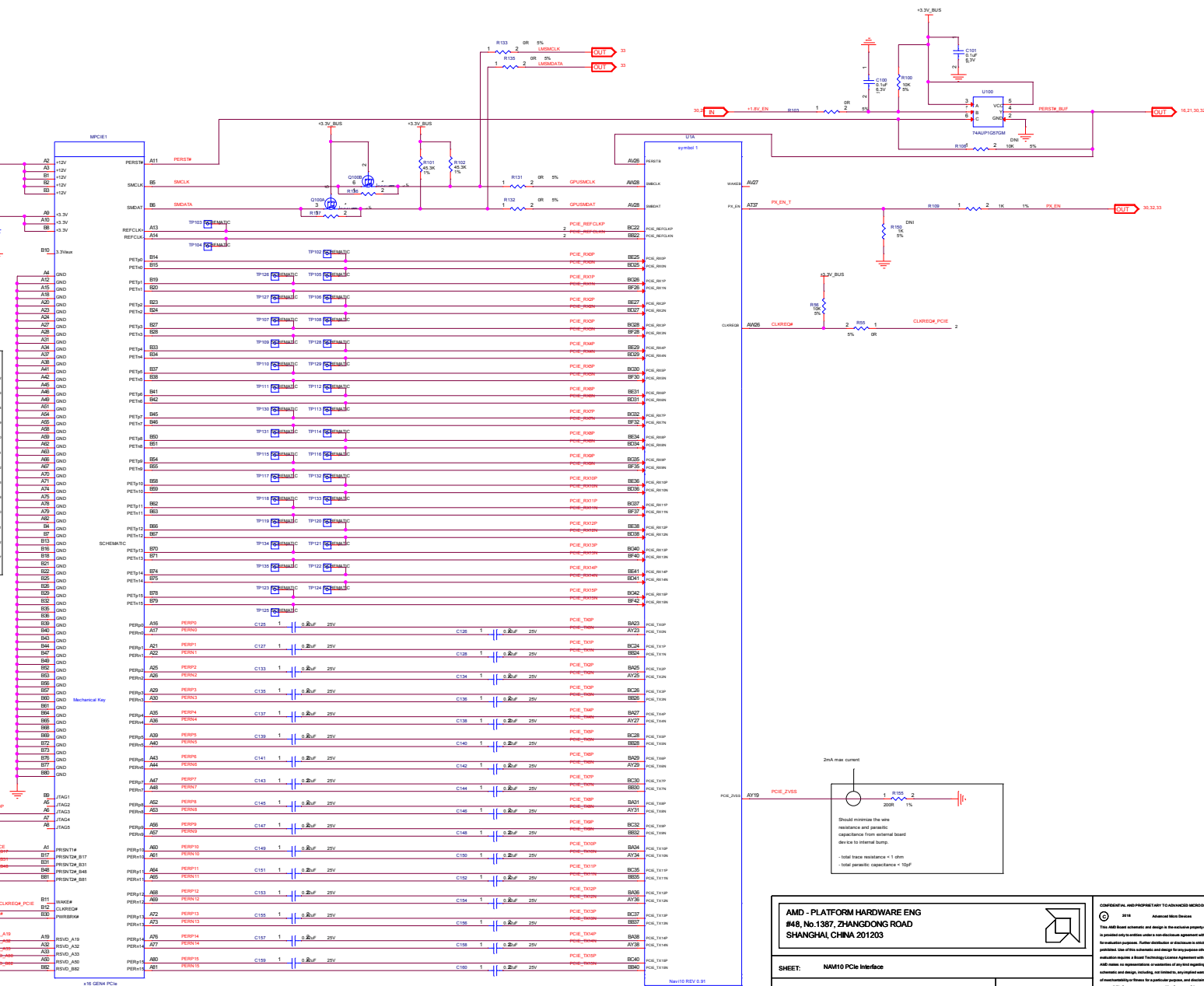
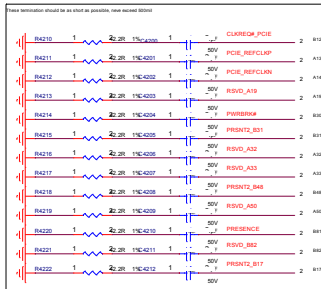
NOTES: NOTE

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TITLE:
NAV110 G8X16Mode DP DP HDMI DP

(1) NAV10 PCIe Edge Connector

PLACE THESE CAPS AS CLOSE TO
YOUR COMPUTER AS POSSIBLE



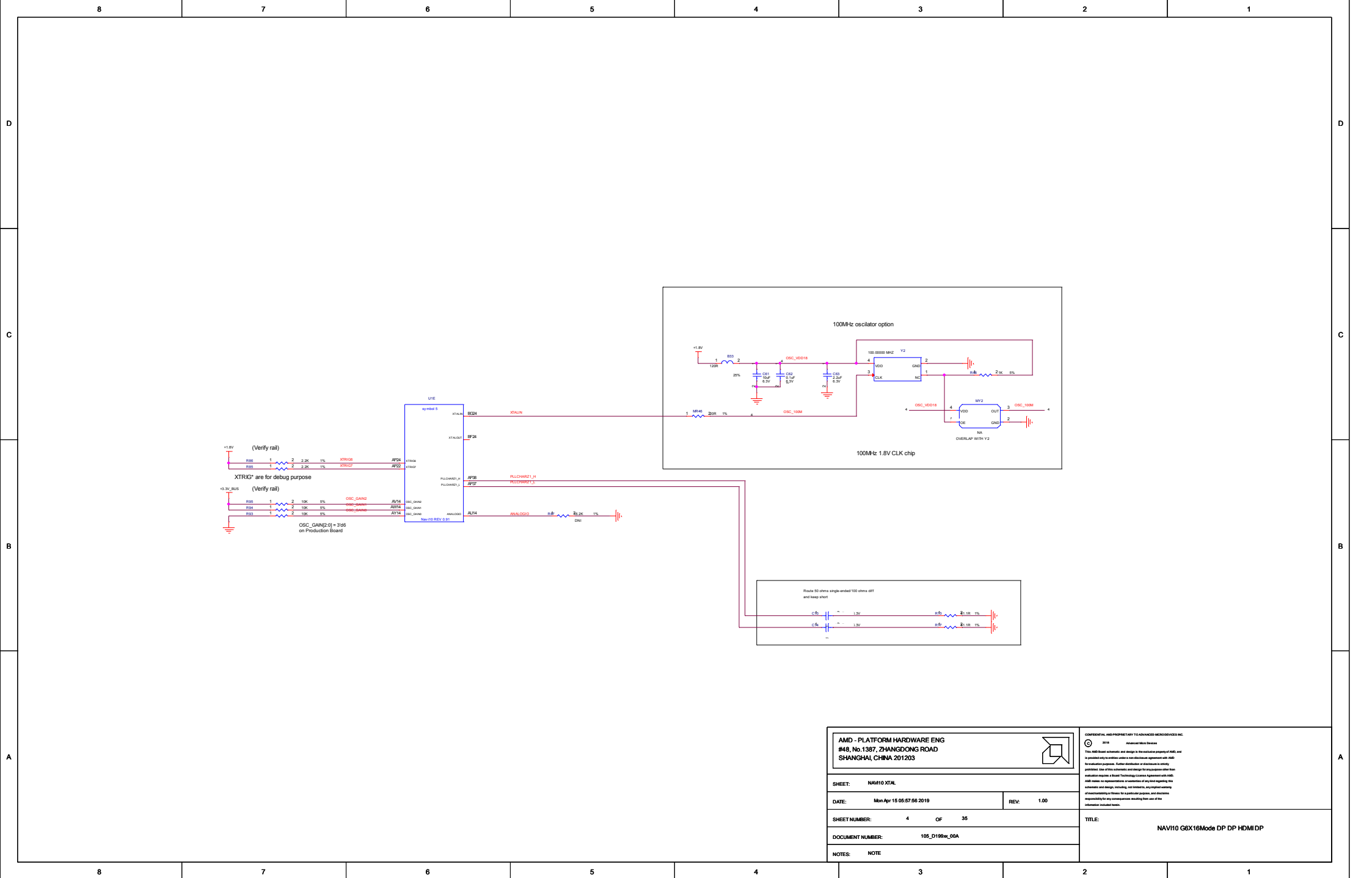
SYMBOL LEGEND	
DN	DO NOT INSTALL
5 0/0	ACTIVE LOW
BUO	BRING UP ONLY
	DIGITAL GROUND
	ANALOG (GROUND)

AMD - PLATFORM HARDWARE ENCLOSURE
#48, No.1387, ZHANGDONG ROAD
SHANGHAI, CHINA 201203

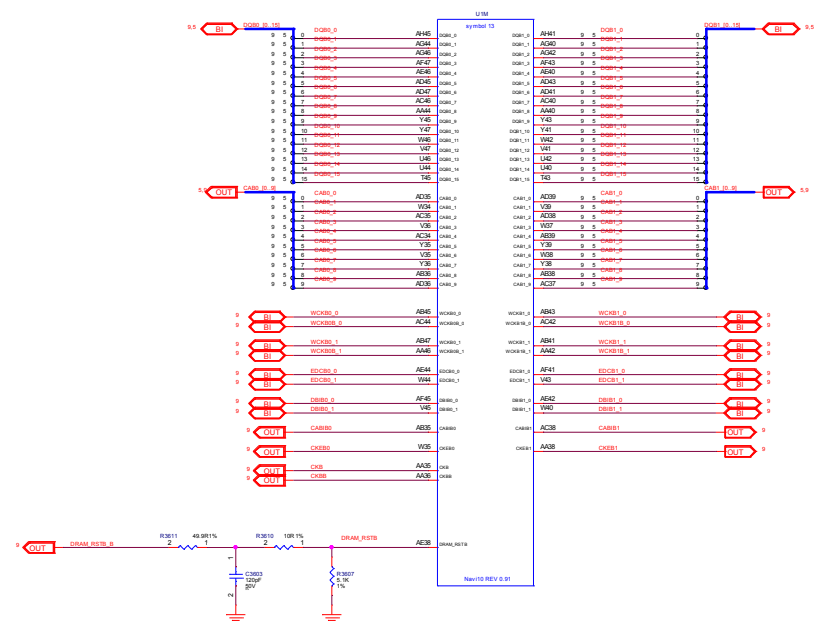
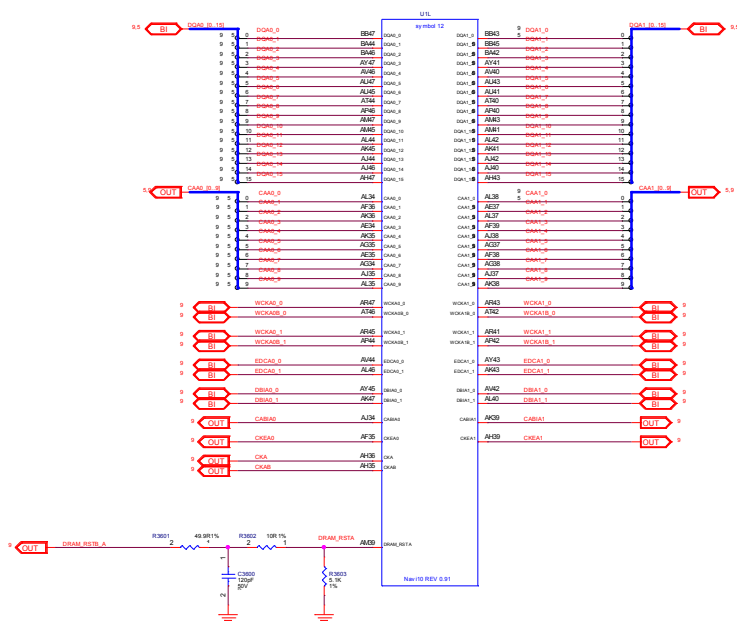
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NAVI10 G6X16Mode DP DP HDMI DP

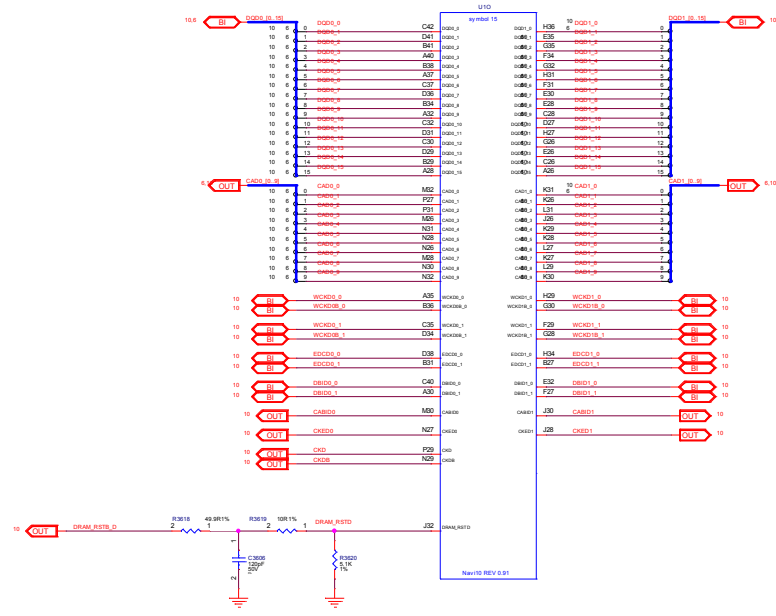
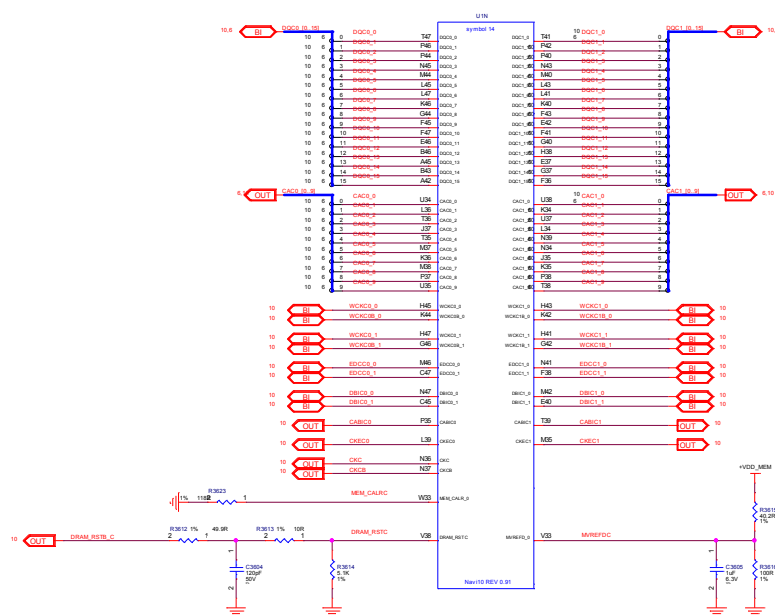


NAVI 10 MEM INTERFACE CH A/B

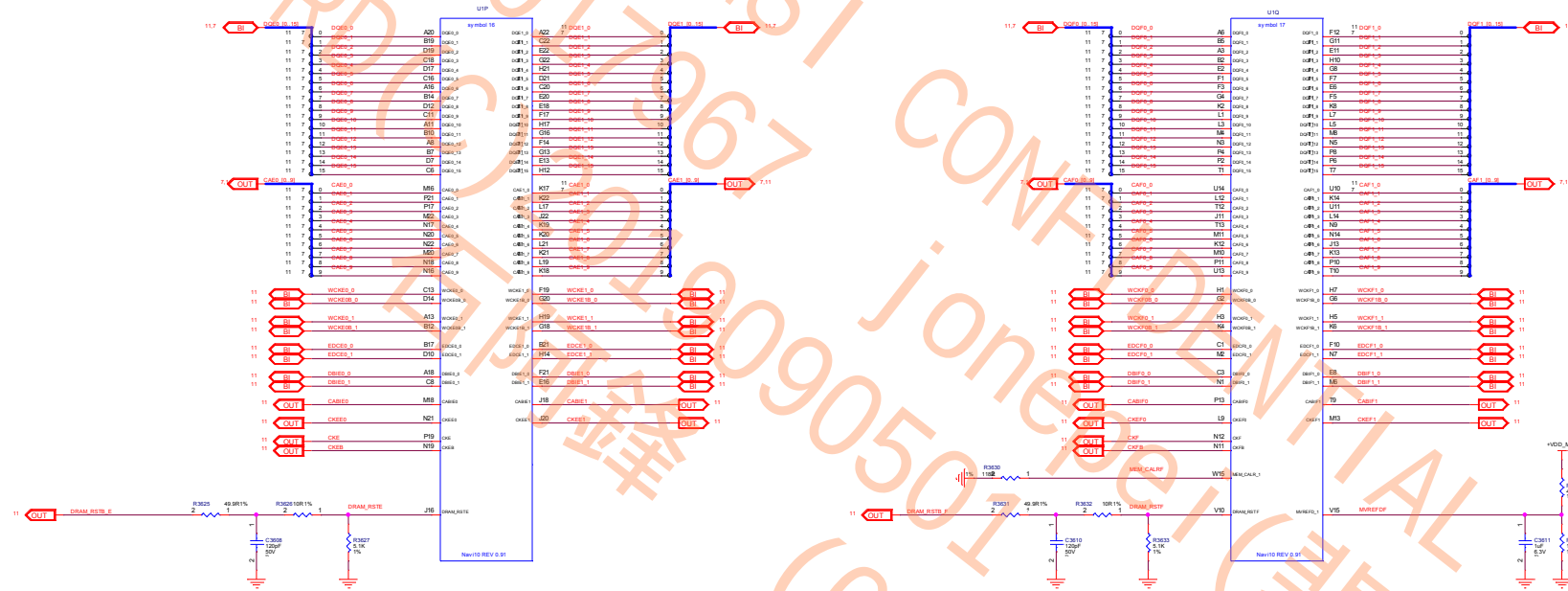


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SHEET: NAV10 MEM CH A/B		
DATE: Mon Apr 15 05:57:57 2019	REV: 1.00	
SHEET NUMBER: 5 OF 35		
DOCUMENT NUMBER: 105_0190w_00A		
NOTES: NOTE		TITLE: NAV10 G8X16Mode DP DP HDMI DP

NAVI 10 MEM INTERFACE CH C/D



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SHEET:	NAVI10 MEM CHCD	REV:	1.00
DATE:	Mon Apr 15 05:57:57 2019	SHEET NUMBER:	6 OF 35
DOCUMENT NUMBER:	105_0190w_00A	TITLE:	NAVI10 G8X16Mode DP DP HDMI DP
NOTES:	NOTE		



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SHEET: NAV10 MEM CHEF

DATE: Mon Apr 15 05:57:57 2019

SHEET NUMBER: 7 OF 31

DOCUMENT NUMBER: 105_D199xx_C

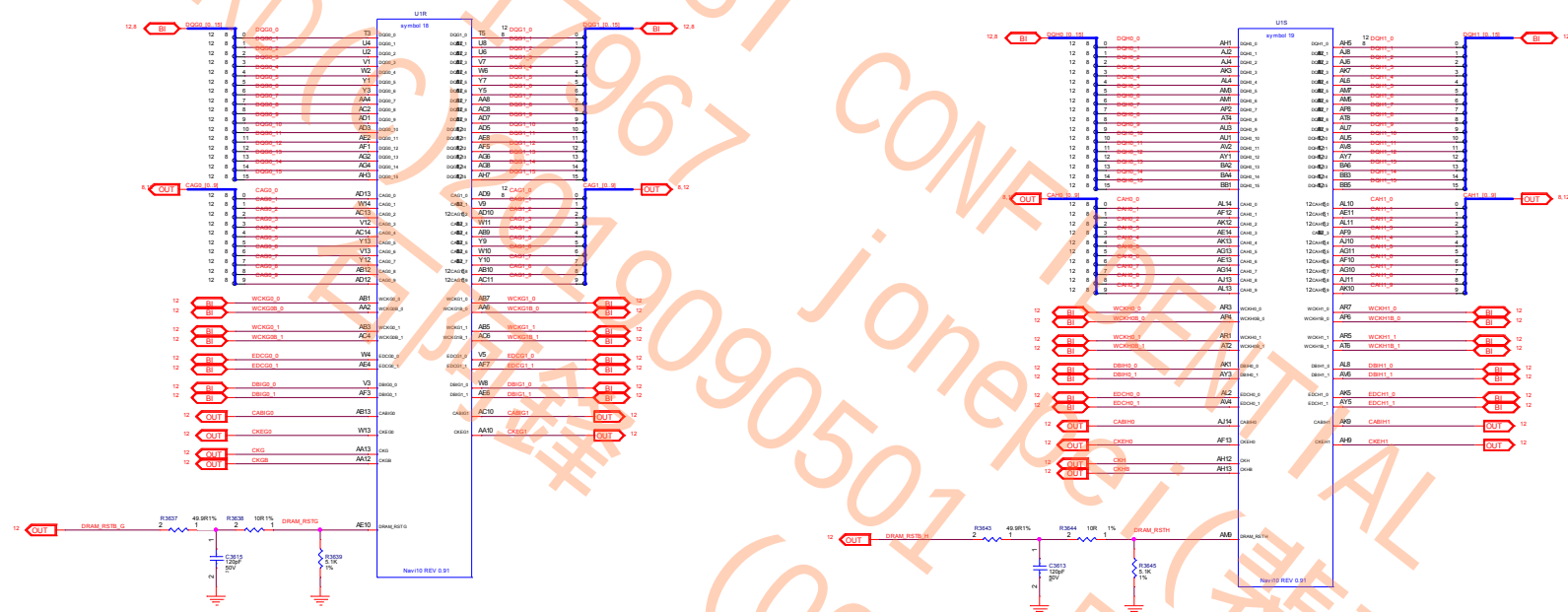
NOTES:	NOTE
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TITLE: NAVI10 G6X16Mode DP DP HDMI DP



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SHEET: NAVI10 MEM CHG

DATE: Mon Apr 15 05:57:58 2019

REV: 1.00

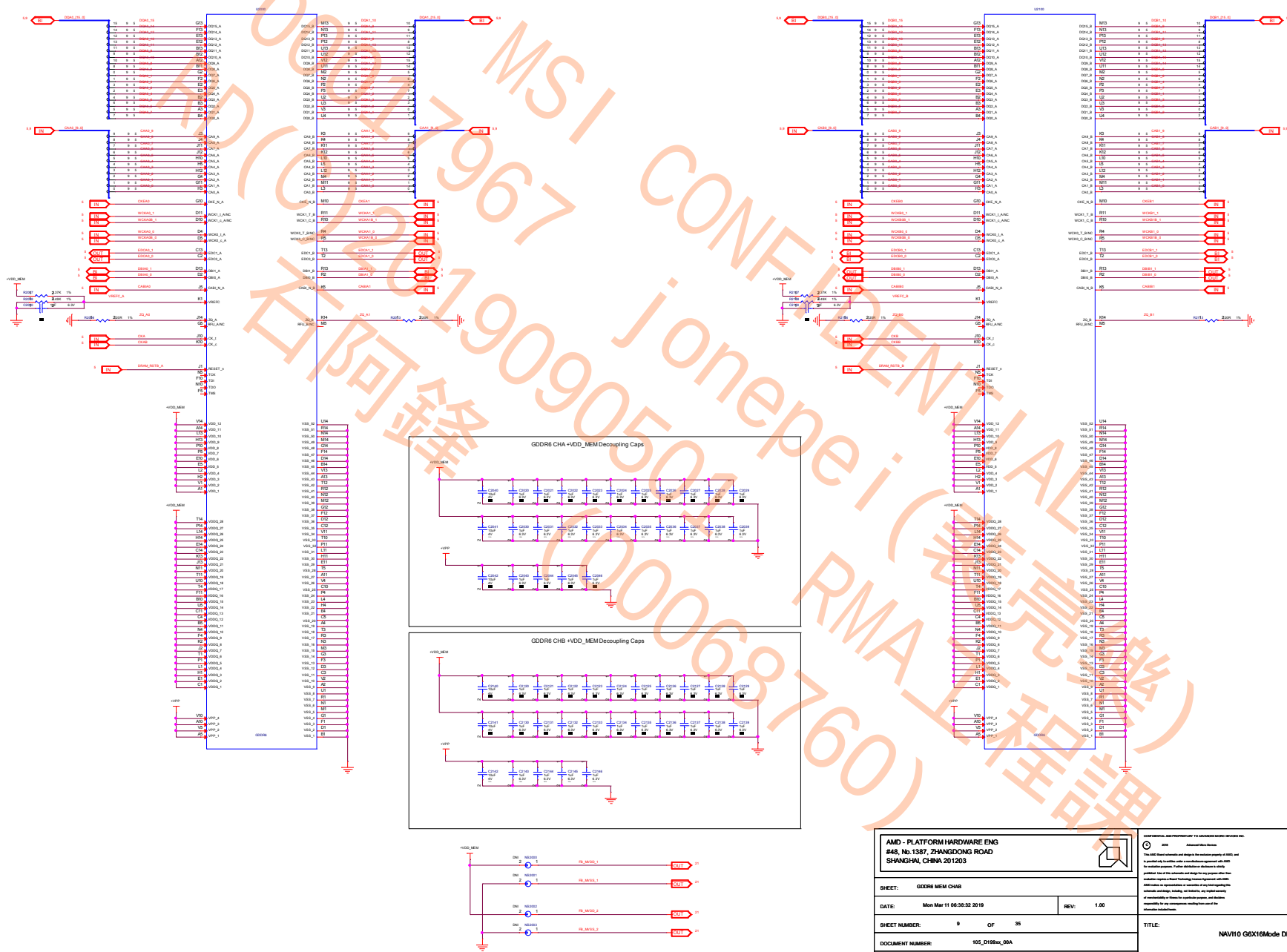
SHEET NUMBER: 8 OF 35

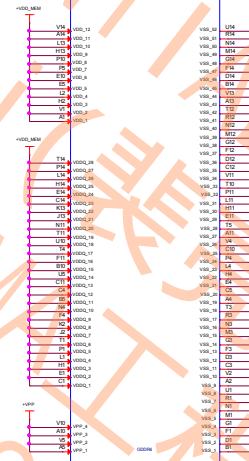
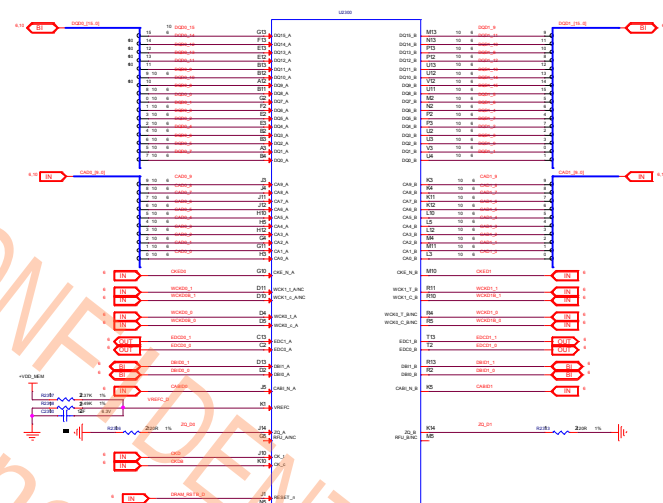
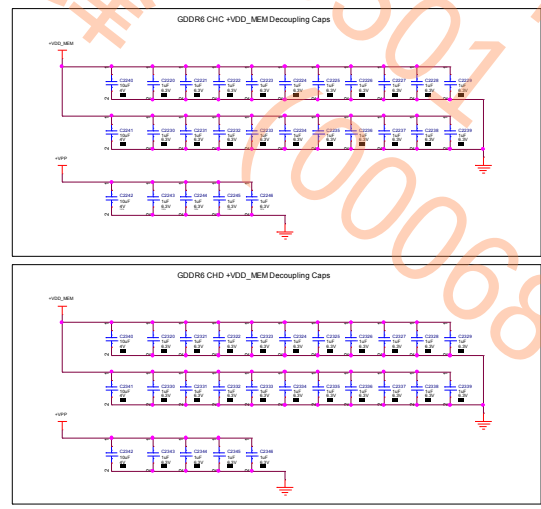
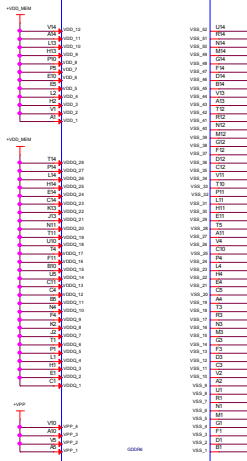
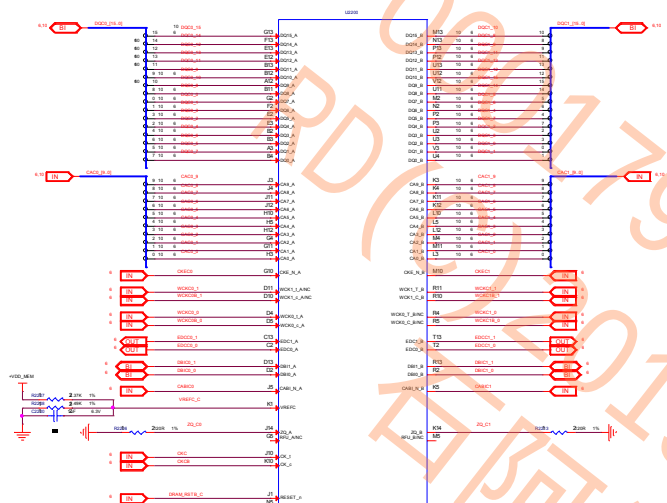
DOCUMENT NUMBER: 105_0190w_00A

NOTES: NOTE

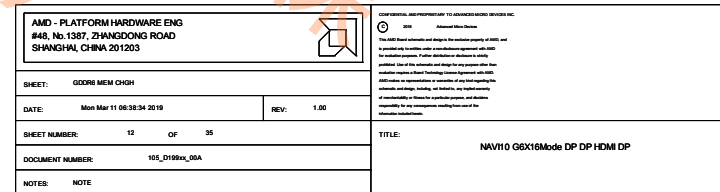
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TITLE: NAVI10 G8X16Mode DP DP HDMI DP

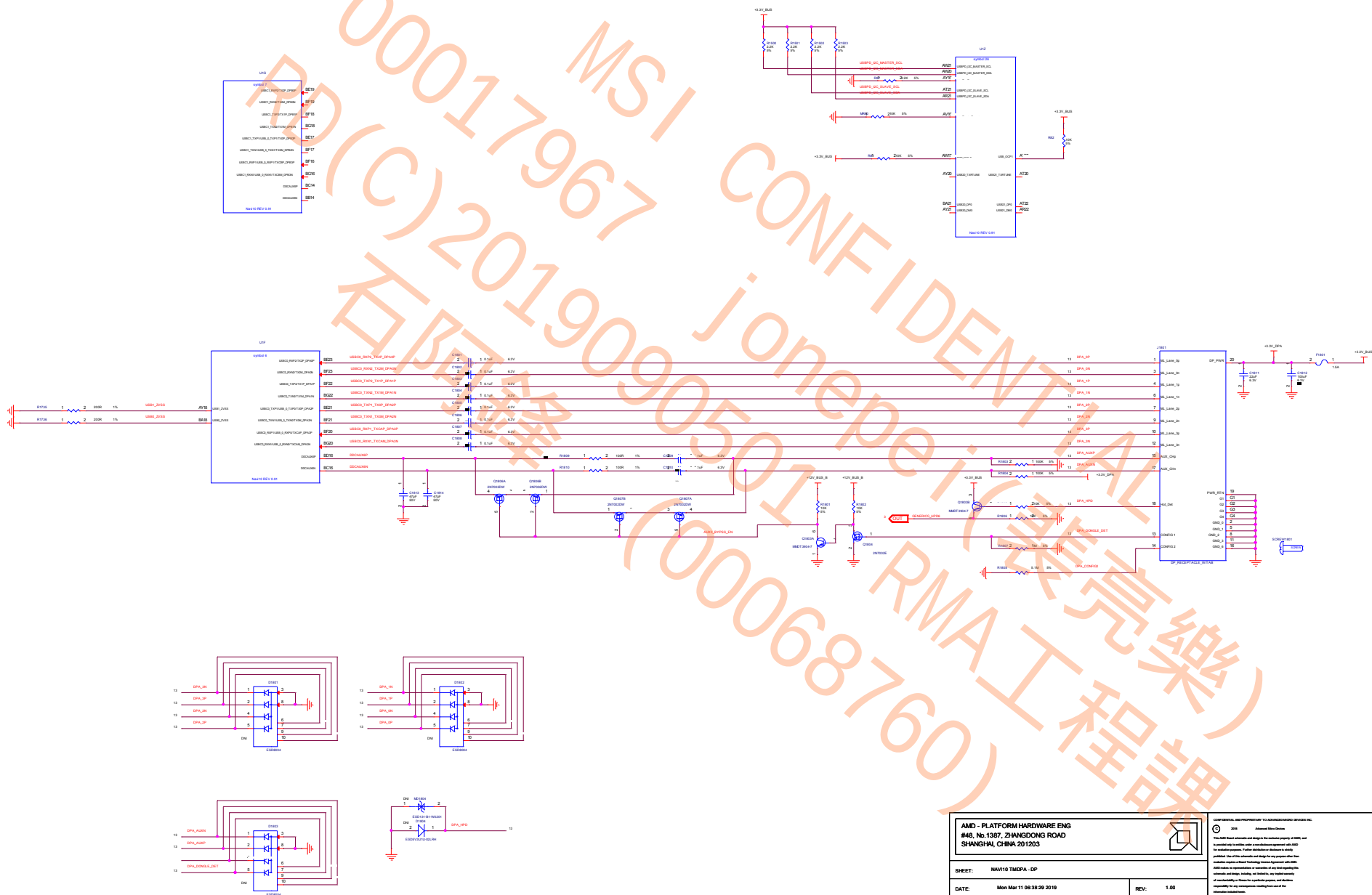




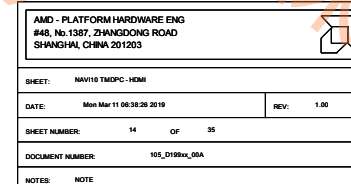
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SHEET: GDS9 MEM CHD		REV: 1.00	
DATE: Mon Mar 11 06:38:33 2019		SHEET NUMBER: 10 OF 35	
DOCUMENT NUMBER: 105_019106_00A		TITLE: NAV110 GX616MD DP DP HDMI DP	
NOTES: NOTE			



NAVI10 TMDP A/B

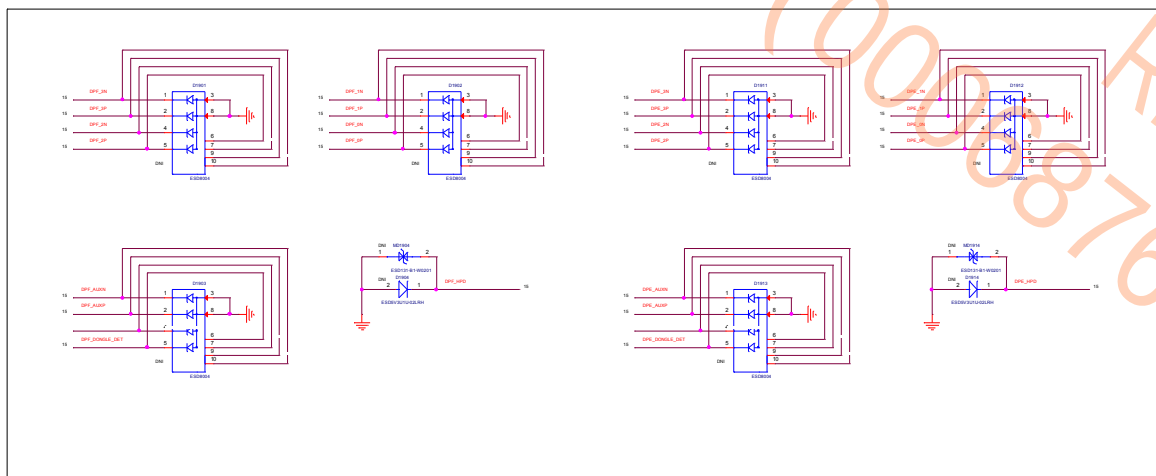
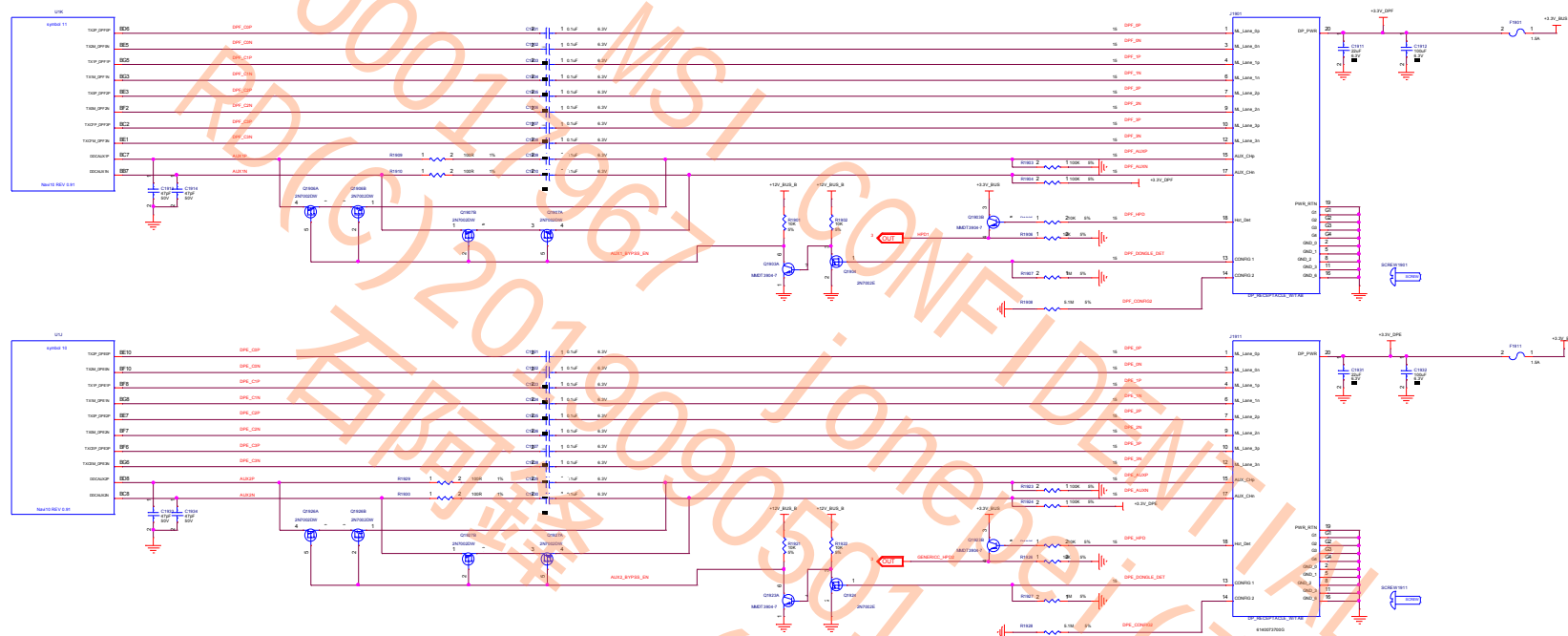


AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203			
SHEET: NAVI10 TMDP-A - DP		REV: 1.00	
DATE: Mon Mar 11 06:38:29 2019		REV: 1.00	
SHEET NUMBER: 13 OF 35		TITLE: NAVI10 G6X16Mode DP DP HDMI DP	
DOCUMENT NUMBER: 101_0198x_BSA			
NOTES: NOTE			



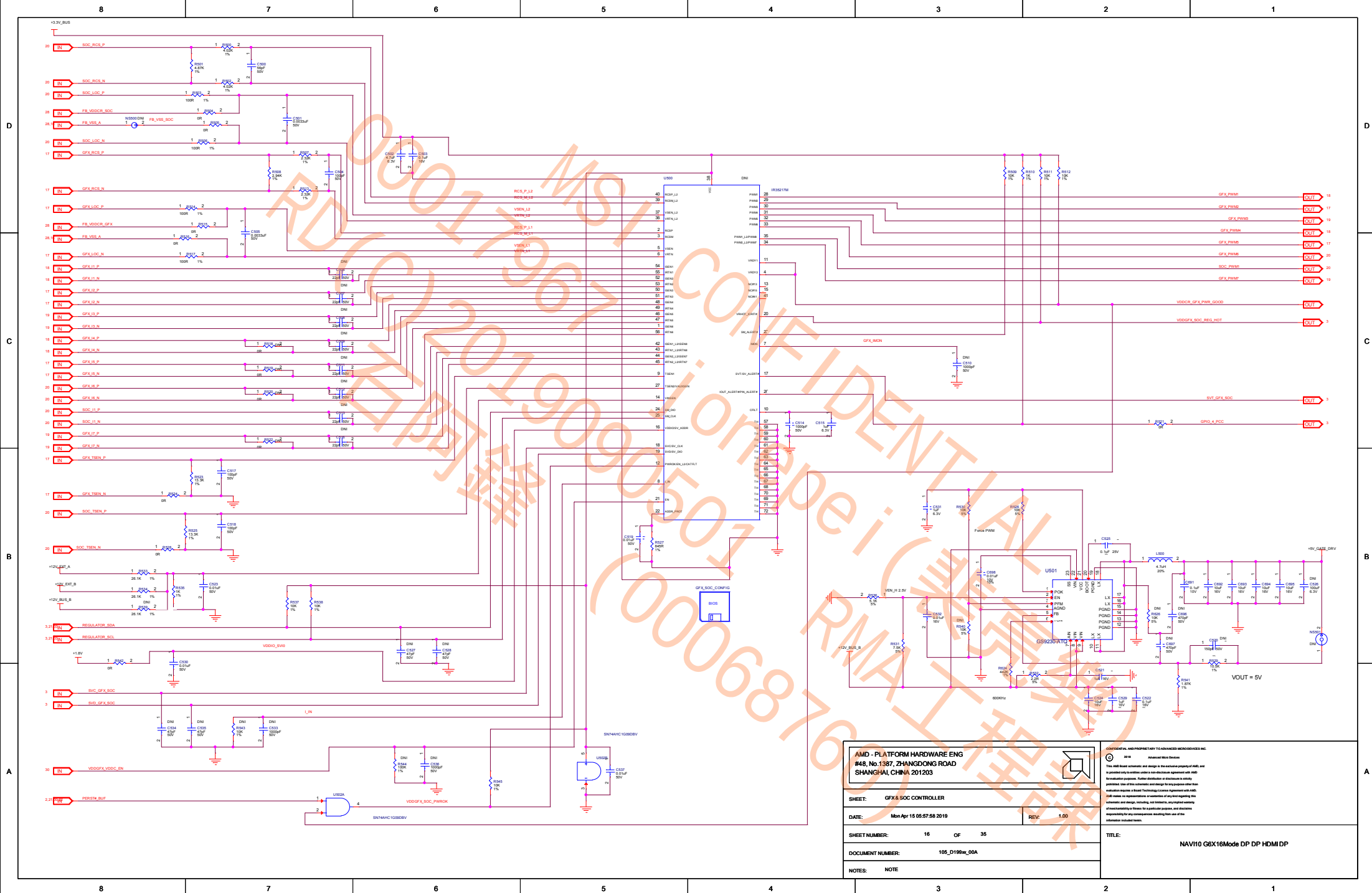
TITLE:	NAV110 G6X16Mode DP DP HDMI DP
--------	--------------------------------

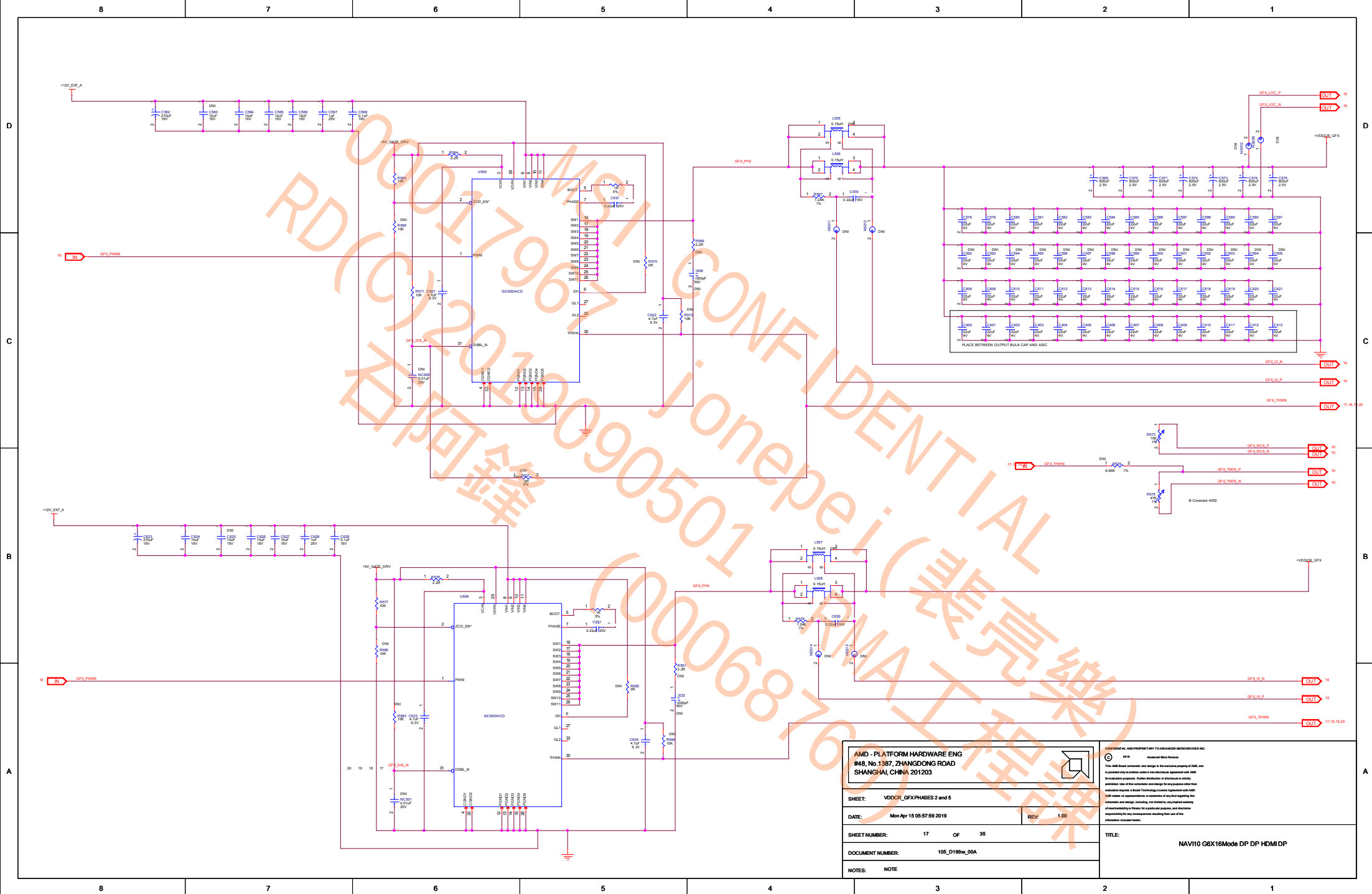
(9) NAVI10 TMDP E/F

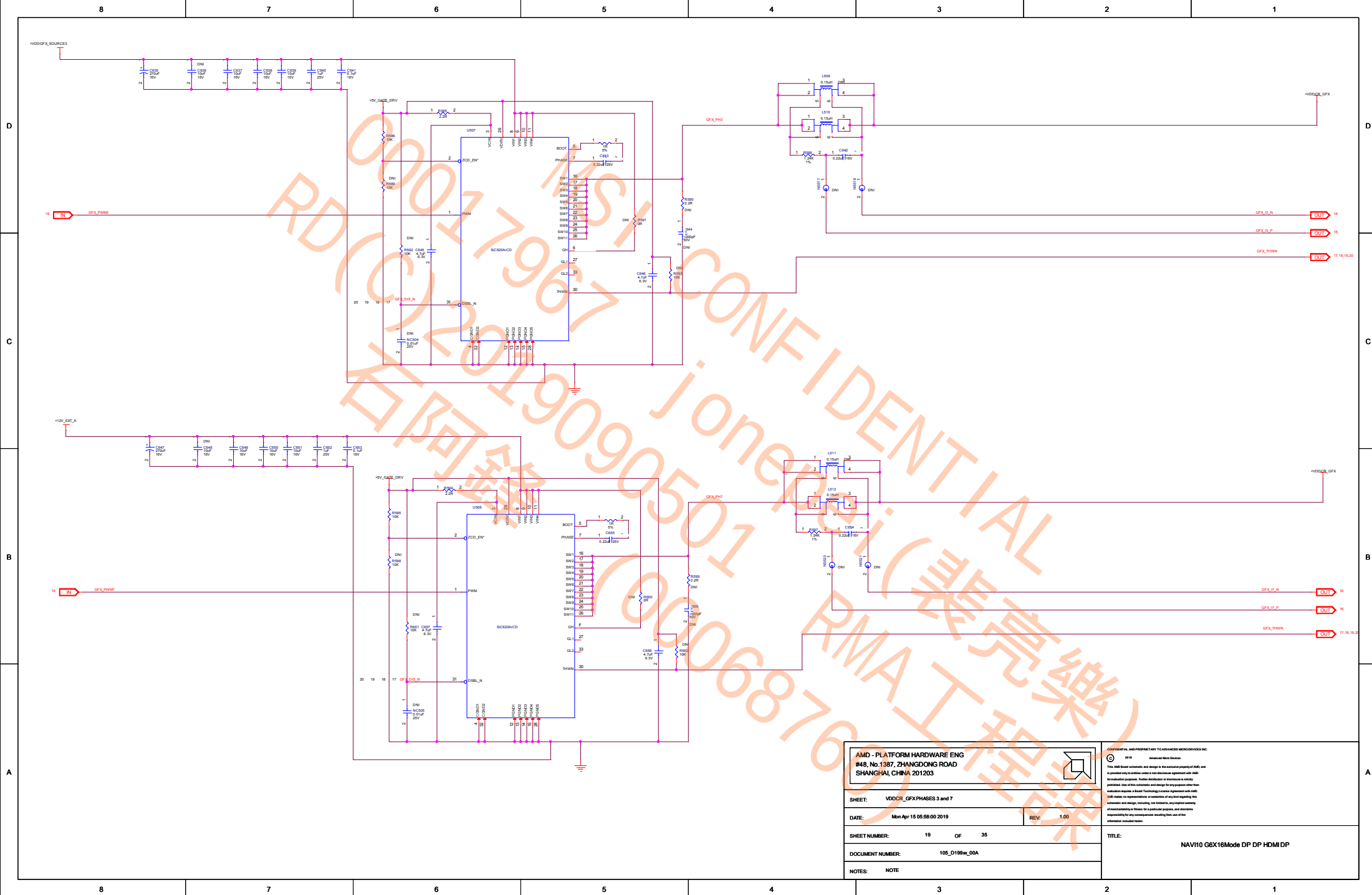


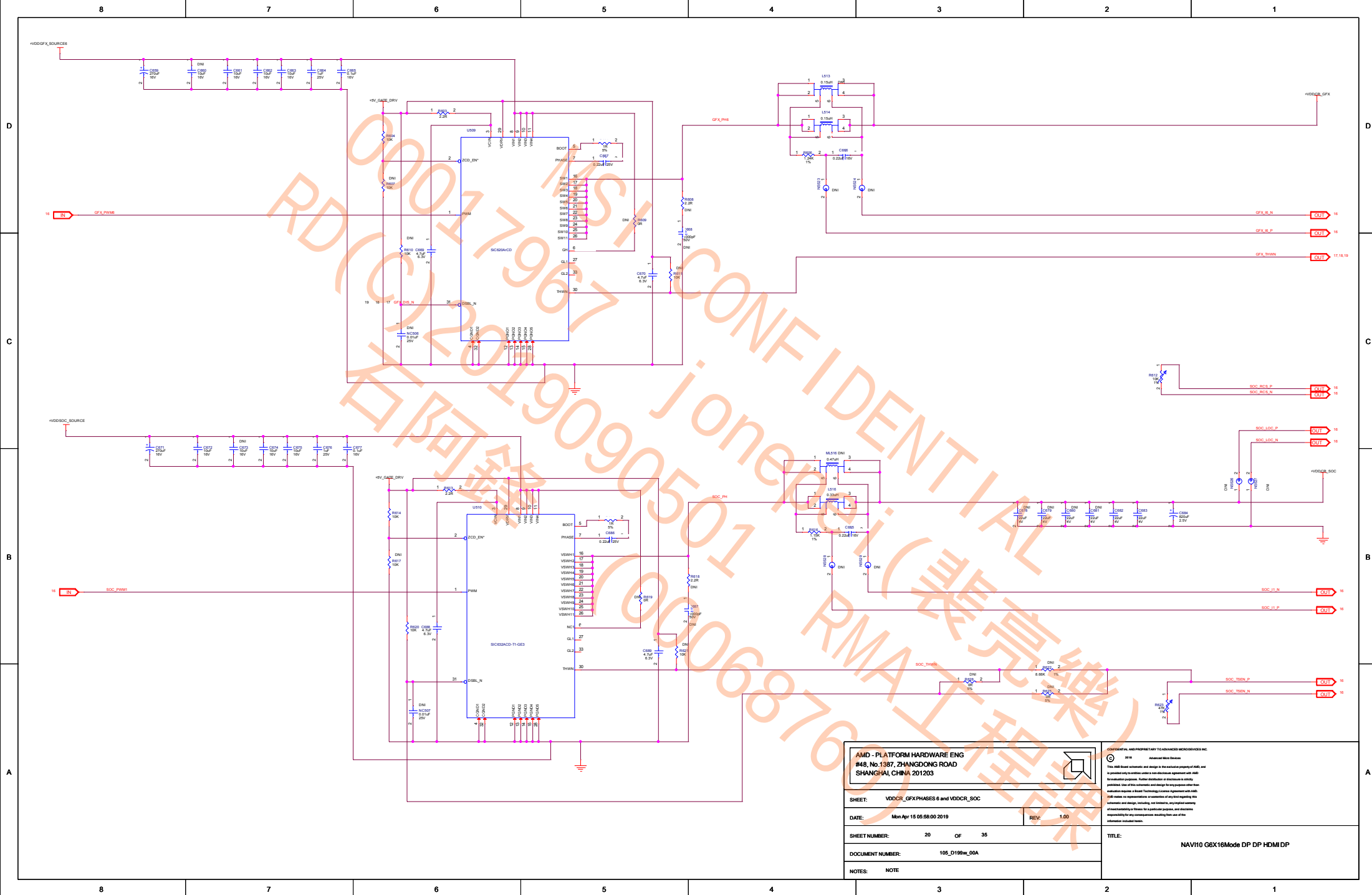
AMD - PLATFORM HARDWARE ENG #46, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203			
SHEET: NAVI10 TMDP E/F - DP DP		REV: 1.00	
DATE: Mon Mar 11 06:38:27 2019		SHEET NUMBER: 15 OF 35	
DOCUMENT NUMBER: 105_0198x_BSA		TITLE: NAVI10 G6X16Mode DP DP HDMI DP	
NOTES: NOTE			

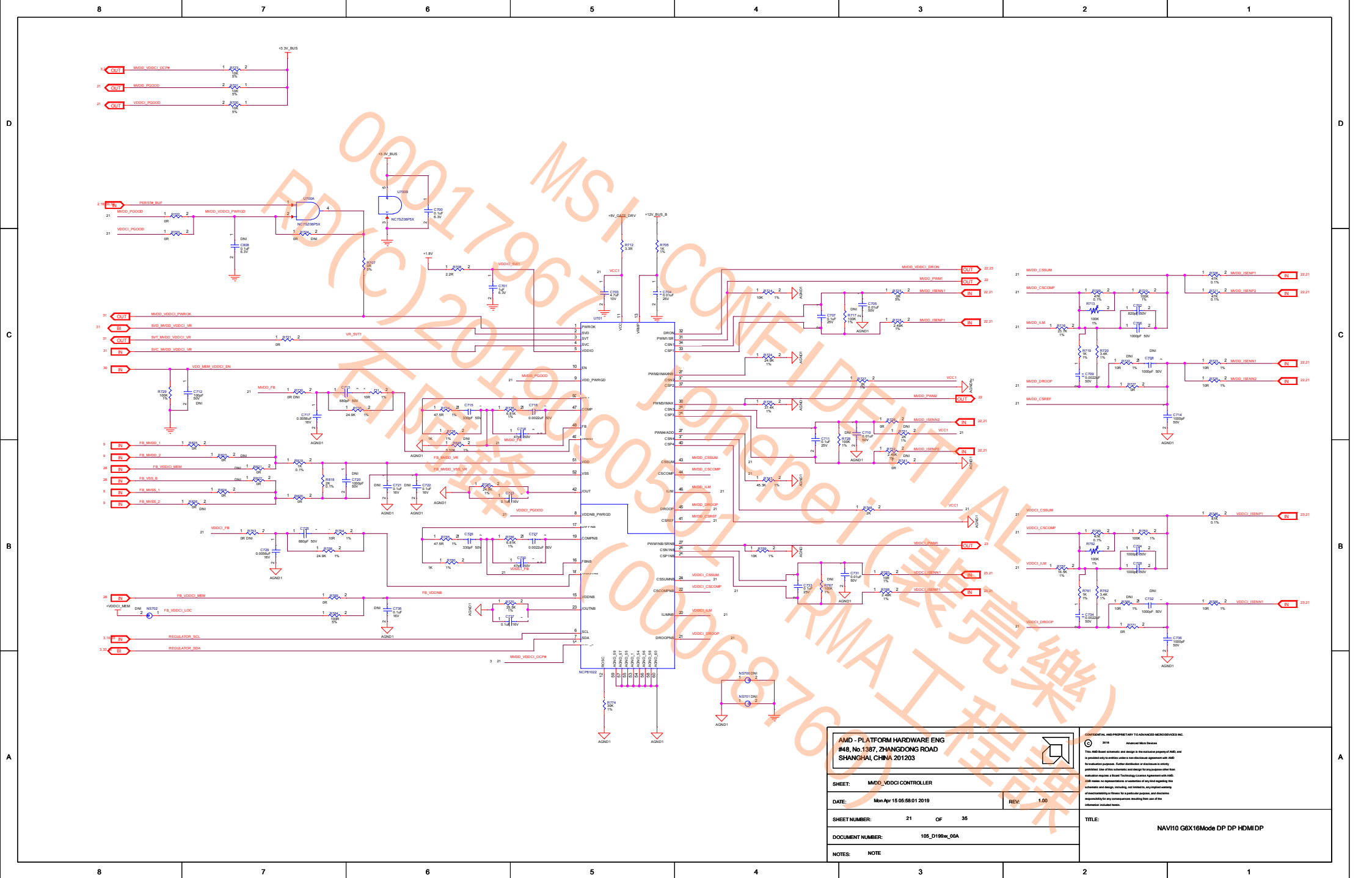
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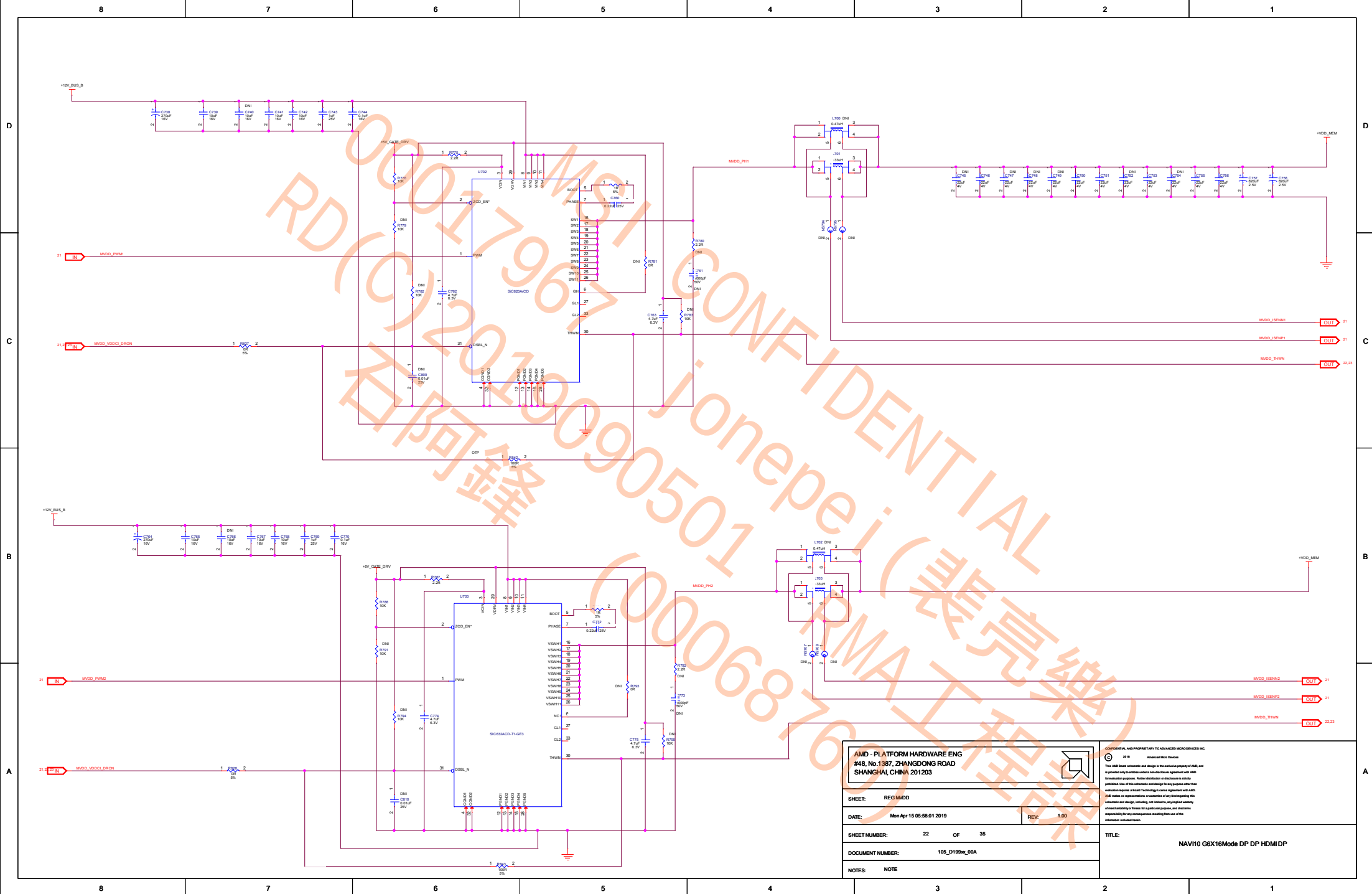


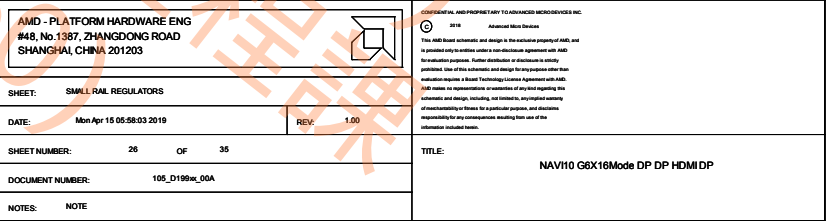


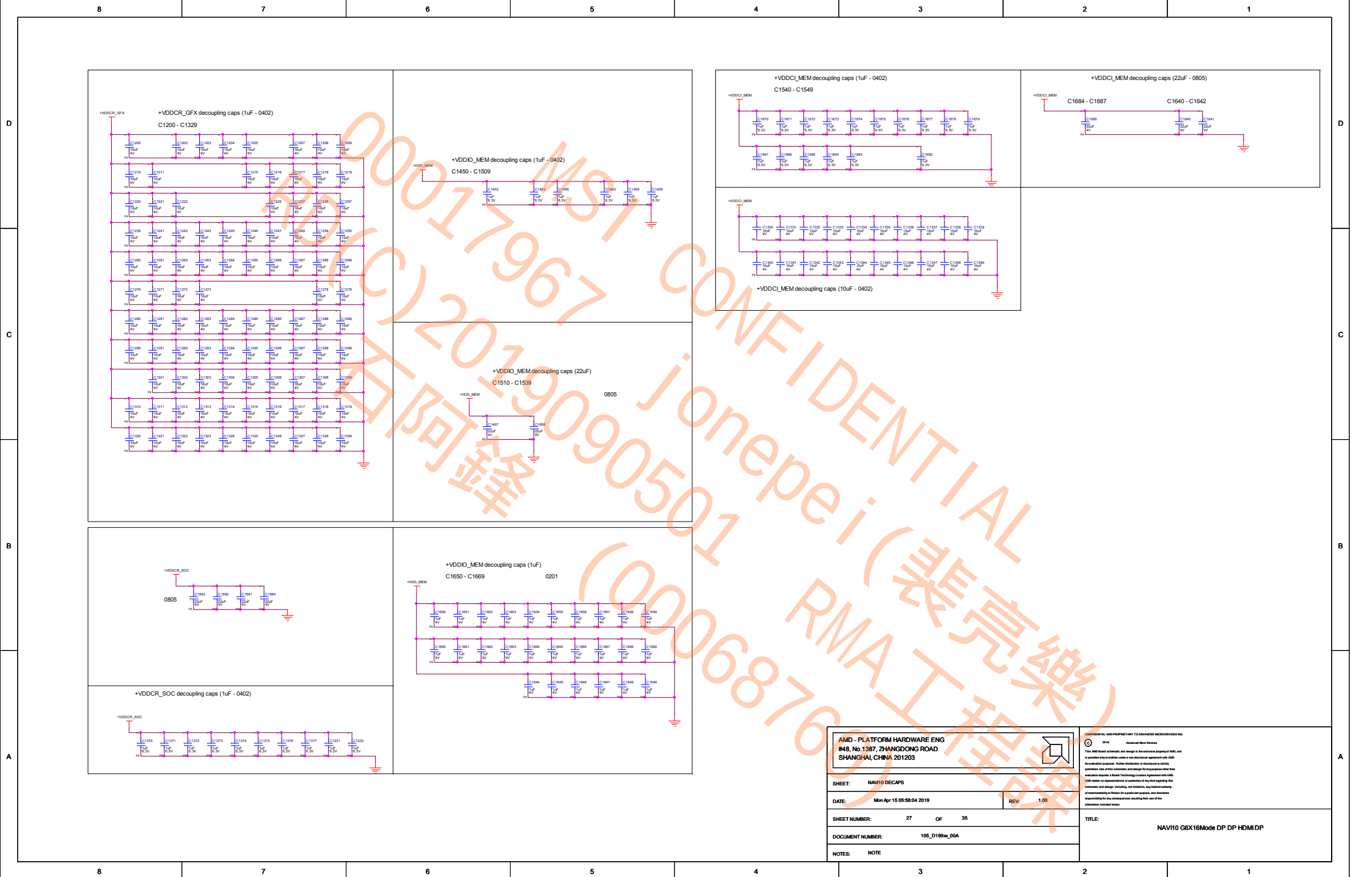








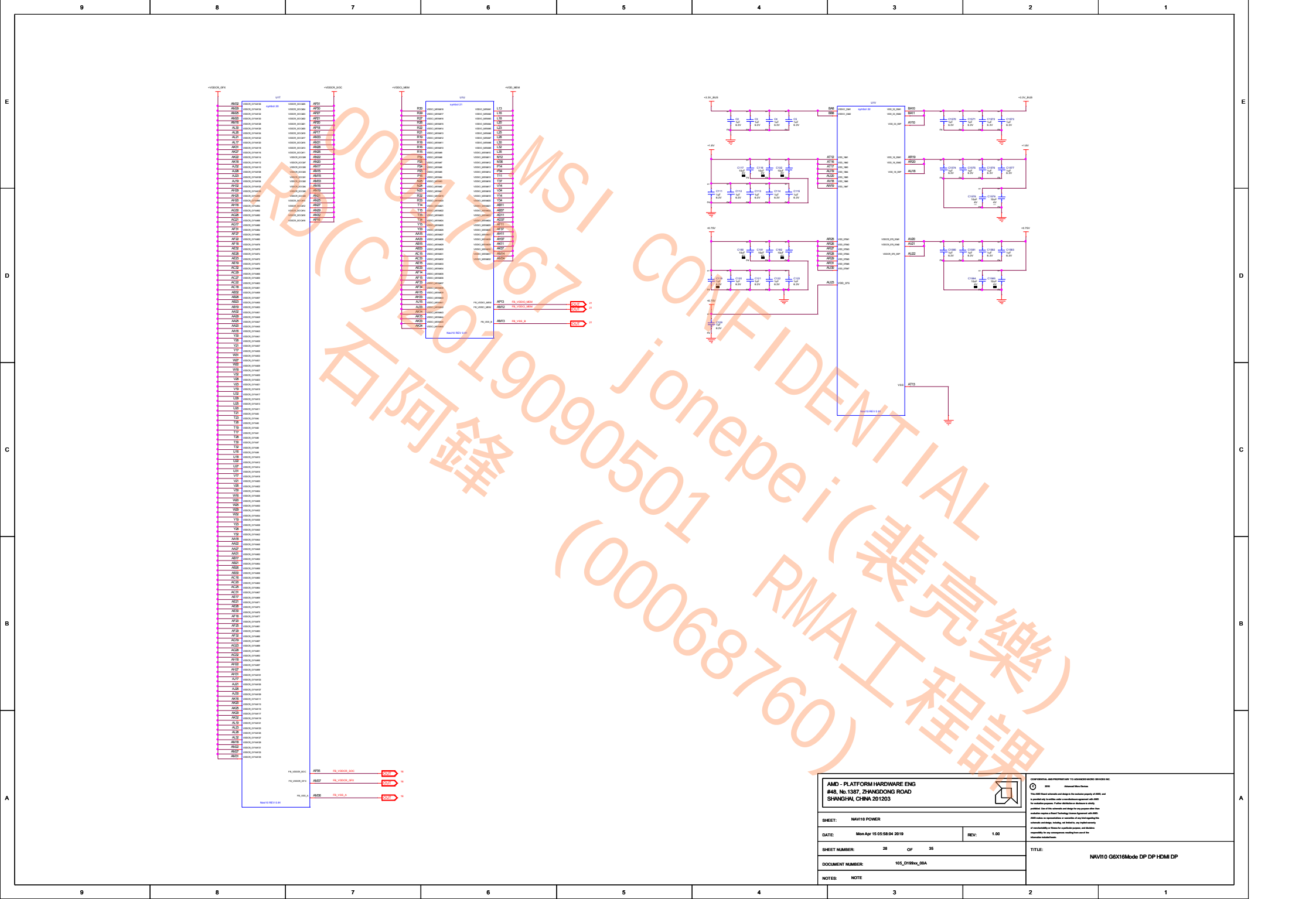


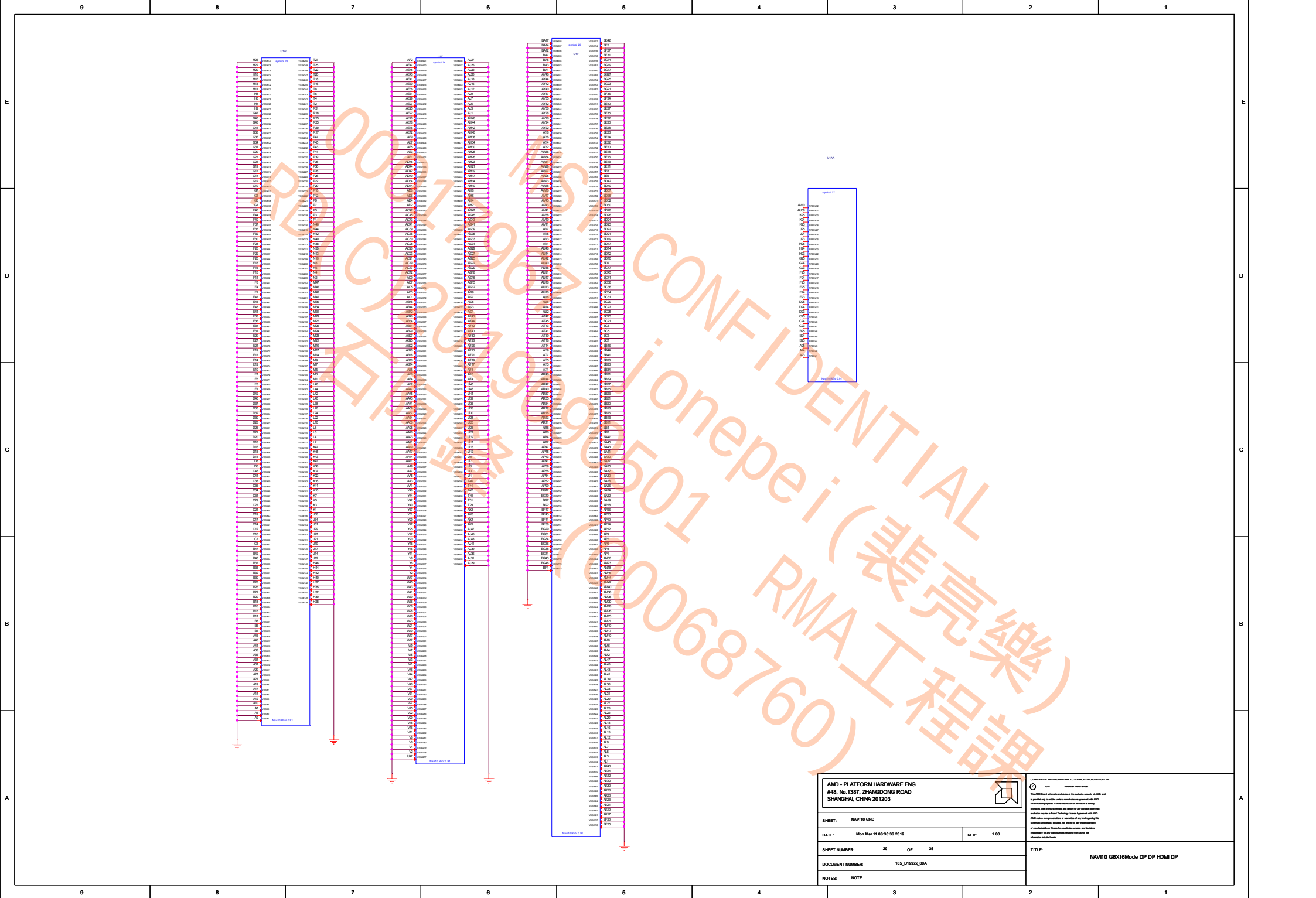


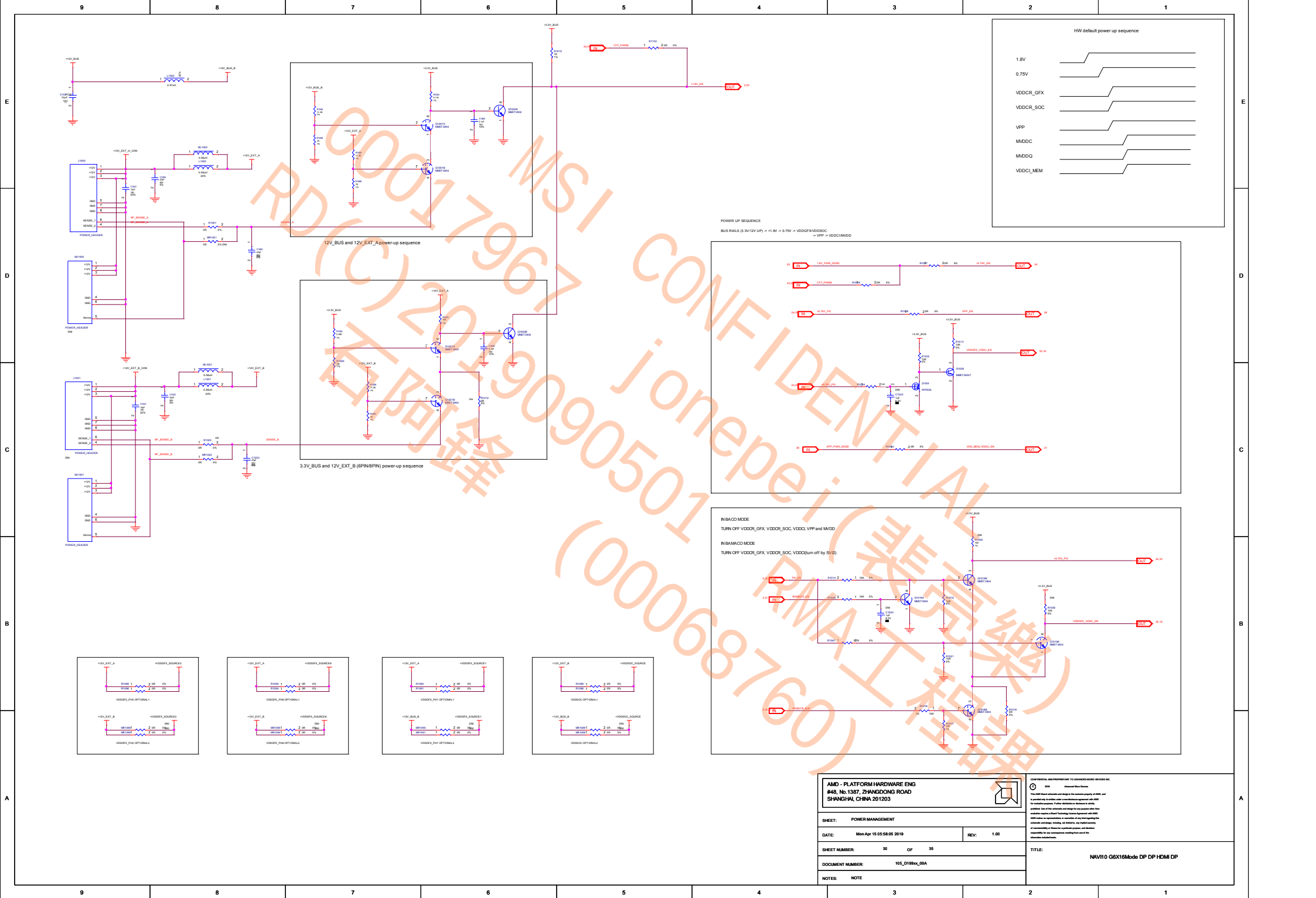
AMD - PLATFORM HARDWARE ENG #48, No.1387, ZHANGDONG ROAD SHANGHAI, CHINA 201203	
SHEET: NAV10 DECAPS	
DATE: Mon Apr 15 05:58:04 2019	REV: 1.00
SHEET NUMBER: 27 OF 35	
DOCUMENT NUMBER: 105_D190w_00A	
NOTES: NOTE	

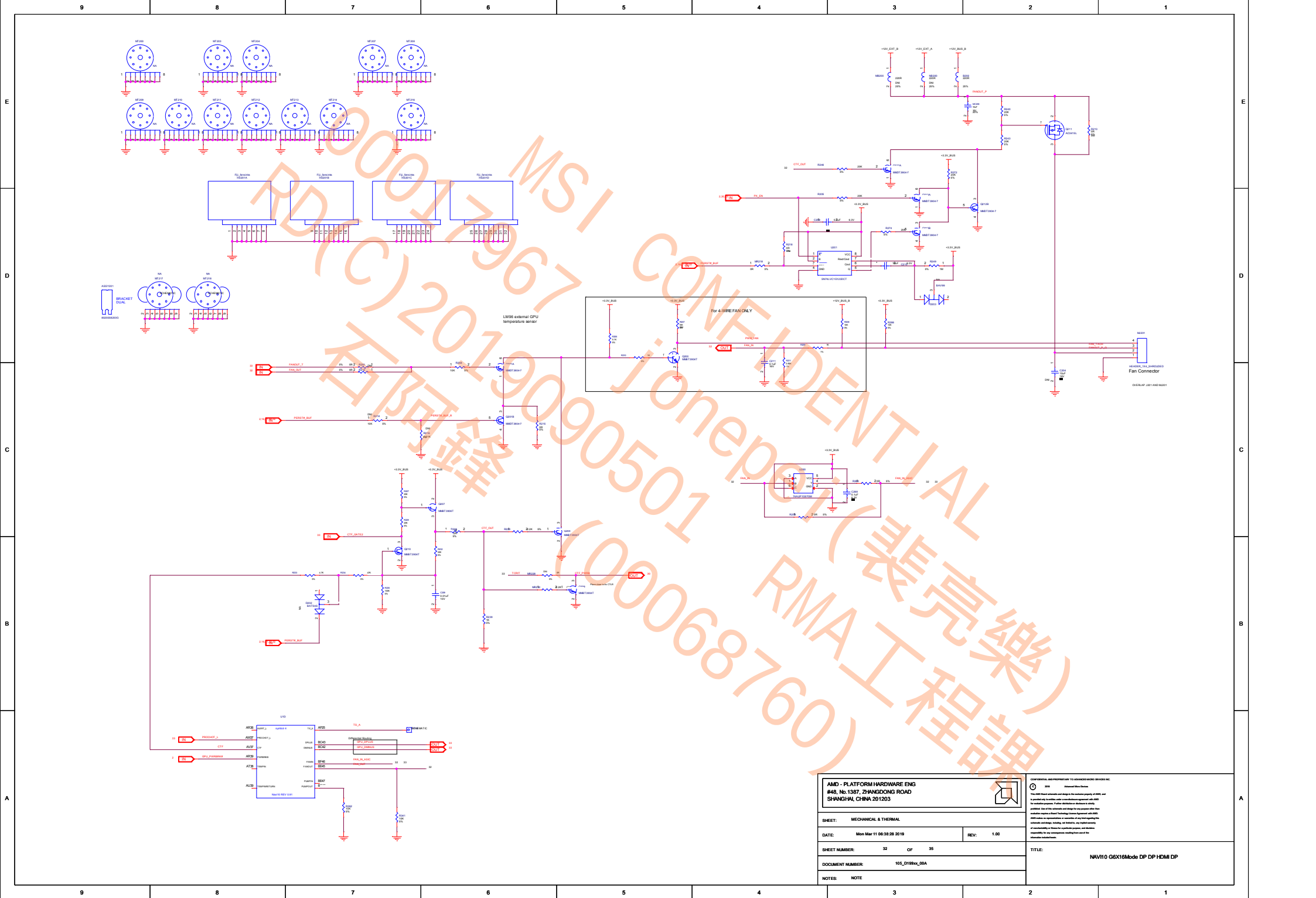
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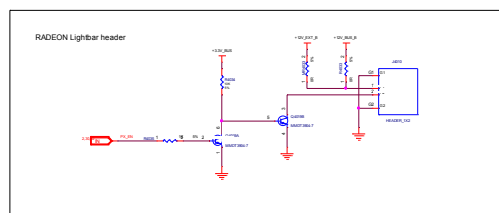
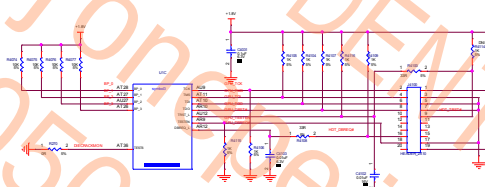
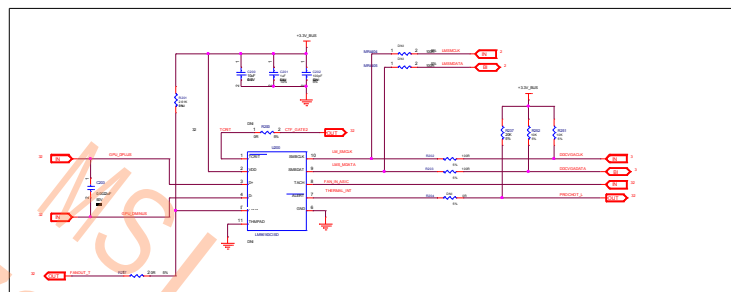
TITLE: NAV10 G8X16Mode DP DP HDMI DP











ADD: PLATONIA HANGWAIRI ERI HSE, 16, TONY, ZAWANGLO ROAD SHANSHAN CHINA 201253			① 014 [Redacted] [Redacted] [Redacted] [Redacted] [Redacted] [Redacted] [Redacted] [Redacted] [Redacted]
SHEET: 01/04	DATE: Monday 11/08/2019		REV: 1.0
SHEET NUMBER: 33		TITLE: NAVIS GETTABLES CP/CP/CM DP	
DOCUMENT NUMBER: NLSH_014			

REVISION HISTORY

DOCUMENT NUMBER: 105_D199xx_00A

DATE: Mon Mar 11 03:19:21 2019

SHEET NUMBER: 35 OF 35

REV: 1.00

ENGINEER: PEAK, DONG

NOTES:	NOTE
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