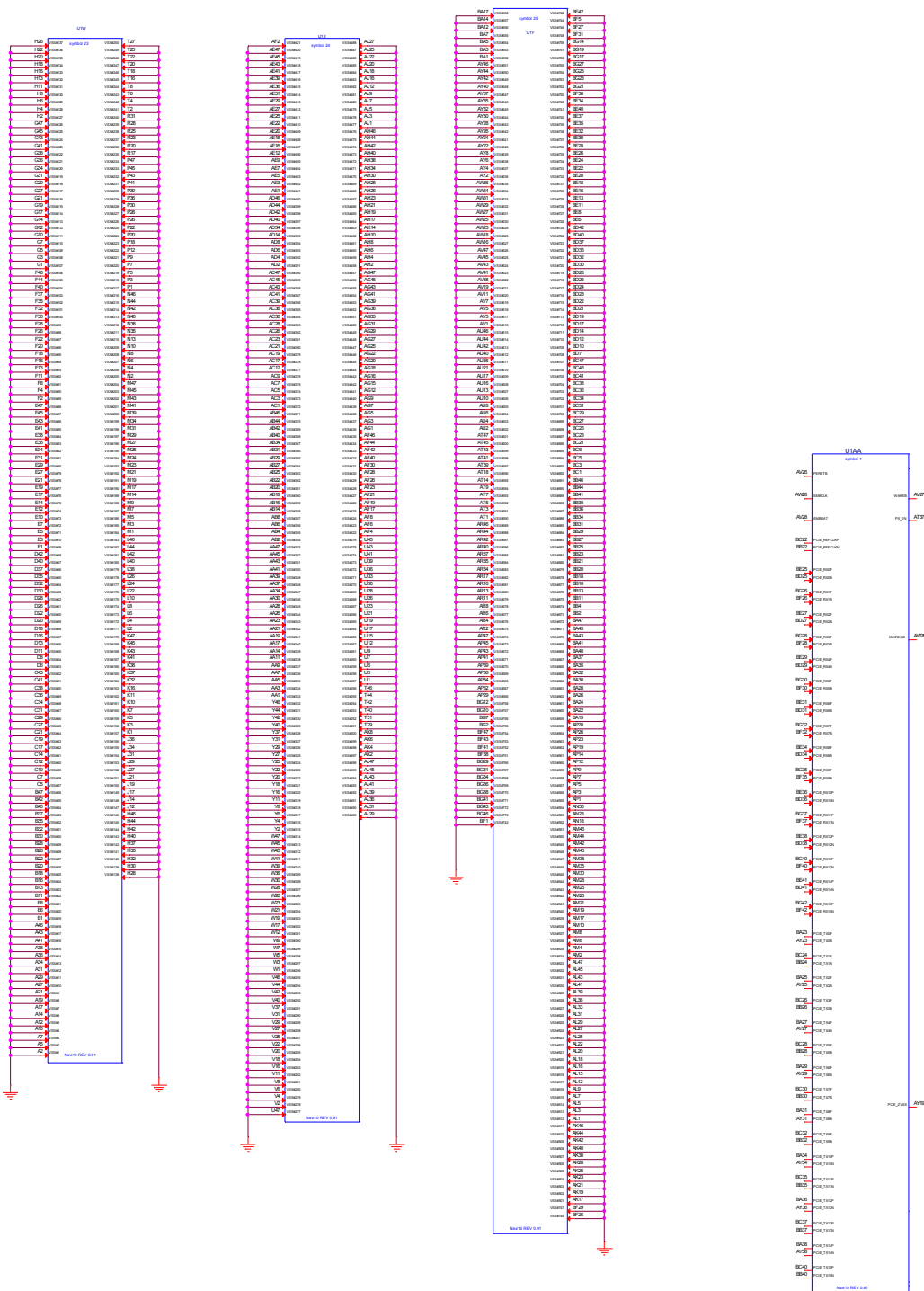




8	7	6	5	4	3	2	1
D							D
C							C
B							B
A							A

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22	REG MVDD		
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24	REG 0.75V		
25	REG 1.8V		

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SHANGHAI, CHINA 201203



SHEET: TOC

DATE: Fri Jun 21 05:25:22 2019REV: 1.00

SHEET NUMBER: 1 OF 35

DOCUMENT NUMBER: 105_0190w_008

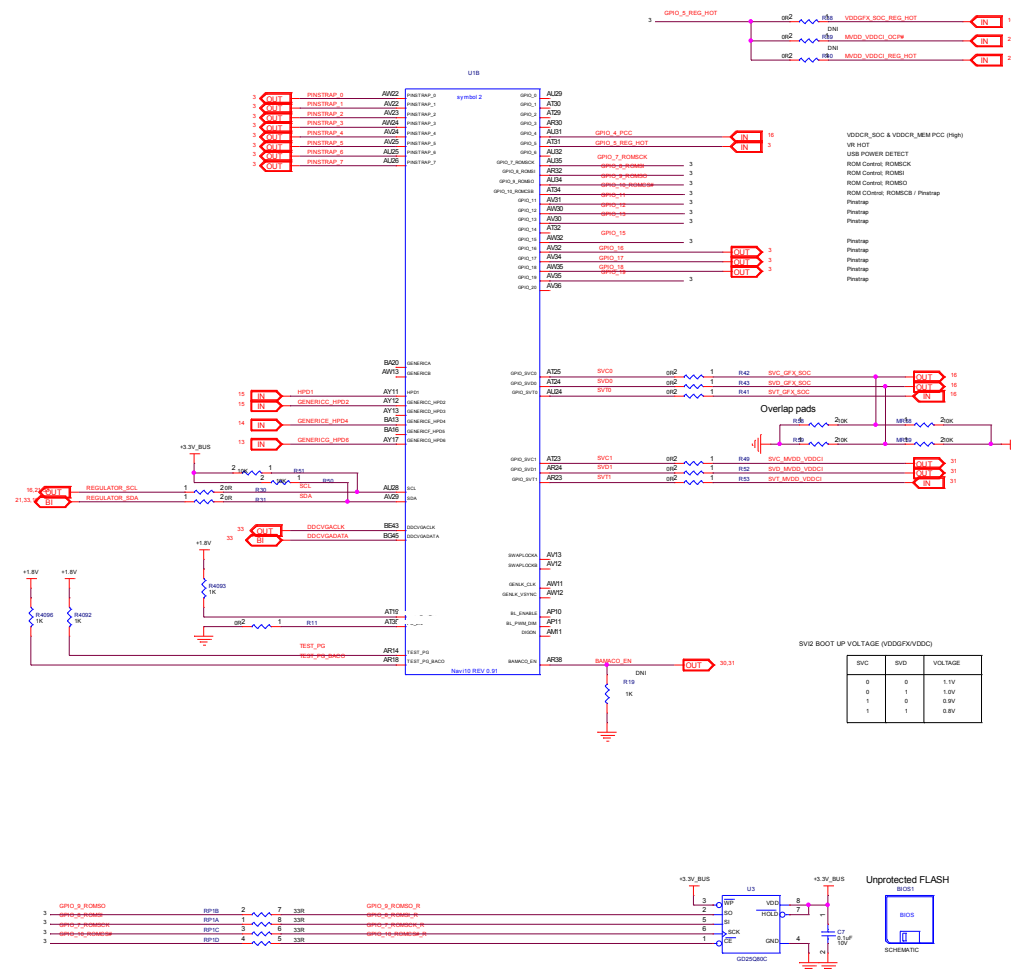
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TITLE:
NAV110 G8X16mode DP DP HDMI DP

(1) NAVI10 PCIe Edge Connector





User		Internal Default Value		Definition	
BIF					STRAP_BIF_GEN4_DIS_A 0: PCIe GEN 4 supported 1: PCIe GEN 4 not supported PINSTRAP_BIF_CLK_PM_EN 0: CLKREQ power management capability is disabled 1: CLKREQ power management capability is enabled PINSTRAP_BIF_LC_TX_SWING PINSTRAP_BIF_VGA_DIS 0: VGA controller capacity enabled 1: The device won't be recognized as the system's VGA controller
					PINSTRAP_AUD_PORT_CONN[2:0] Number of audio-capable display outputs 0: All endpoints connected 1: 6 endpoints connected 2: 5 endpoints connected 3: 4 endpoints connected PINSTRAP_AUD[1:0] 0: Audio for DisplayPort only 1: Audio for DisplayPort and HDMI if single is detected 2: Audio for DisplayPort and native HDMI 3: Audio for DisplayPort and native HDMI
SMU					PINSTRAP_SMBUS_ADDR 0: South 1: North
					PINSTRAP_BIOS_ROM_EN 0: Enable the external BIOS ROM device 1: Enable the external BIOS ROM device



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SHEET: NAV10 GPIO:

DATE: Fri Jun 21 05:25:23 2019

REV: 1.00

SHEET NUMBER

3 OF 35

DOCUMENT NUMBER:

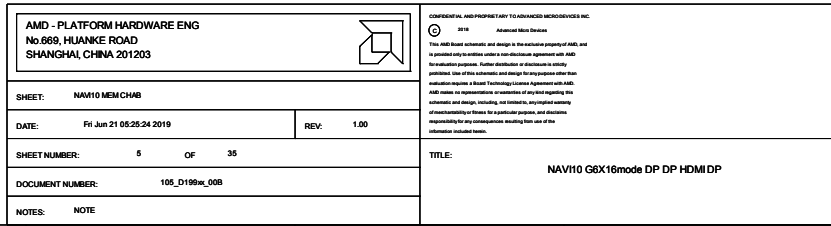
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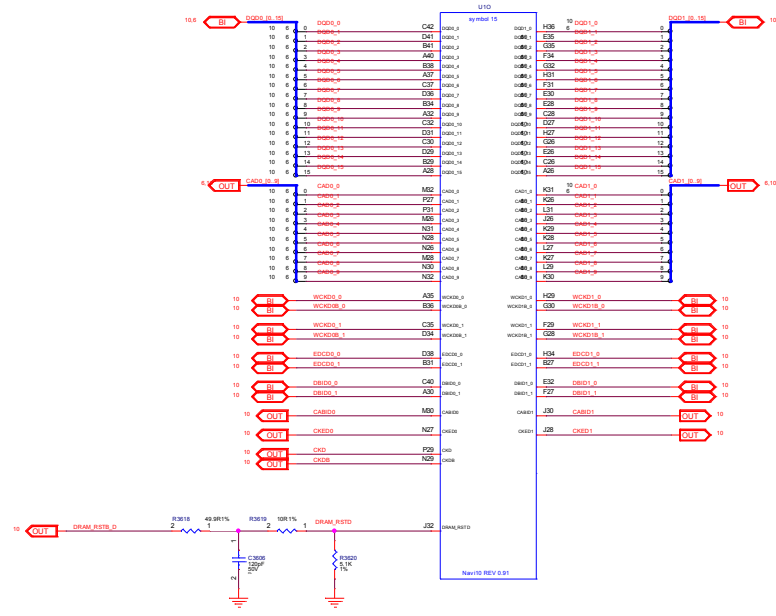
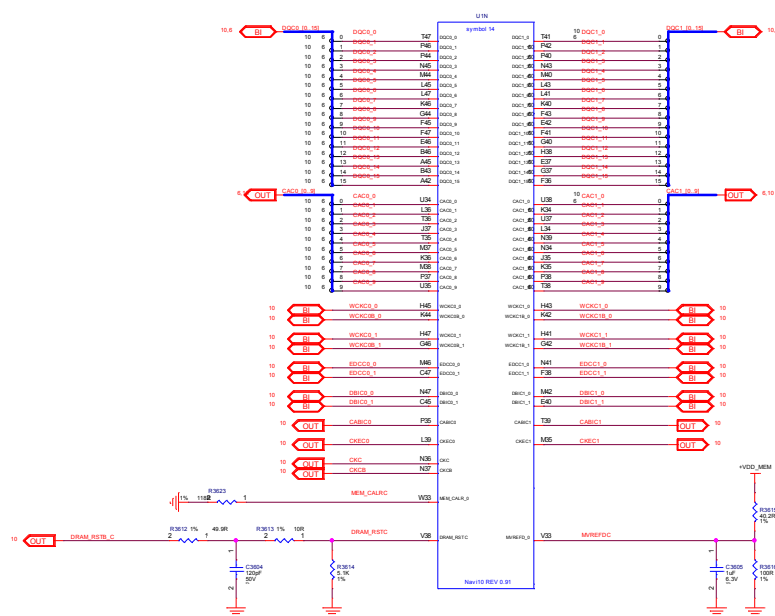
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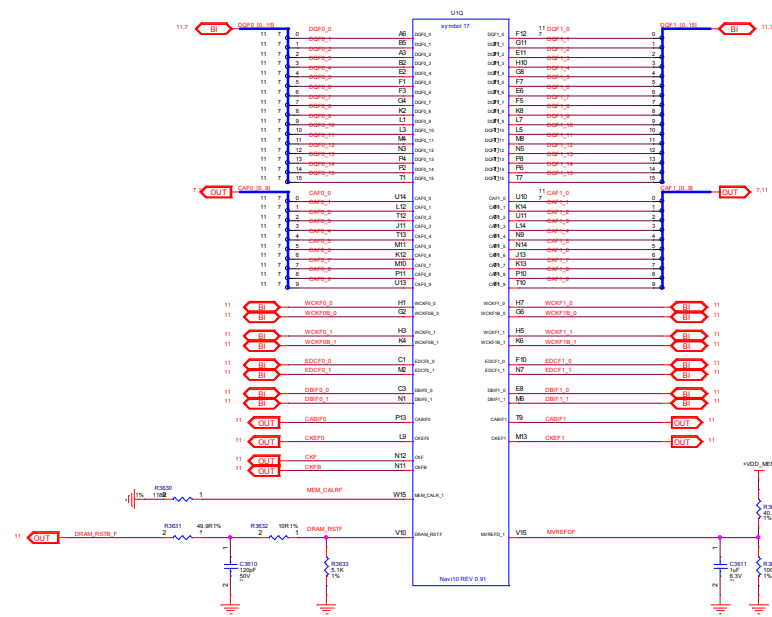
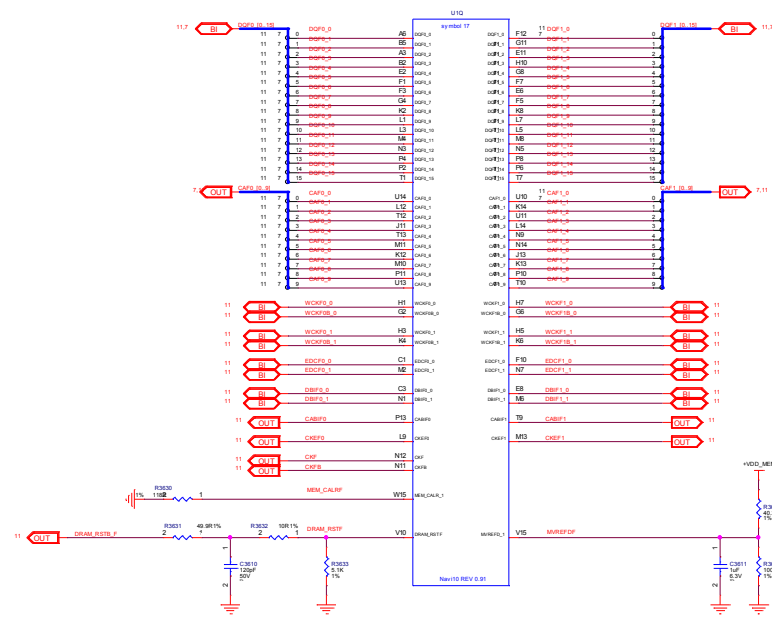
NAVI10 G6X16mode DP DP HDMI DP



NAVI 10 MEM INTERFACE CH C/D



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SHEET: NAVI10 MEM CHCD		
DATE: Fri Jun 21 05:25:24 2019	REV: 1.00	
SHEET NUMBER: 6 OF 35		
DOCUMENT NUMBER: 105_0199w_008		
NOTES: NOTE		TITLE: NAVI10 G8X16mode DP DP HDMI DP



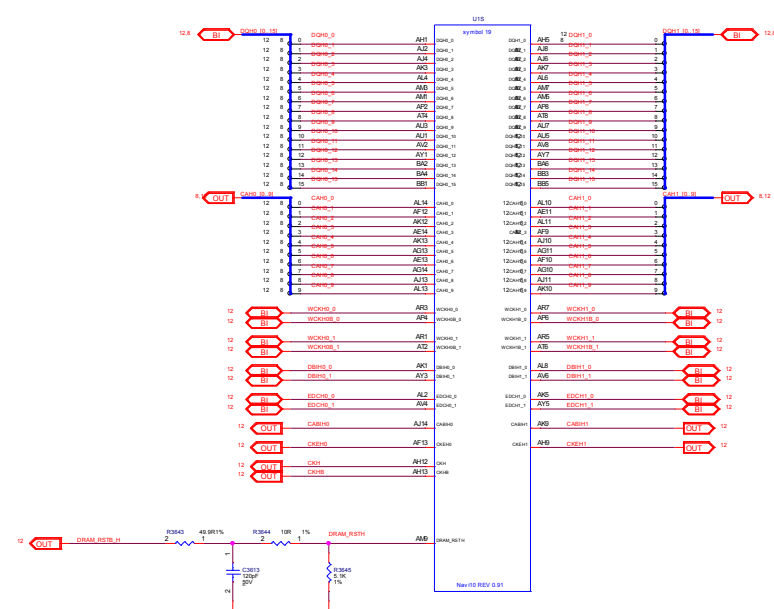
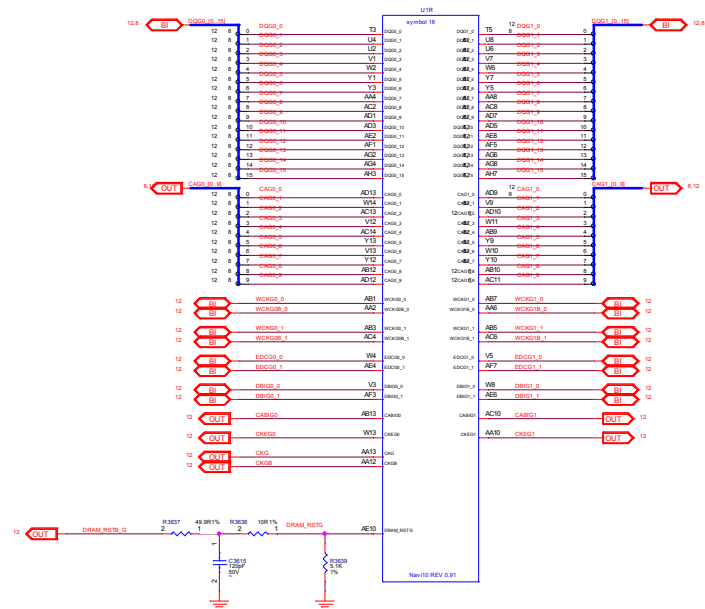
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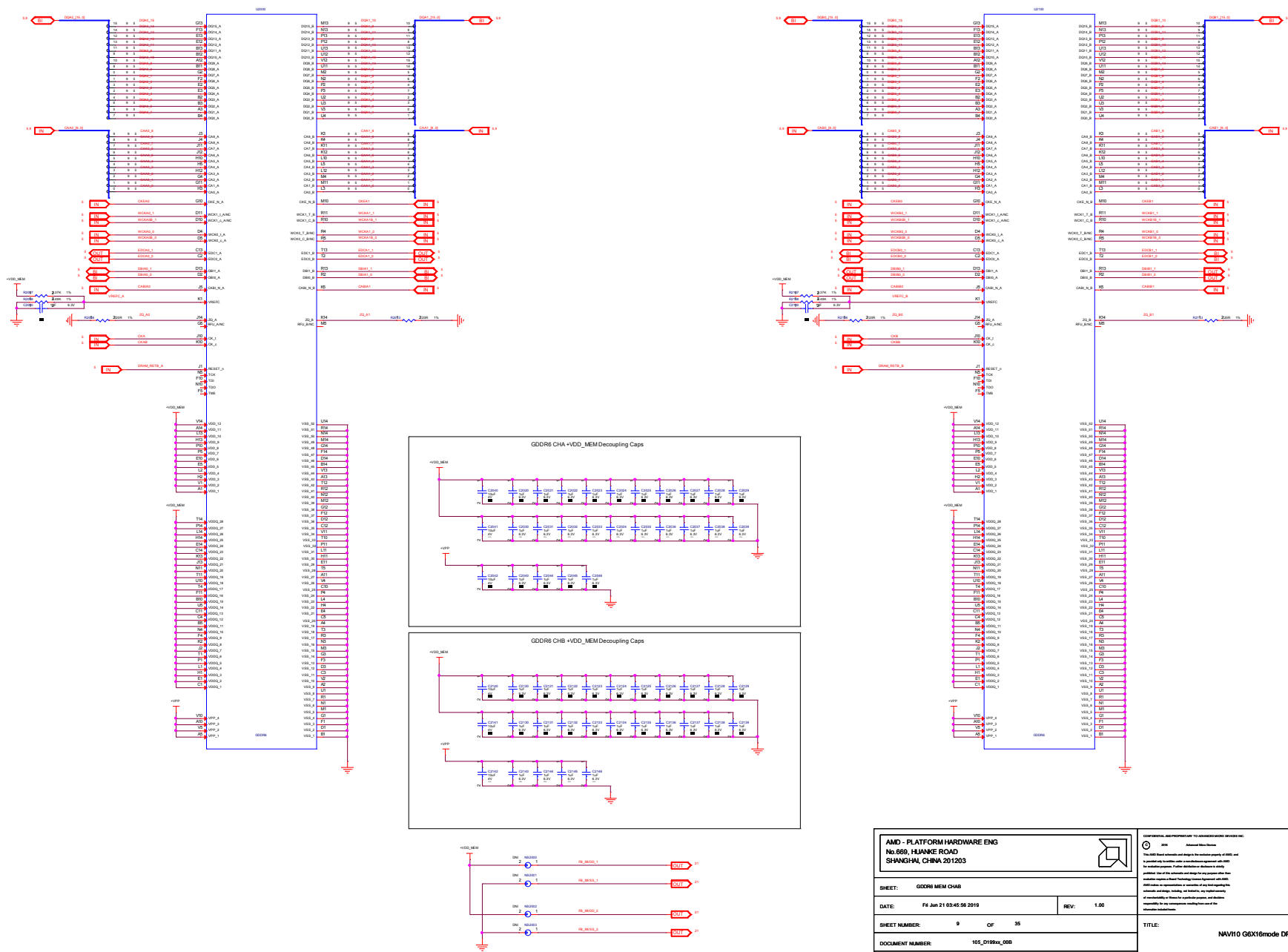
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TITLE: NAVI10 G6X16mode DP DP HDMI DP

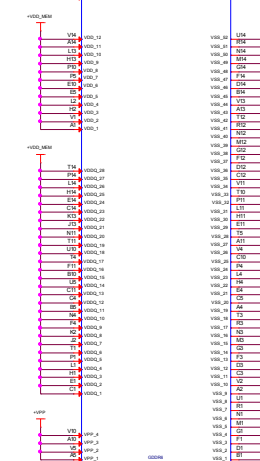
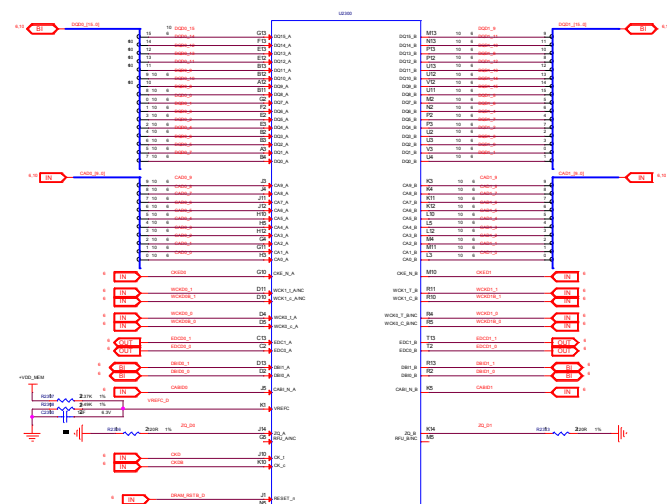
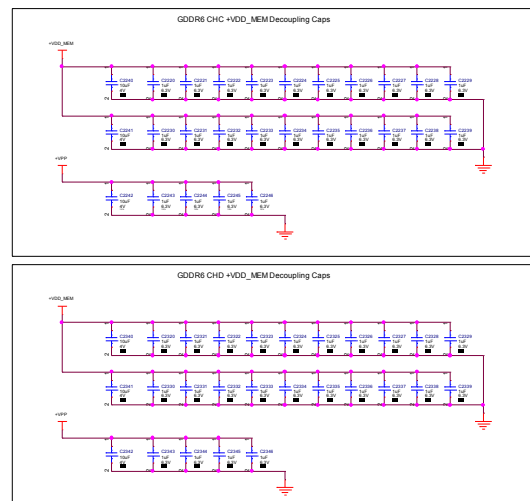
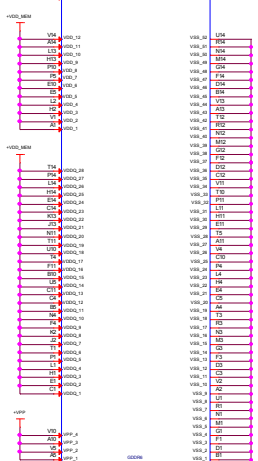
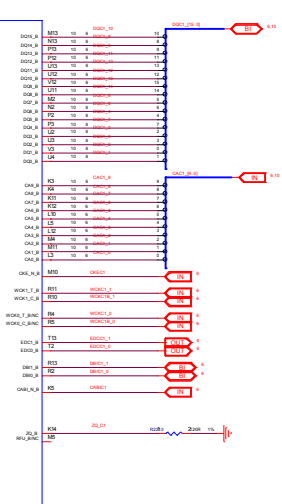
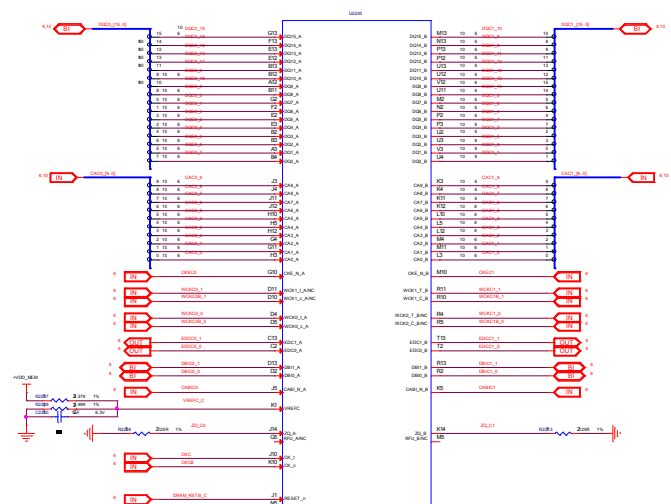
NAVI 10 MEM INTERFACE CH G/H



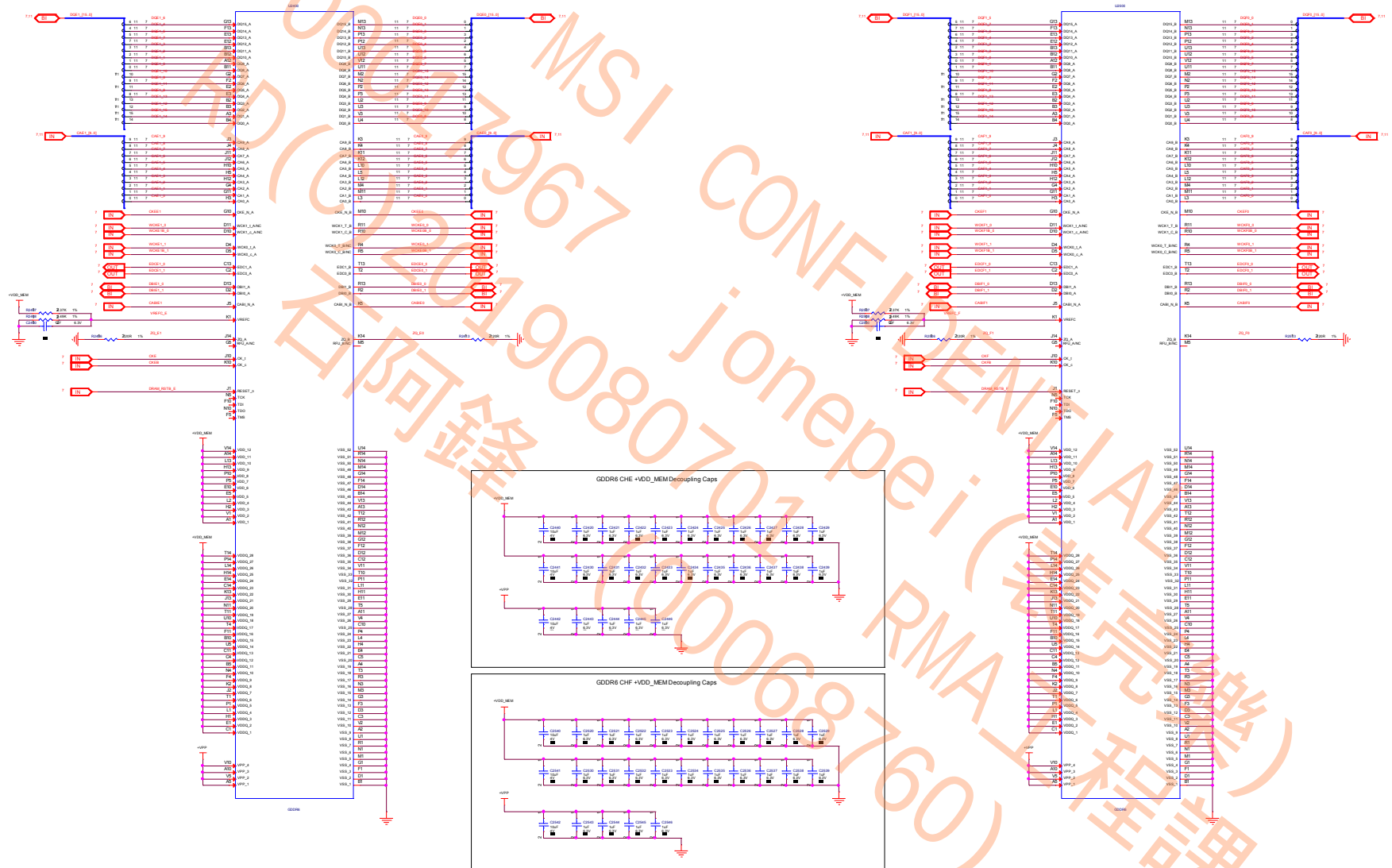
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SHEET: NAV10 MEM CHG DATE: Fri Jun 21 05:25:26 2019 SHEET NUMBER: 8 OF 35 DOCUMENT NUMBER: 105_0190w_008 NOTES: NOTE	REV: 1.00 TITLE: NAVI10 G8X16mode DP HDMI DP		

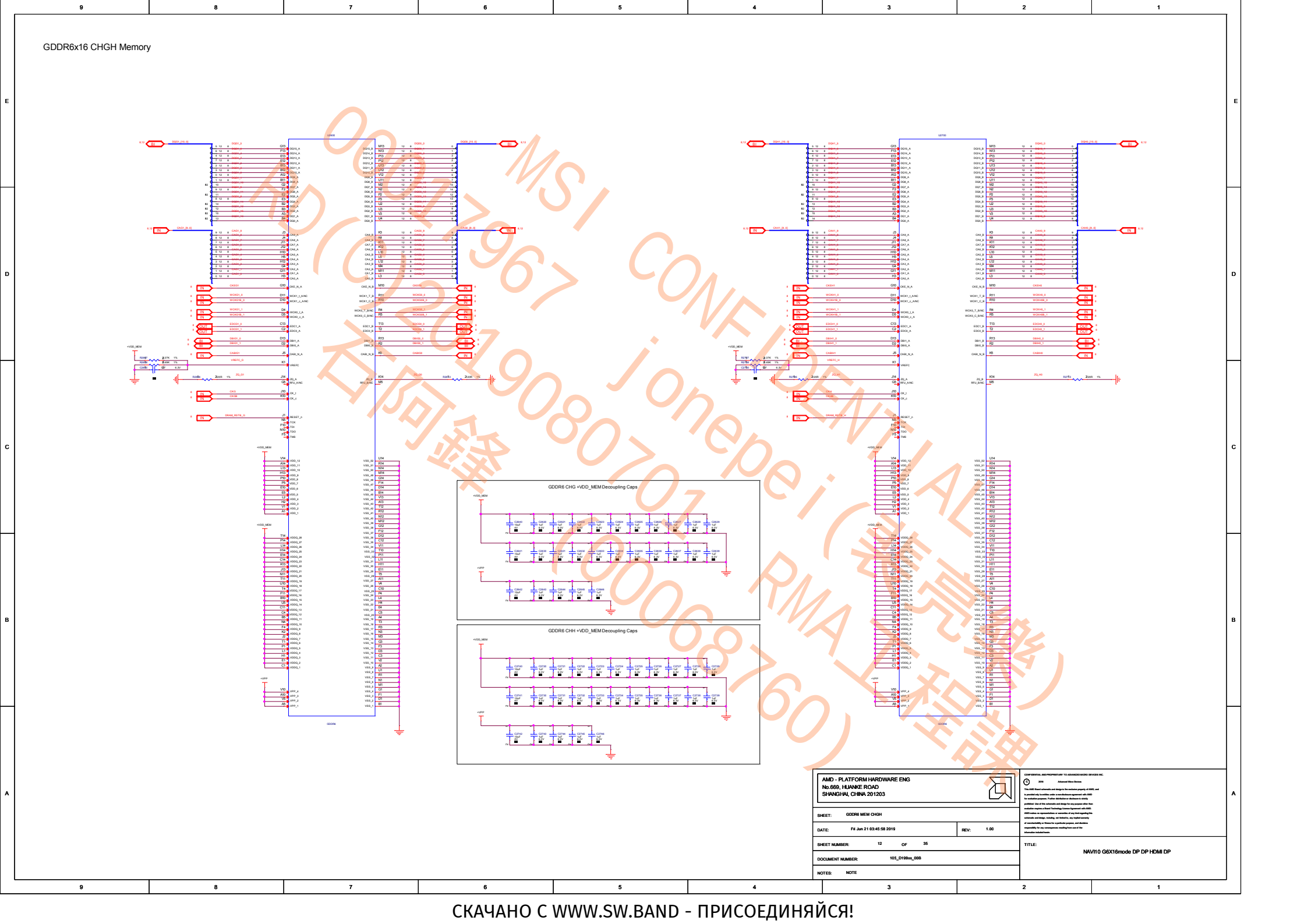


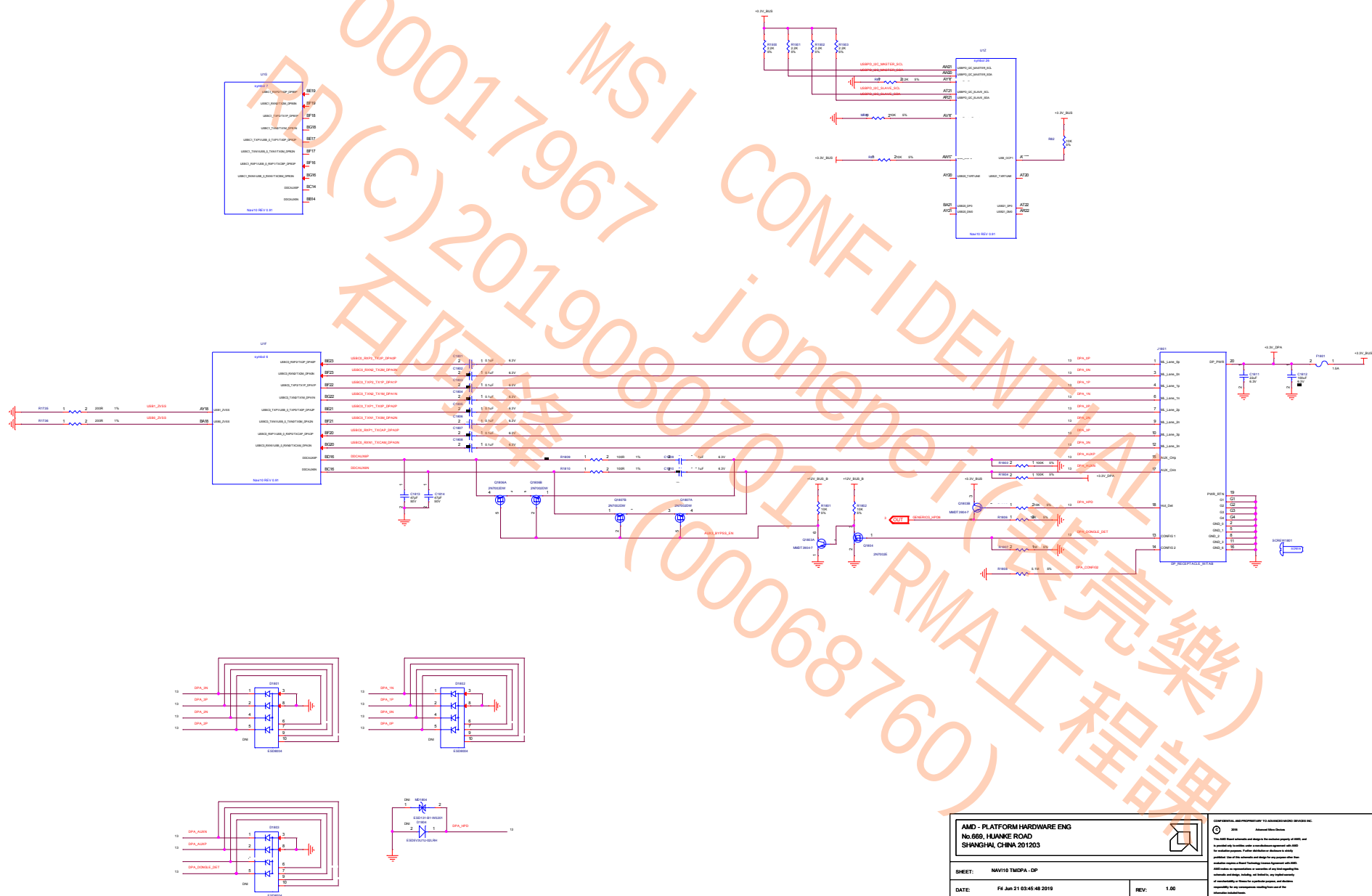
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SHEET: GDDR6 MEM CHAB		REV: 1.00	
DATE: Fri Jan 21 03:45:56 2019		TITLE: NAV10 G6X16mode DP DP HDMI DP	
SHEET NUMBER: 9 OF 35			
DOCUMENT NUMBER: 105_0198x_888			
NOTES: NOTE			



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SHEET: GD001 MEM CHCD				This AMD Board schematic and design is the exclusive property of AMD, and is controlled only by written order. AMD disclaims any responsibility for the information provided. This AMD Board schematic is provided for the customer's reference only. AMD disclaims any responsibility for the information provided. This AMD Board schematic is provided for the customer's reference only. AMD disclaims any responsibility for the information provided.	
DATE: Fri Jun 21 03:45:58 2019		REV: 1.00			
SHEET NUMBER 10 OF 35				TITLE: NAV10 G6X16mode DP DP HDMI DP	
DOCUMENT NUMBER 105_D199xx_008					
NOTES: NONE					







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No.009, HUANKE ROAD
SHANGHAI, CHINA 201203

SHEET: NAVI10 TMDP A - DP

DATE: F4 Jan 21 03:45:48 2019

SHEET NUMBER: 13 OF 35

DOCUMENT NUMBER: 101_0198x_008

NOTES: NOTE

REV: 1.00

Sheet description: AMD-Platform NAVI 10 (AMD) DP A/B

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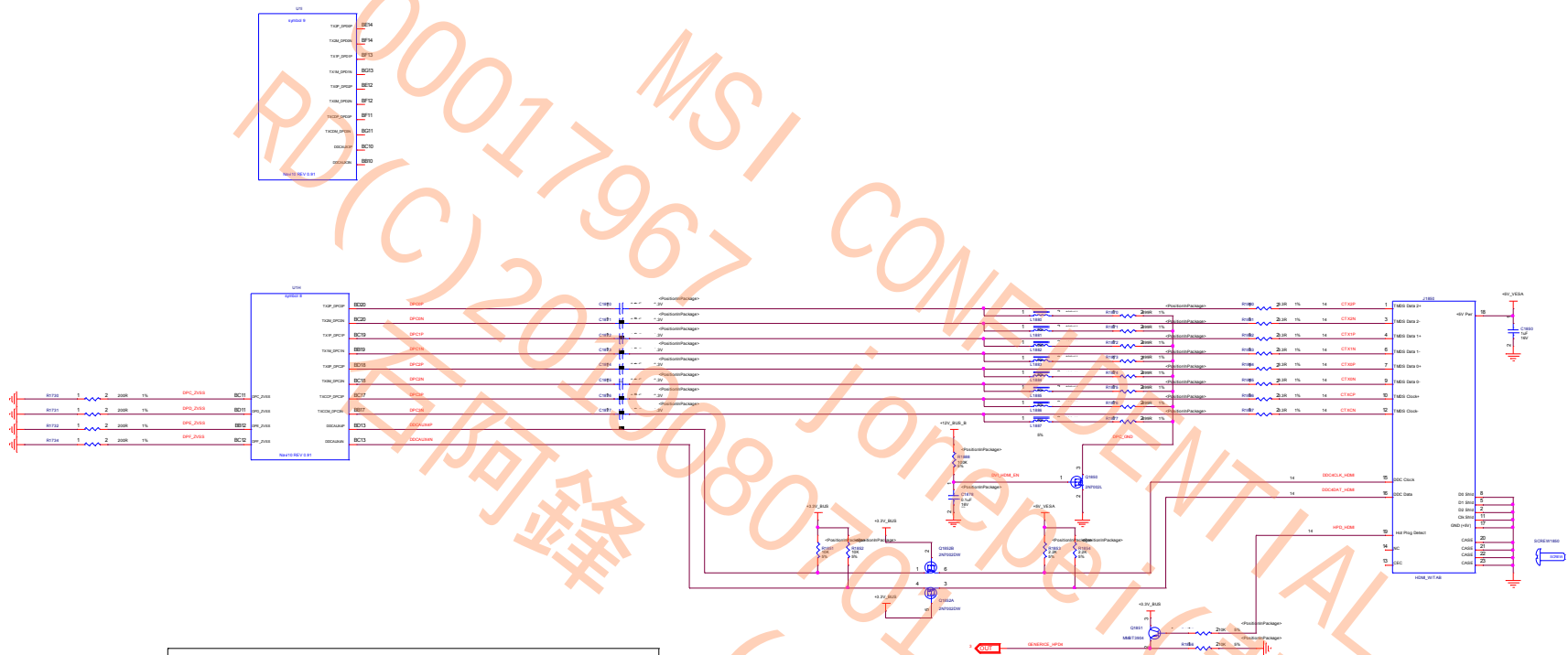
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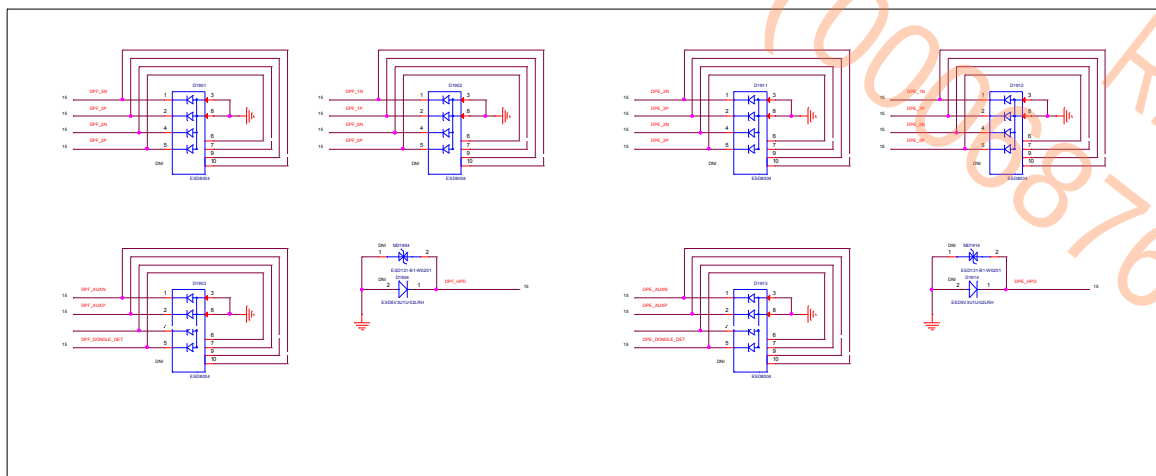
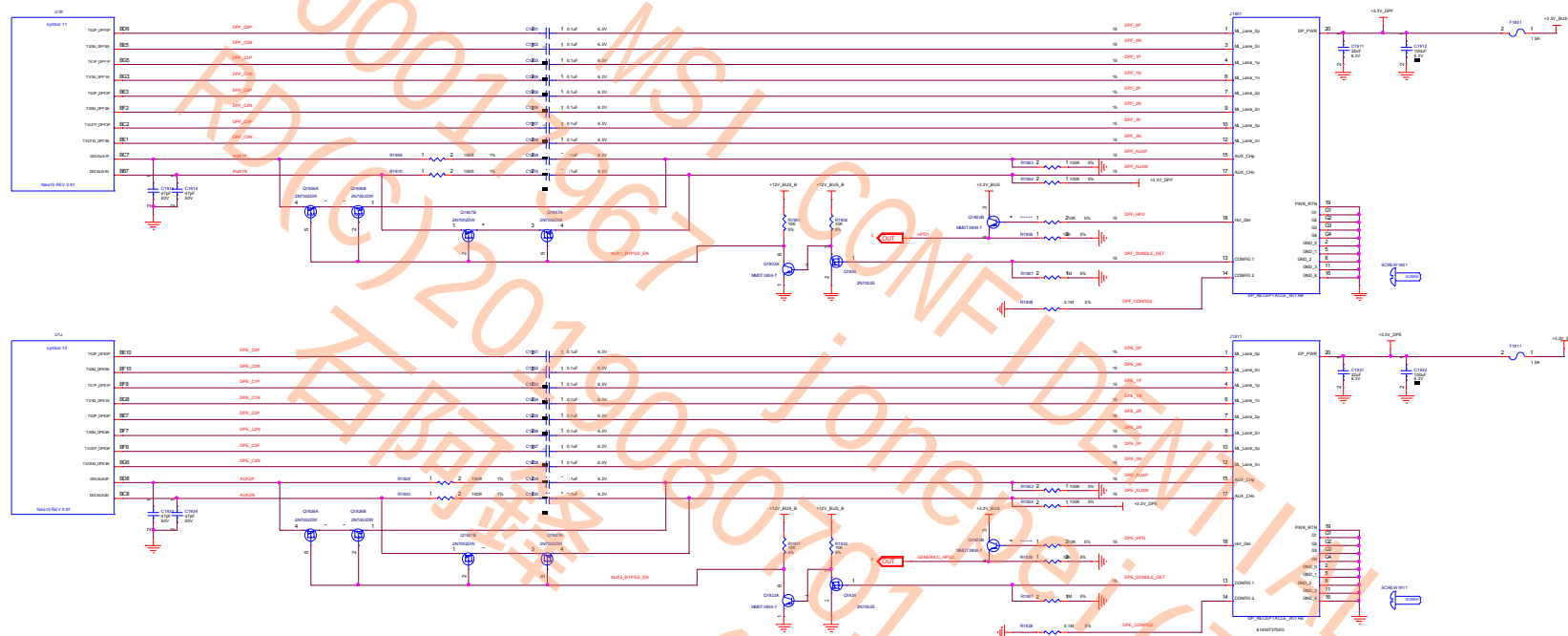
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TITLE: NAVI10 G6X16mode DP DP HDMI DP

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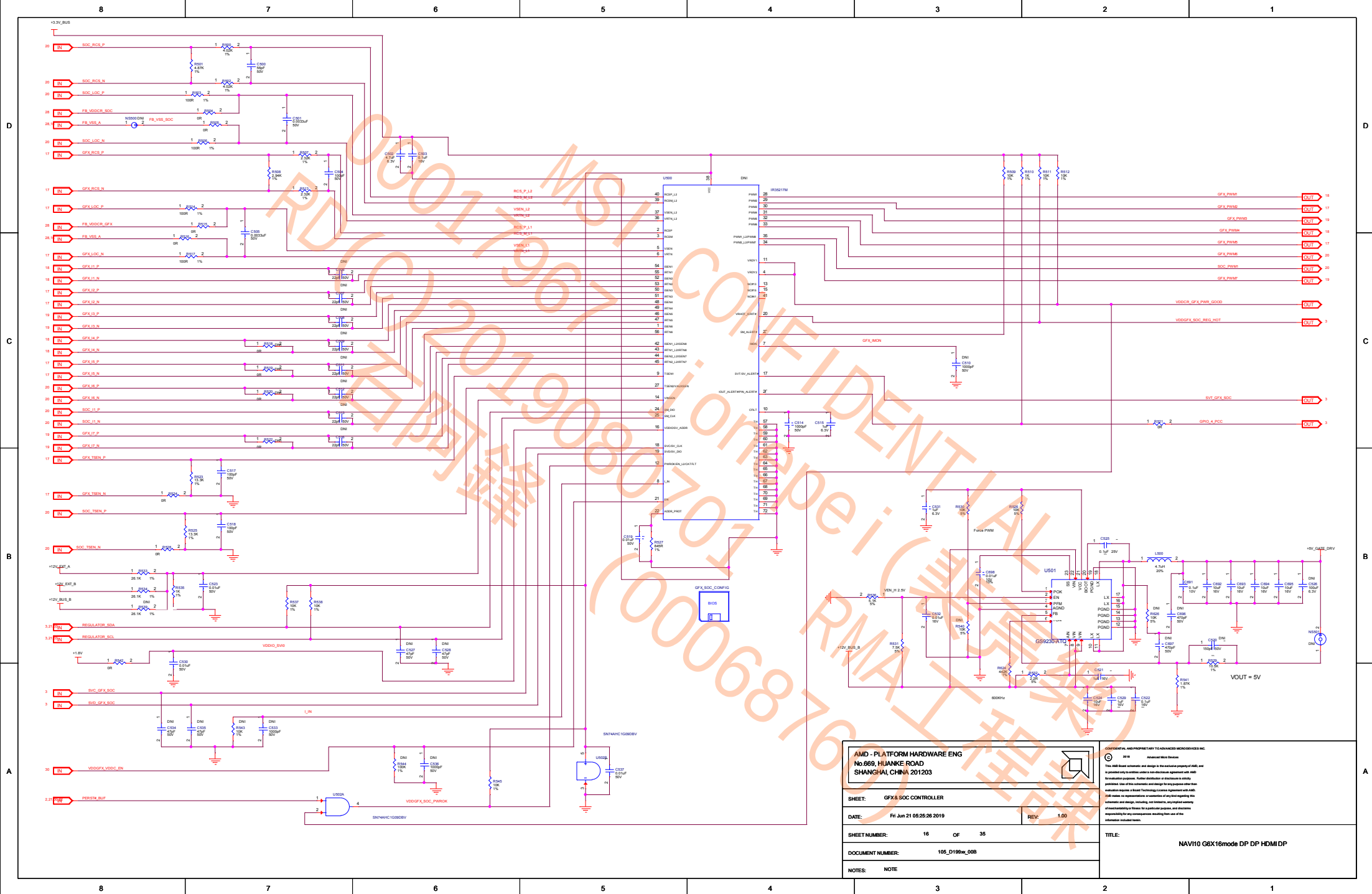
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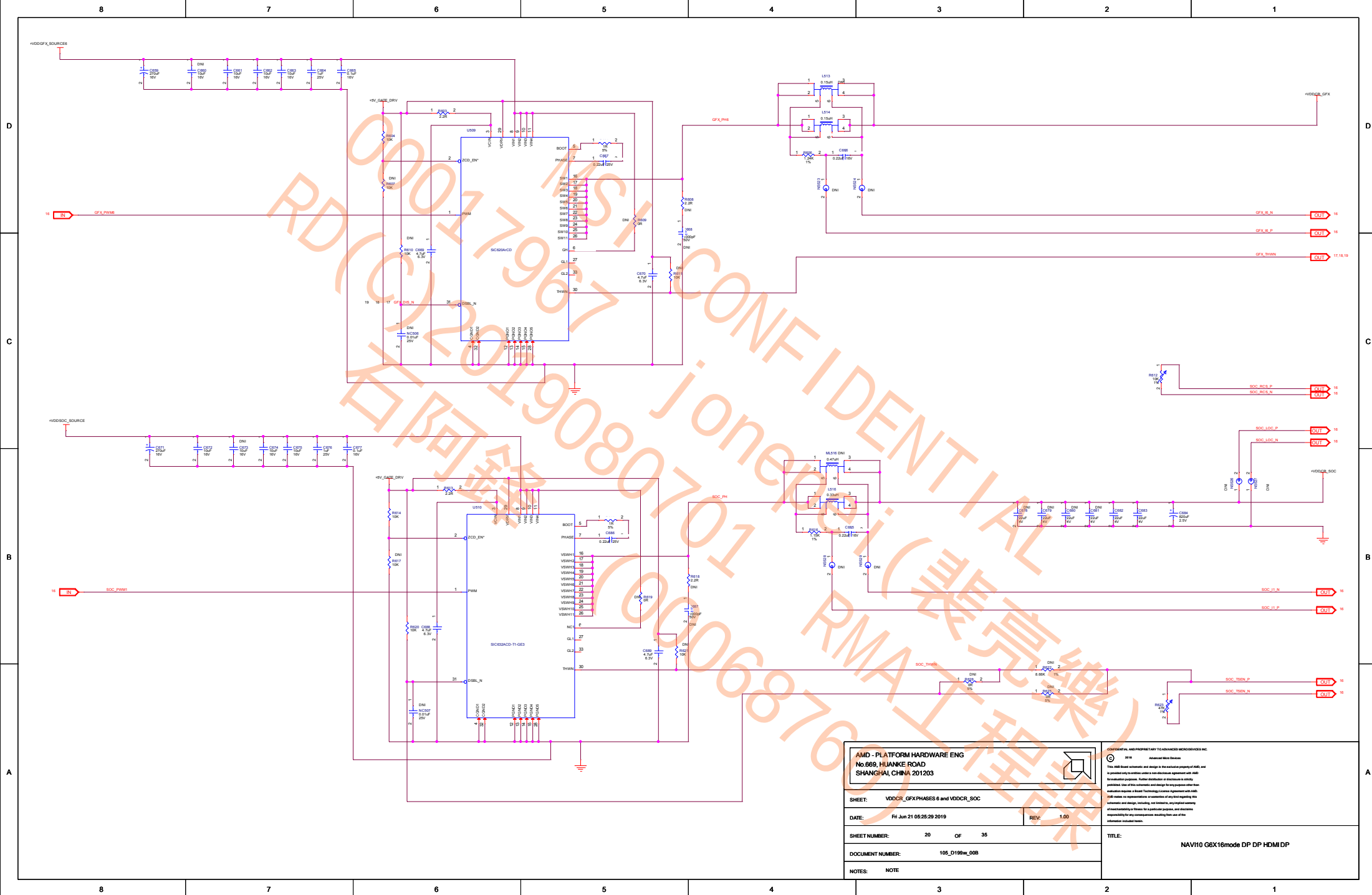


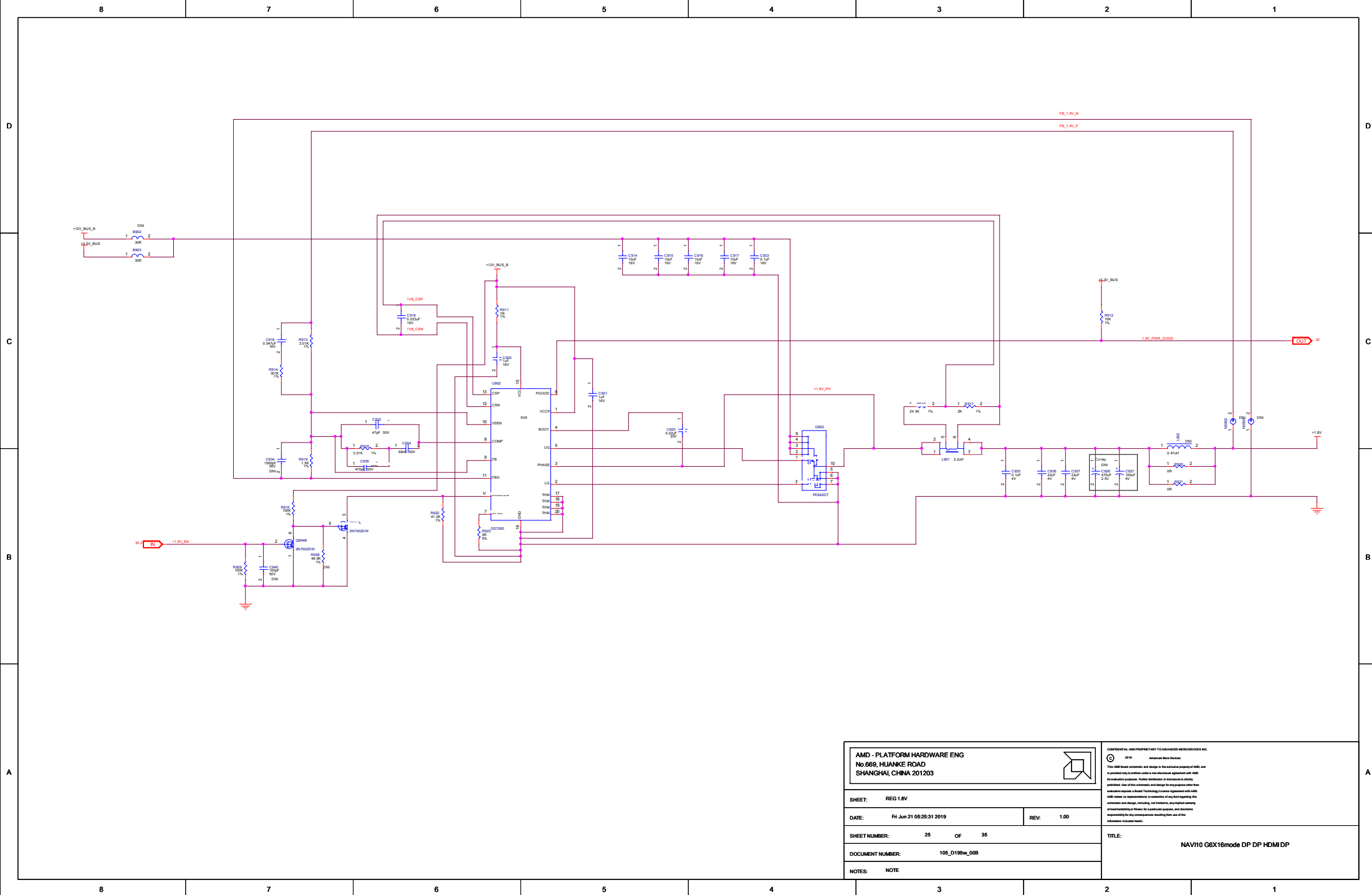
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SHEET: NAVI10 TMDPEF - DP DP	REV: 1.00
DATE: Fri Jan 21 03:45:49 2010	
SHEET NUMBER: 15 OF 35	
DOCUMENT NUMBER: 105_0100x_008	
NOTES: NOTE	

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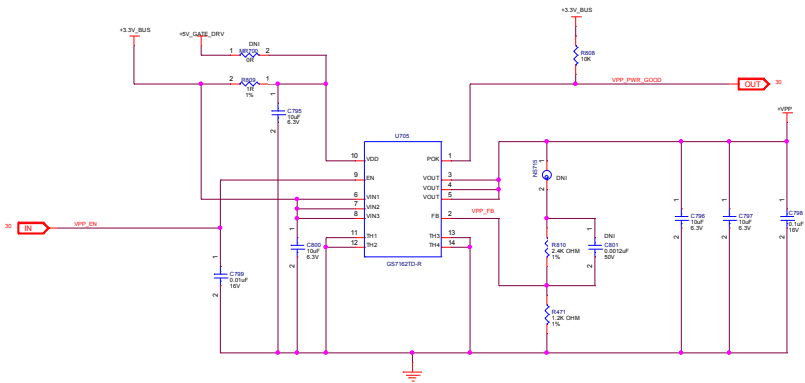
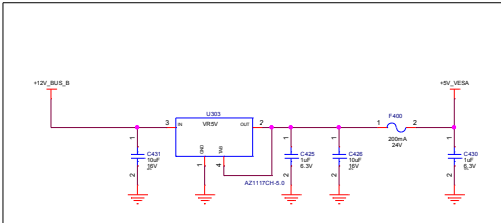
NAVI10 G6X16mode DP DP HDMI DP





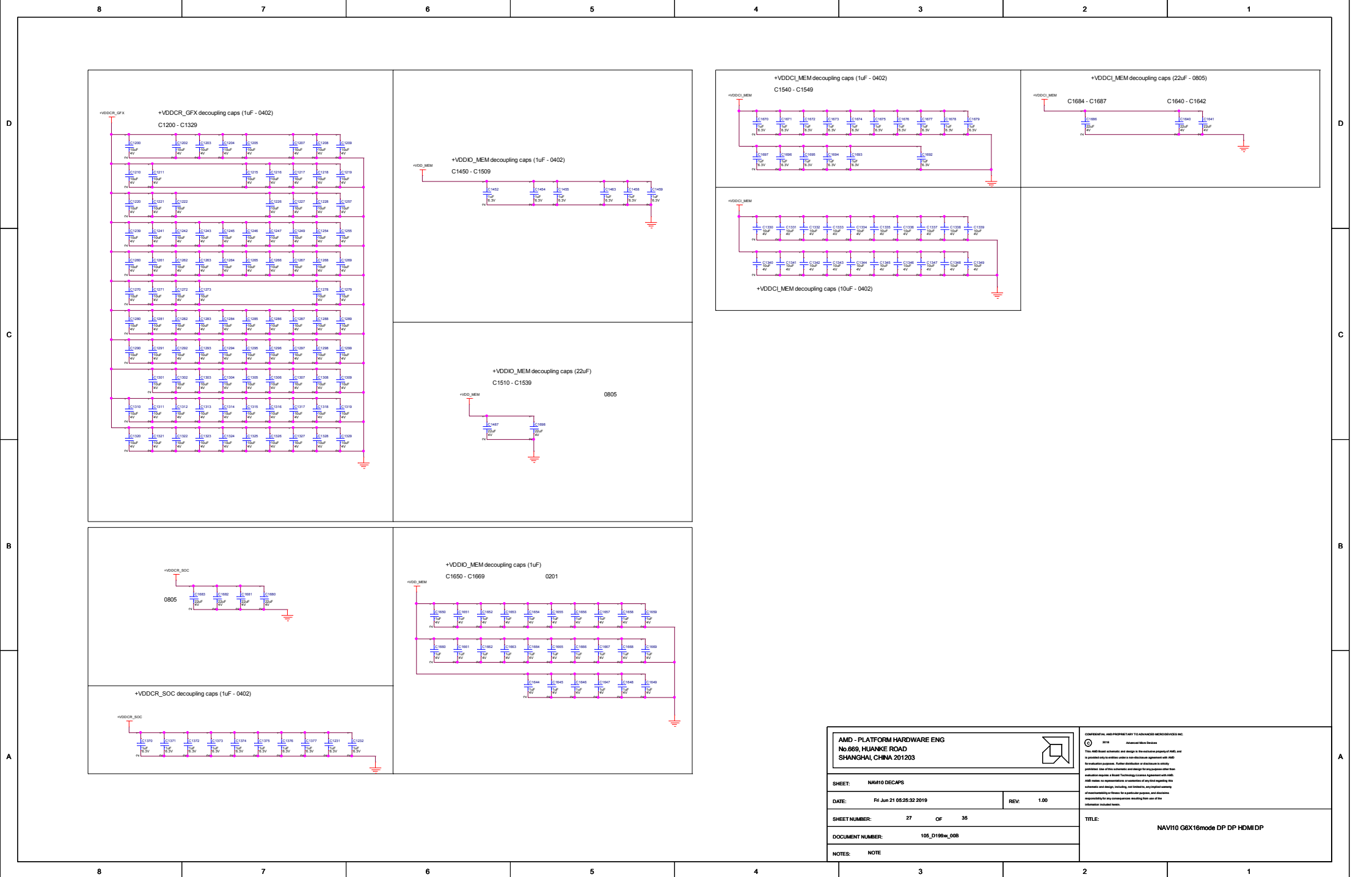


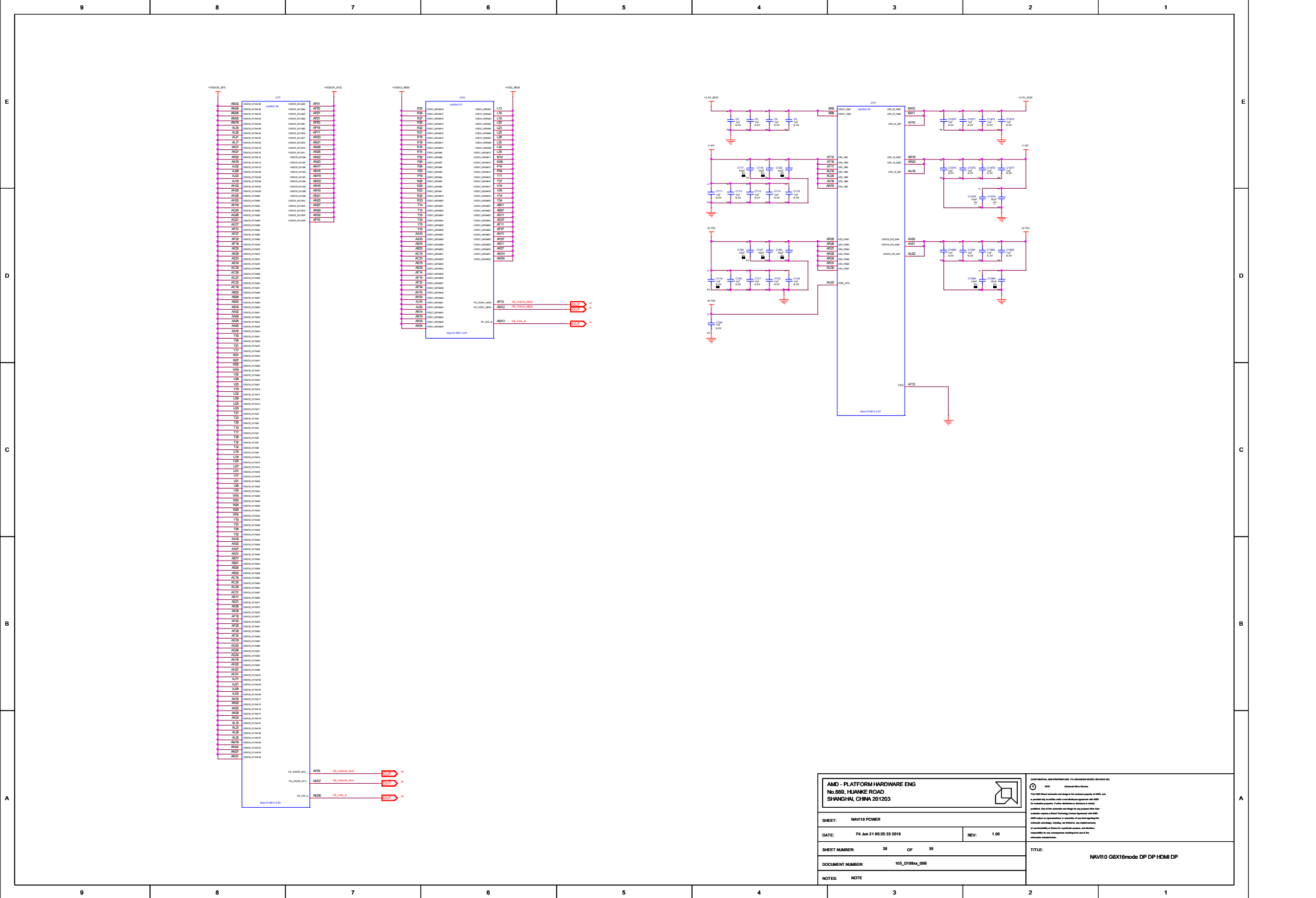
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IOUT = 50mA

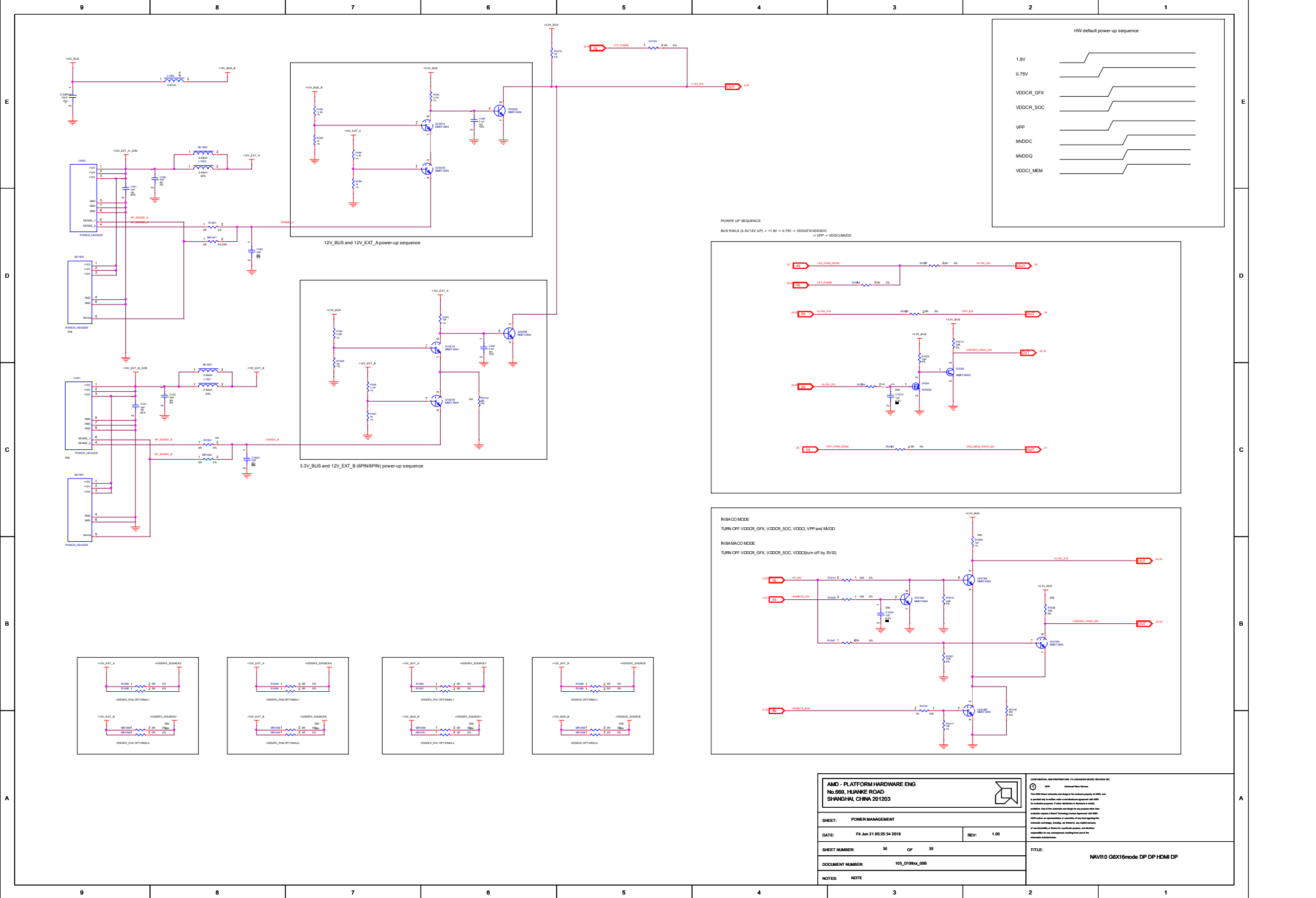


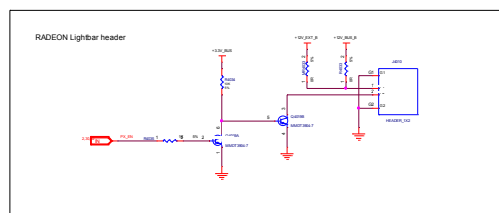
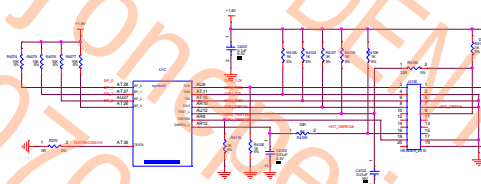
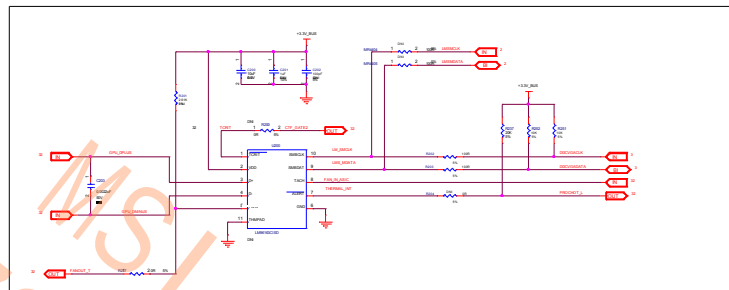
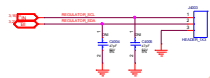
AMD - PLATFORM HARDWARE ENG No.669, HUANKE ROAD SHANGHAI, CHINA 201203	
SHEET: SMALL RAIL REGULATORS	
DATE: Fri Jun 21 05:25:32 2019	REV: 1.00
SHEET NUMBER: 26 OF 35	
DOCUMENT NUMBER: 105_0190w_008	
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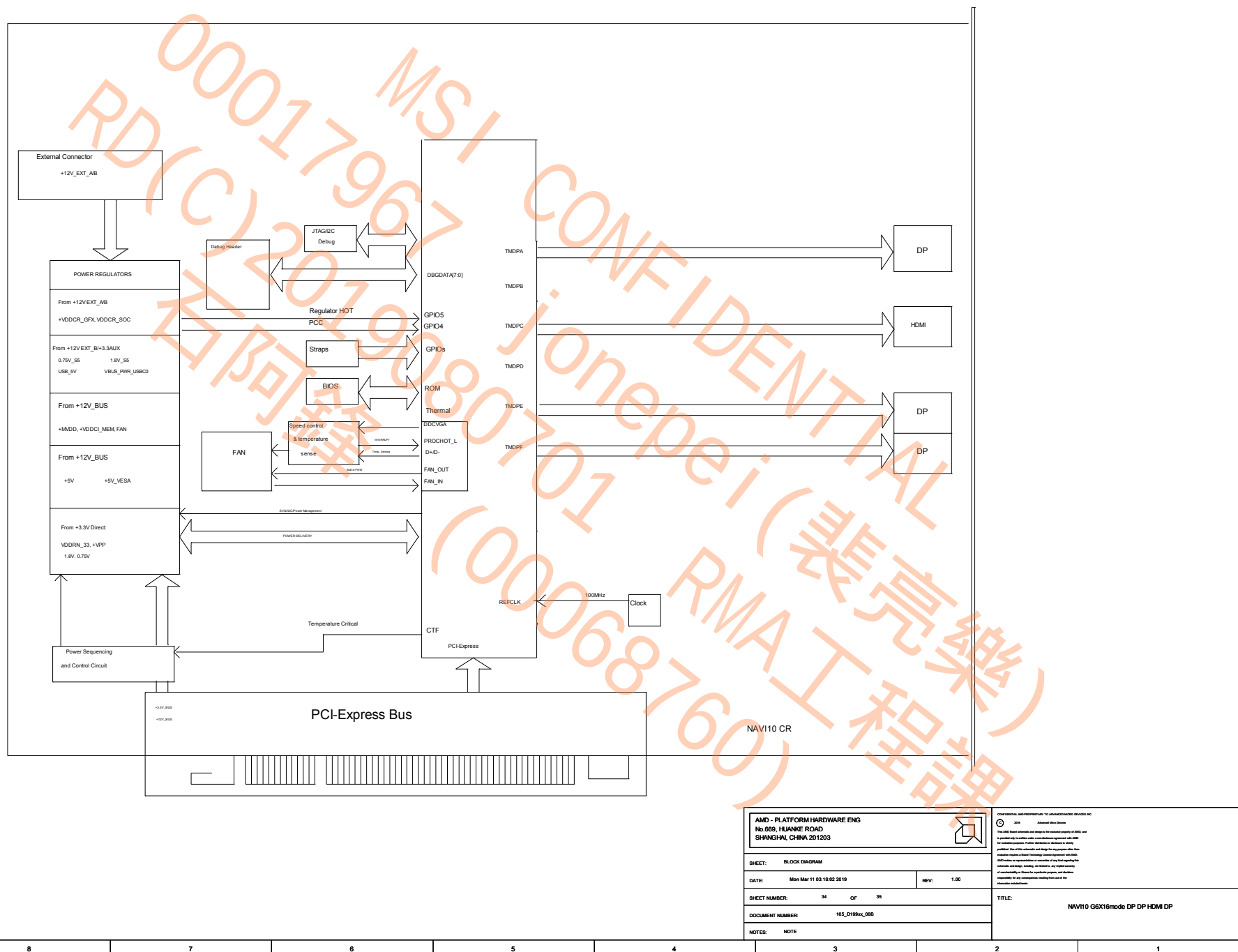








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NATURE: PLANNING REQ'D			
SHARED BY: CHINA, 2012/03			
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DATE: Fri, Jan 20 04:55:58 2012	REV: 1.0		
SHEET NUMBER: 33		TITLE:	NV10V GETMode EPICPMEM DP
DOCUMENT NUMBER: NVL_010A_000			



REVISION HISTORY

ENGINEER:

NOTES:

NOTE

105_D199xx_008

DATE: Fri Jun 21 03:44:58 2019

SHEET NUMBER: 35 OF 35

REV: 1.00

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SCH Rev	PCB Rev	Date	REVISION DESCRIPTION
1	-00A	FEB 11 2019	-00A.schematic
2	-00B	JUN 21 2019	ADD PULL HIGH ON THE PWBRKB