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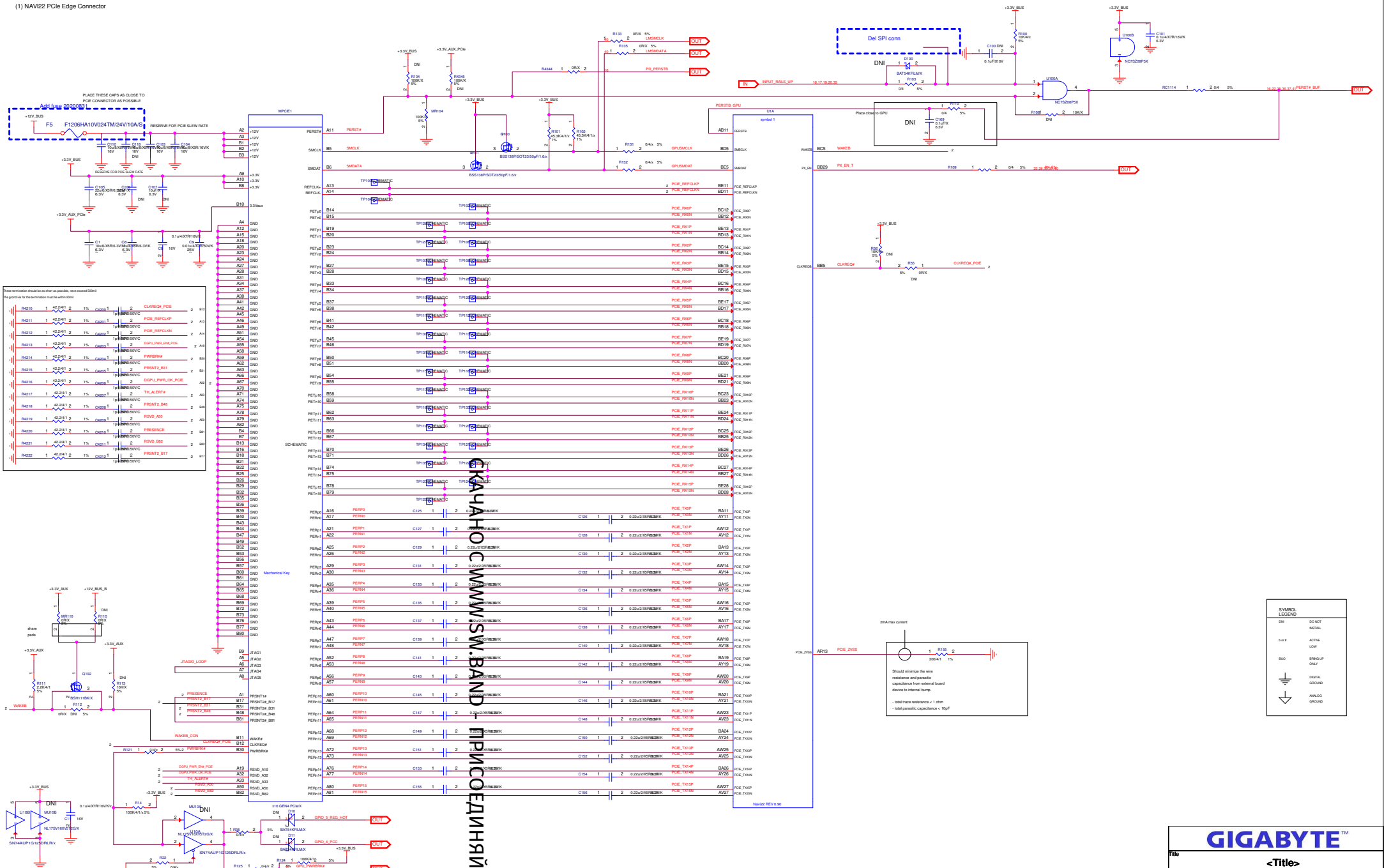
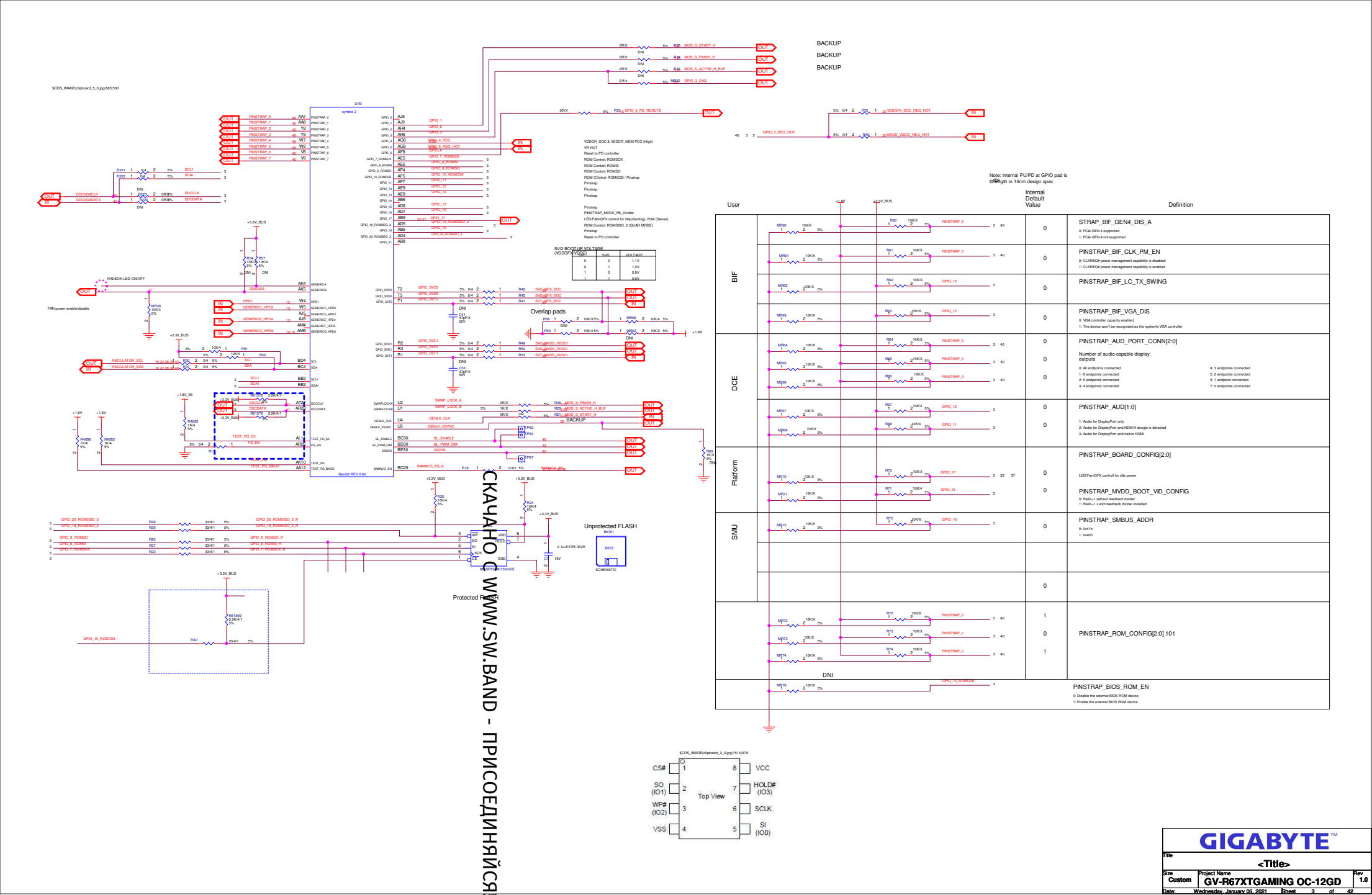
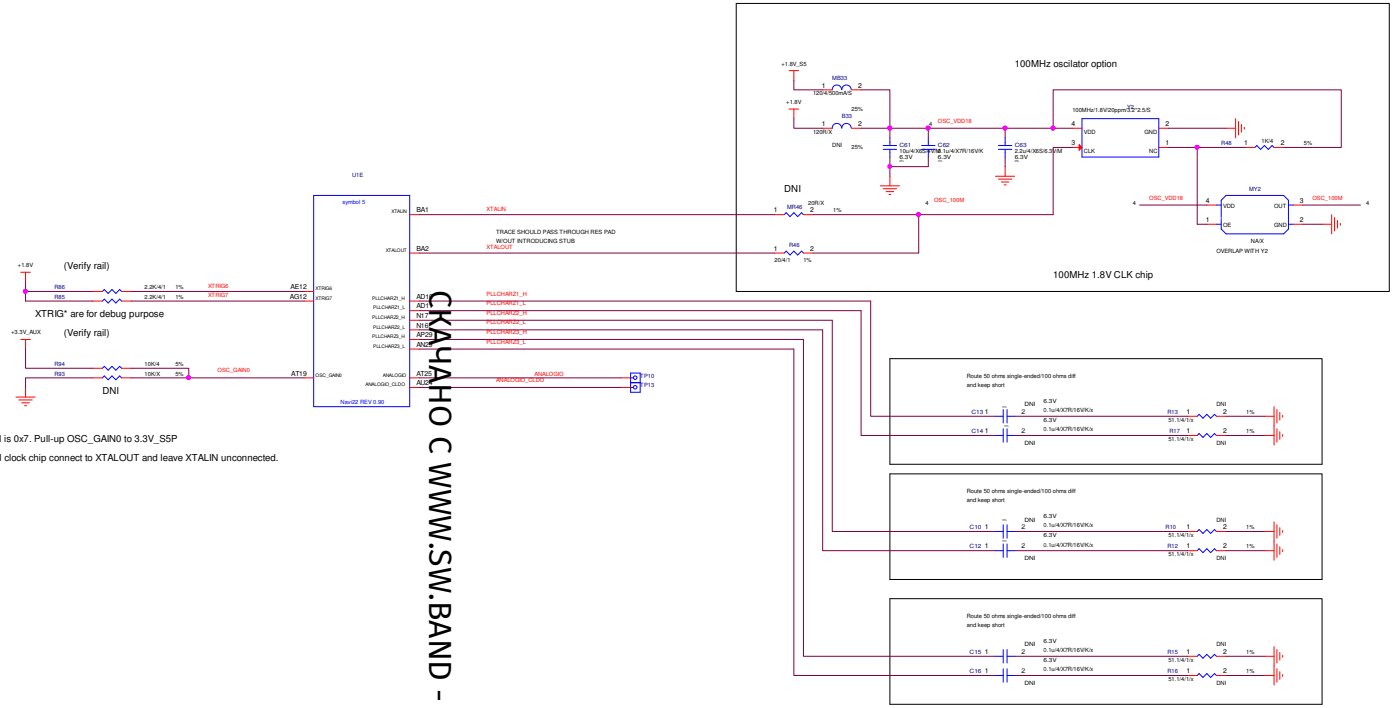
(1) NAVI22 PCIe Edge Connector







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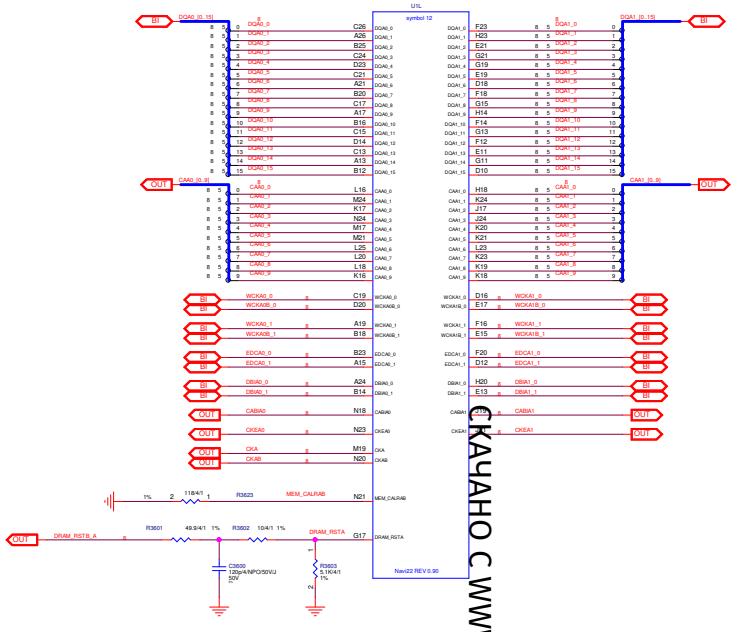

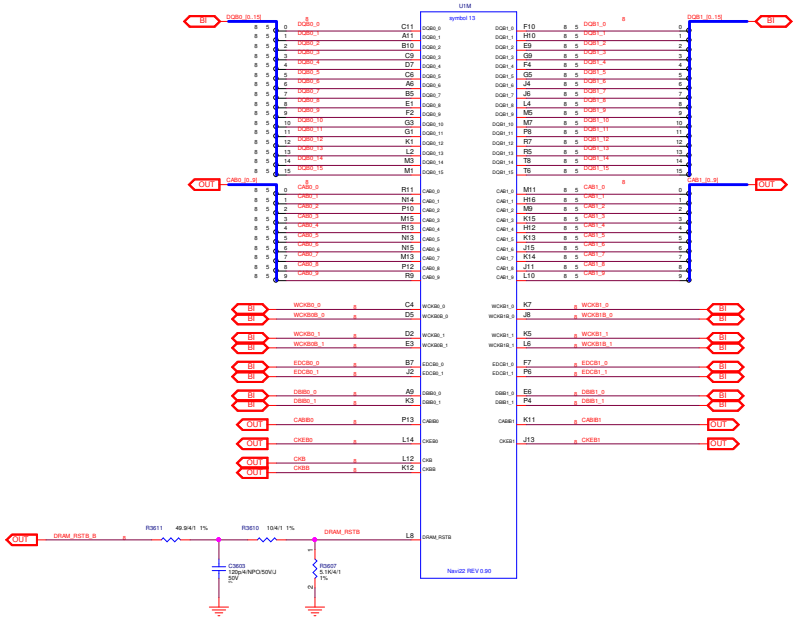

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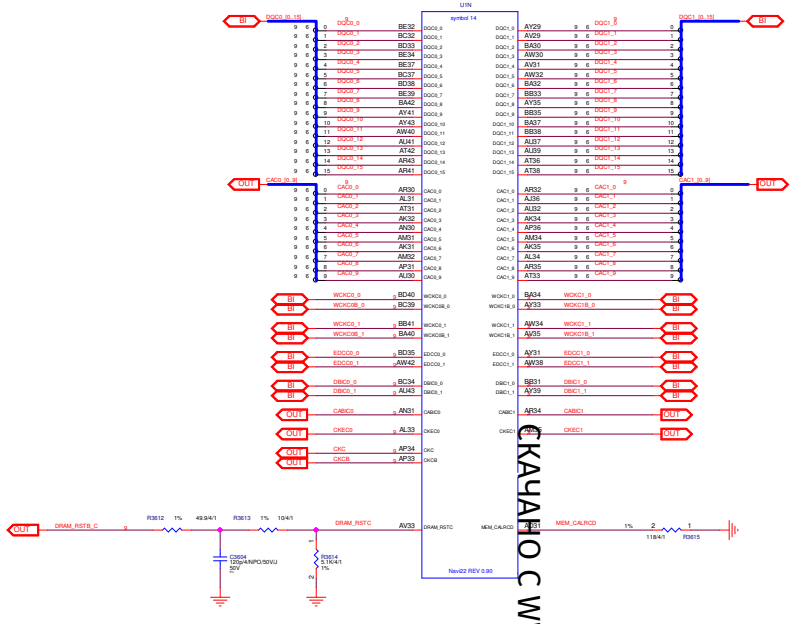

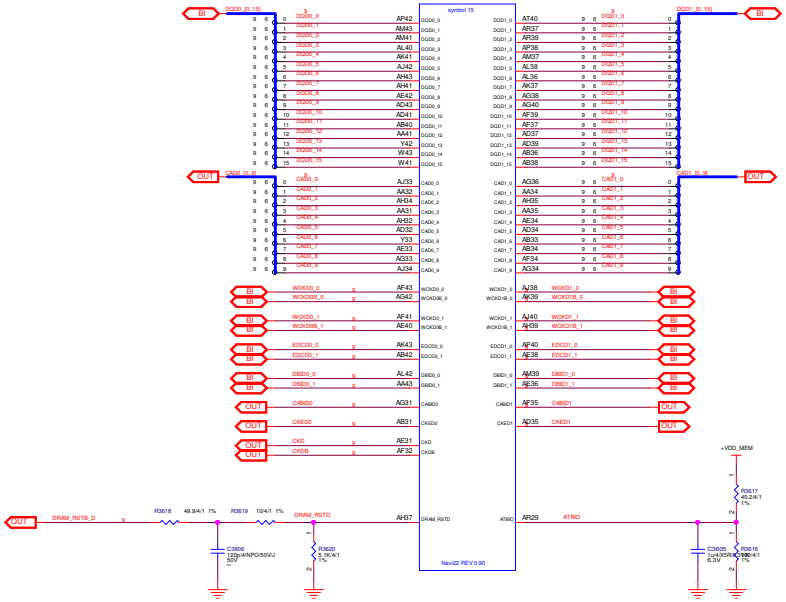

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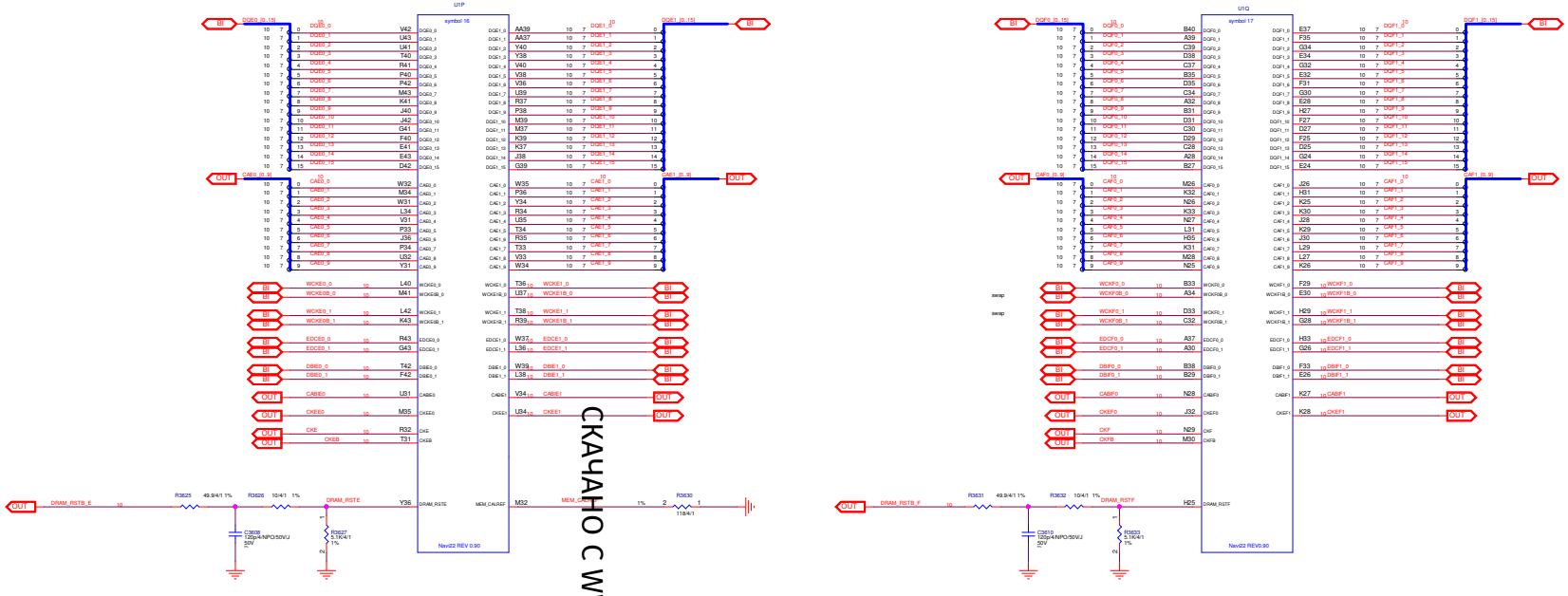

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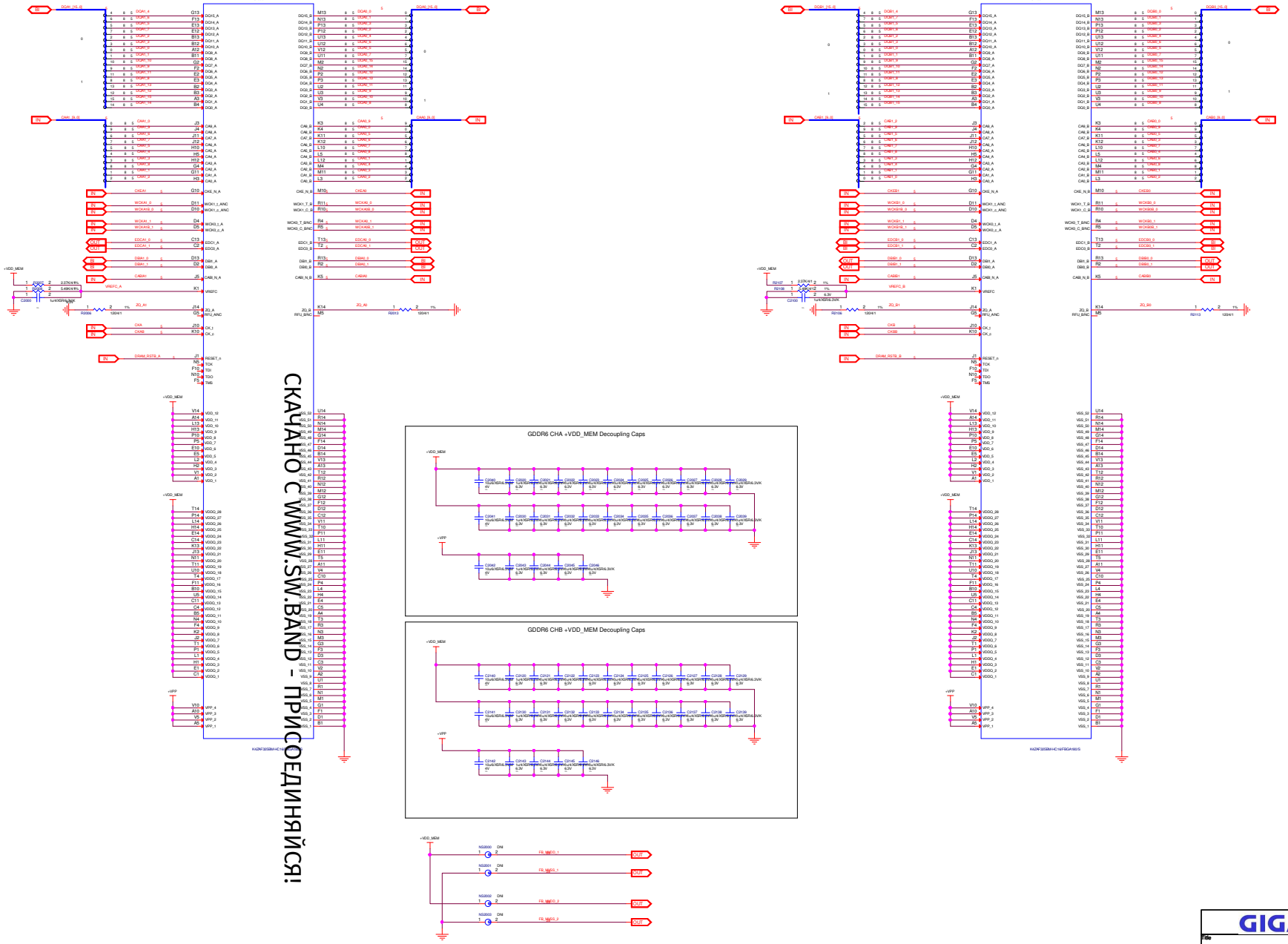

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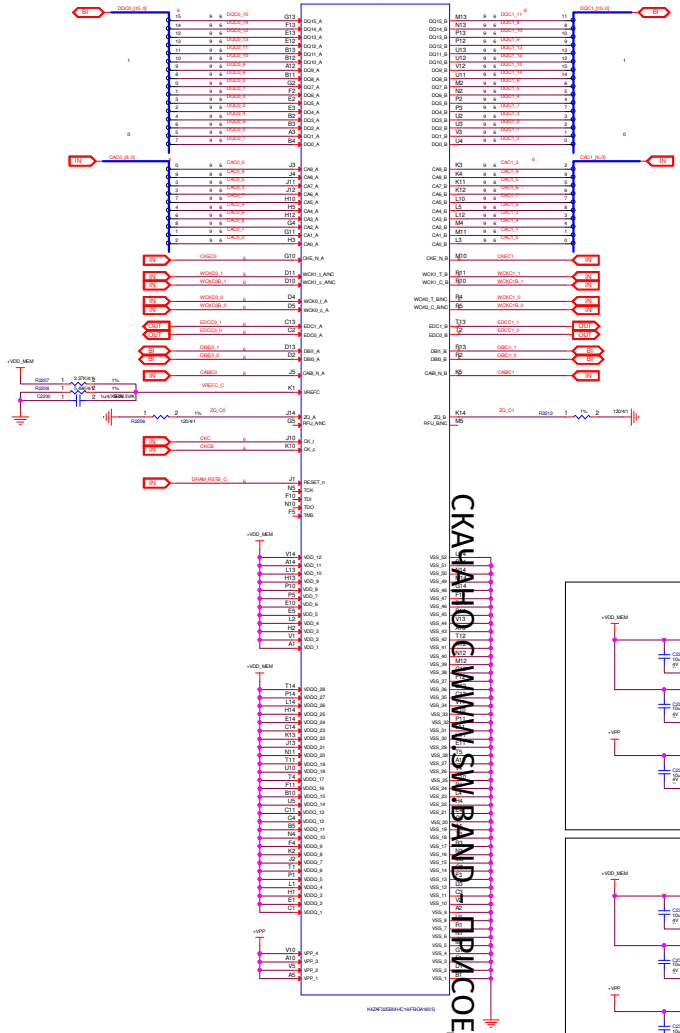

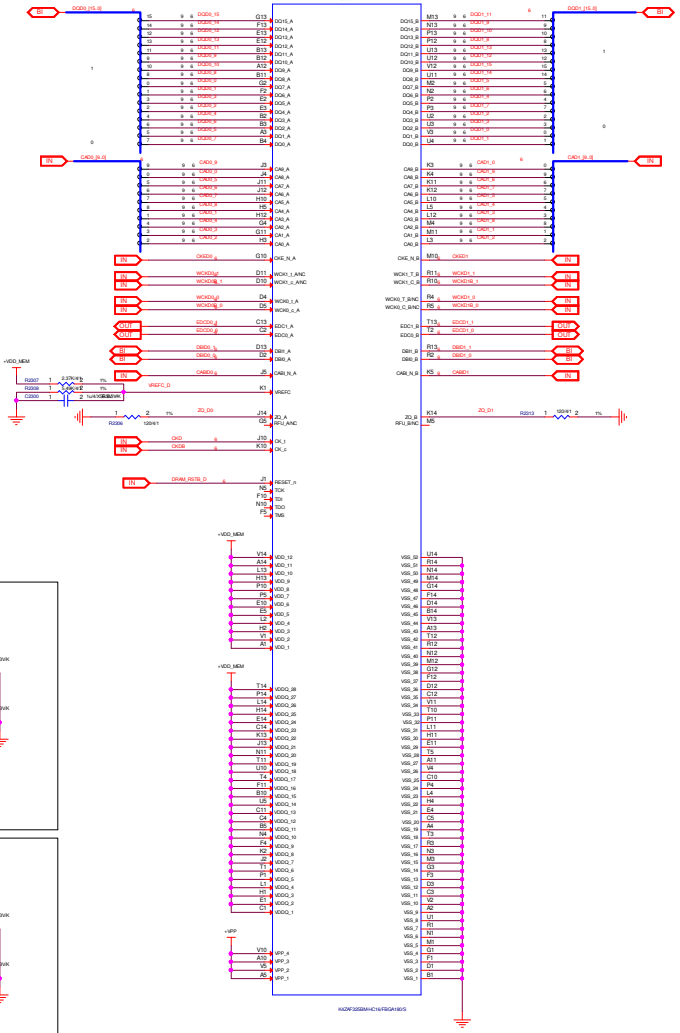

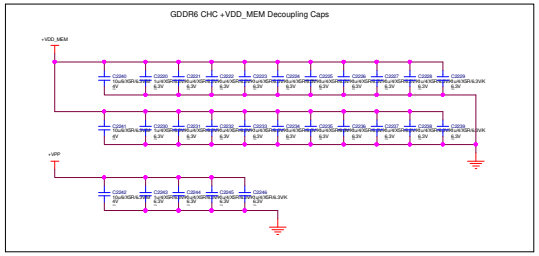

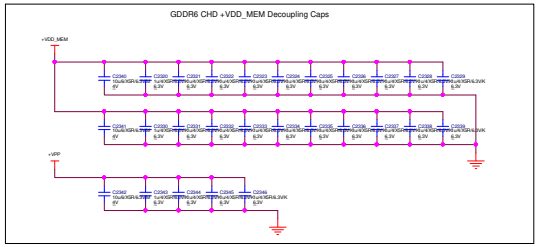

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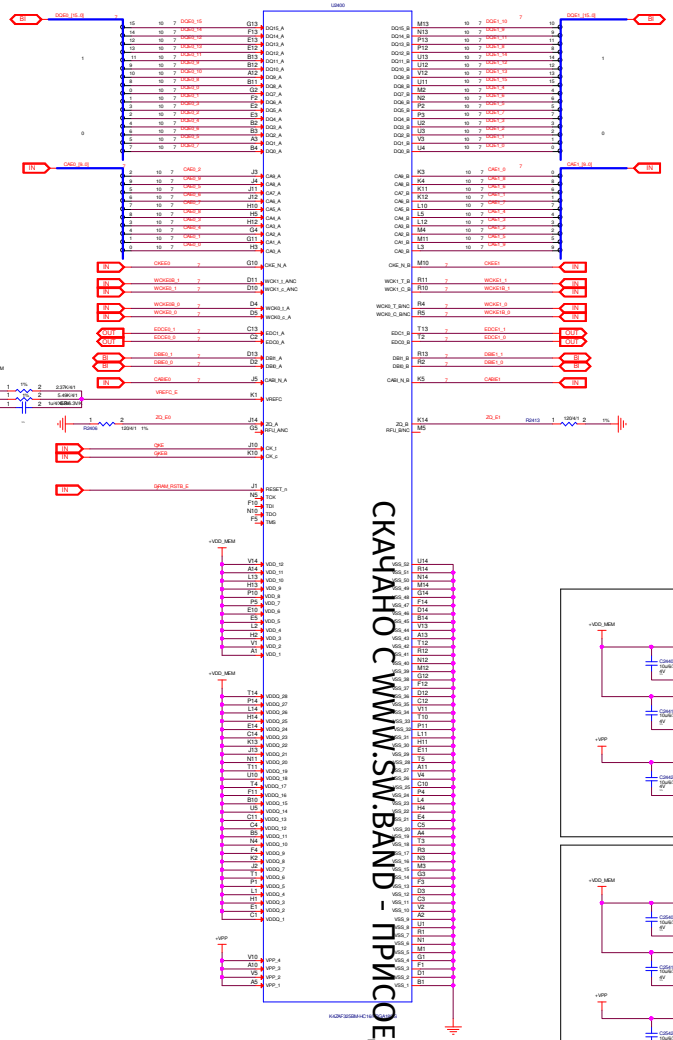

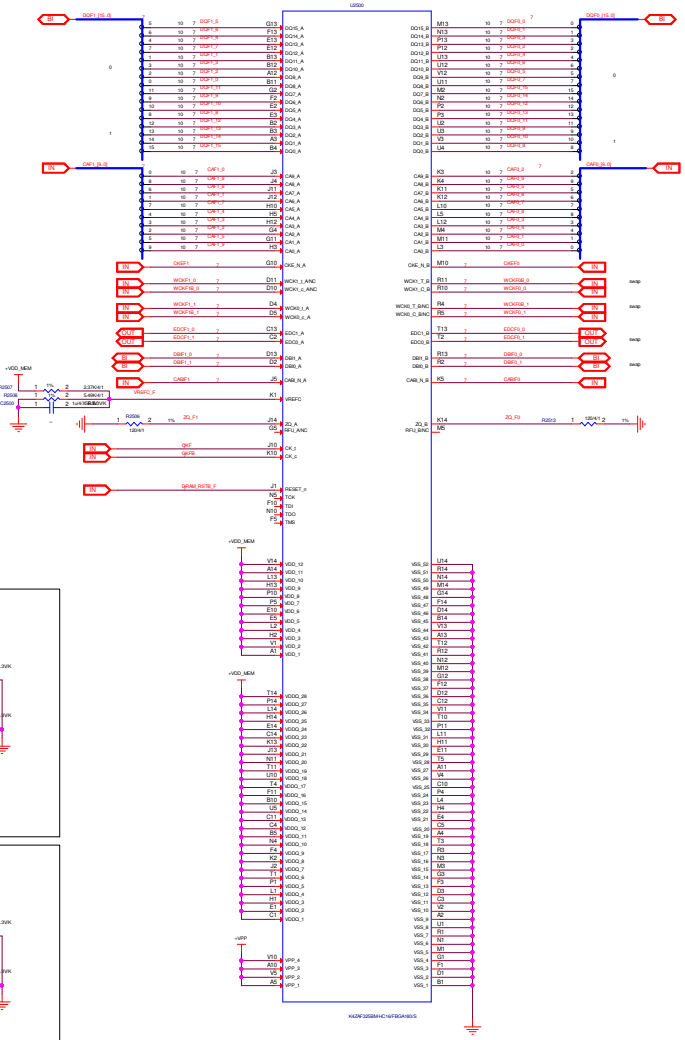

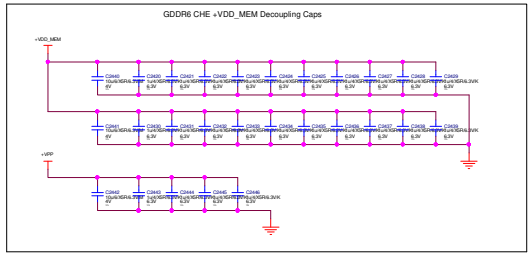

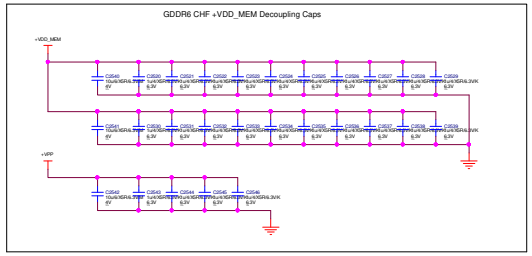

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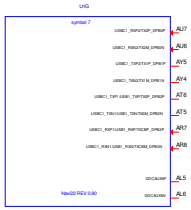

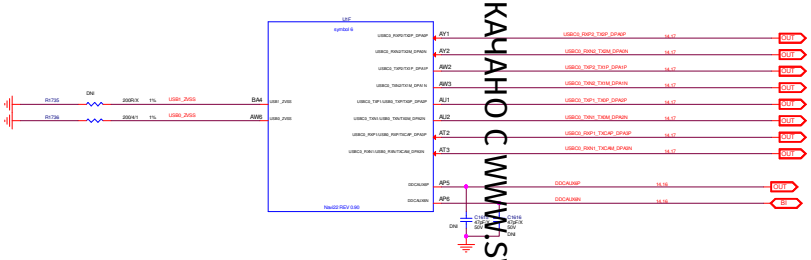

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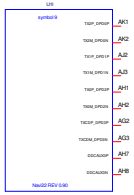

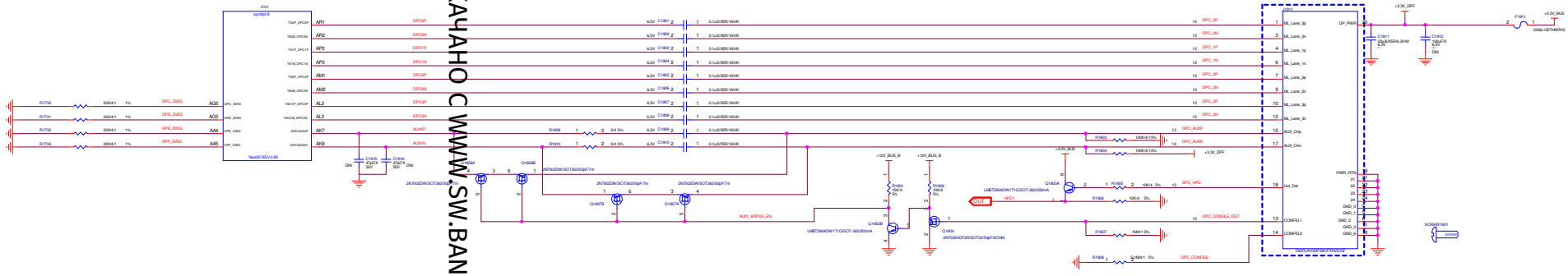

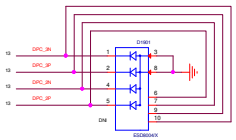

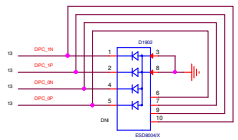

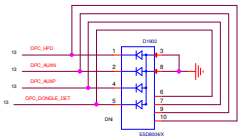

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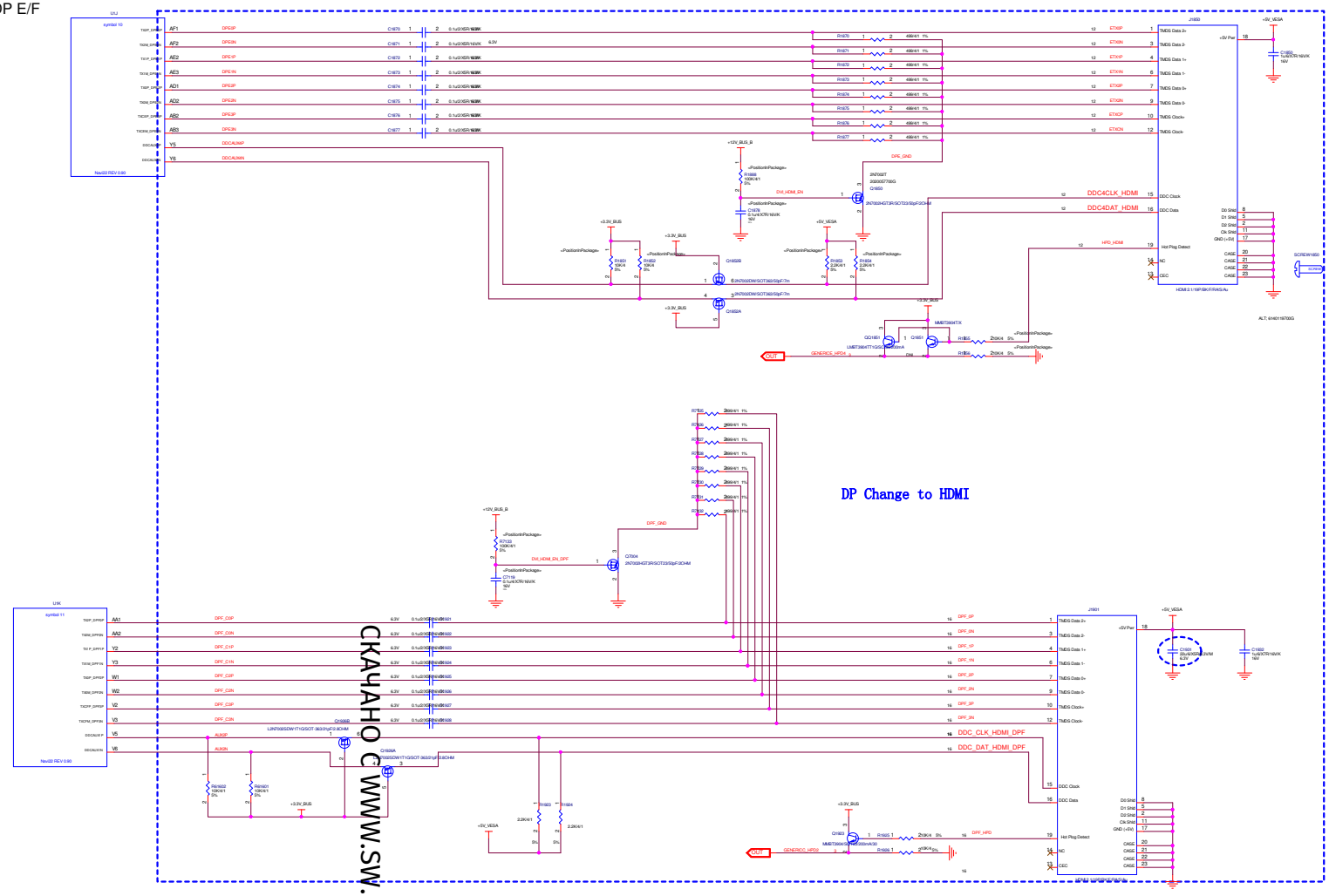

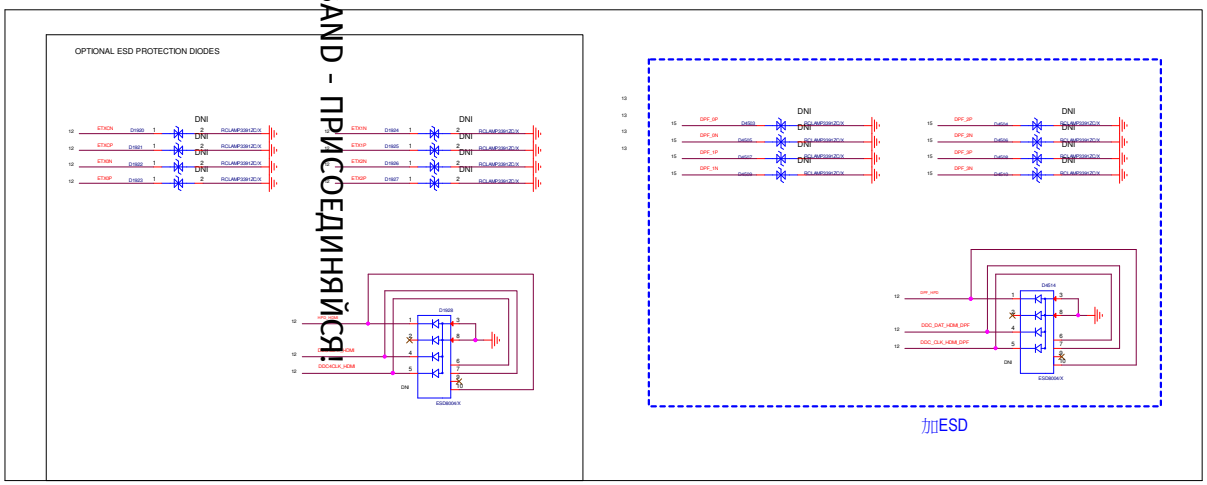

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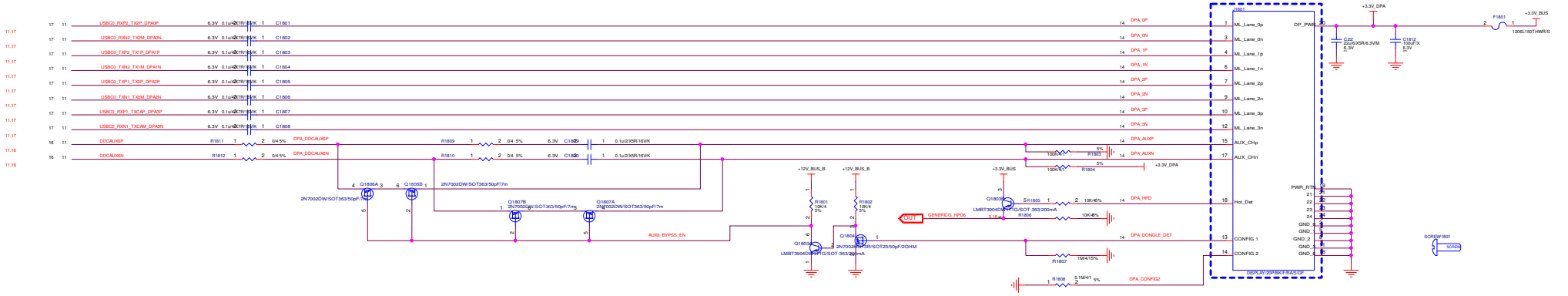
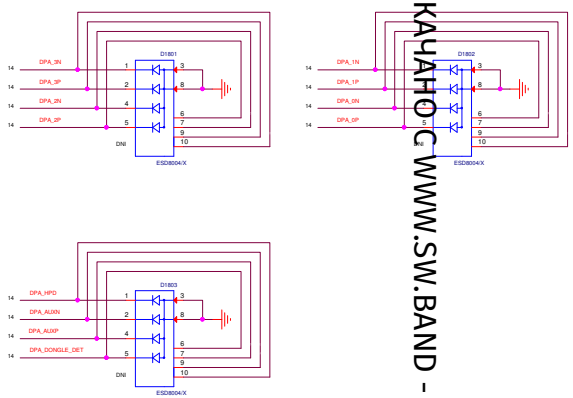


Overlap with USBC



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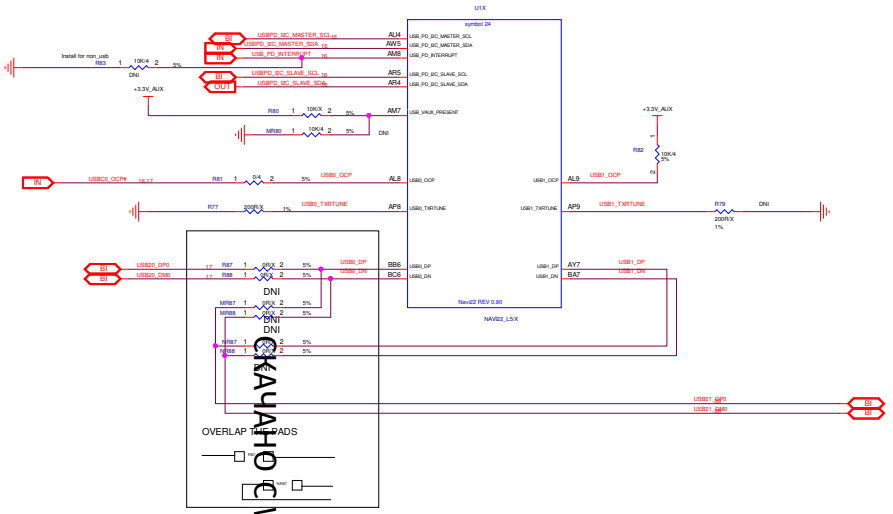

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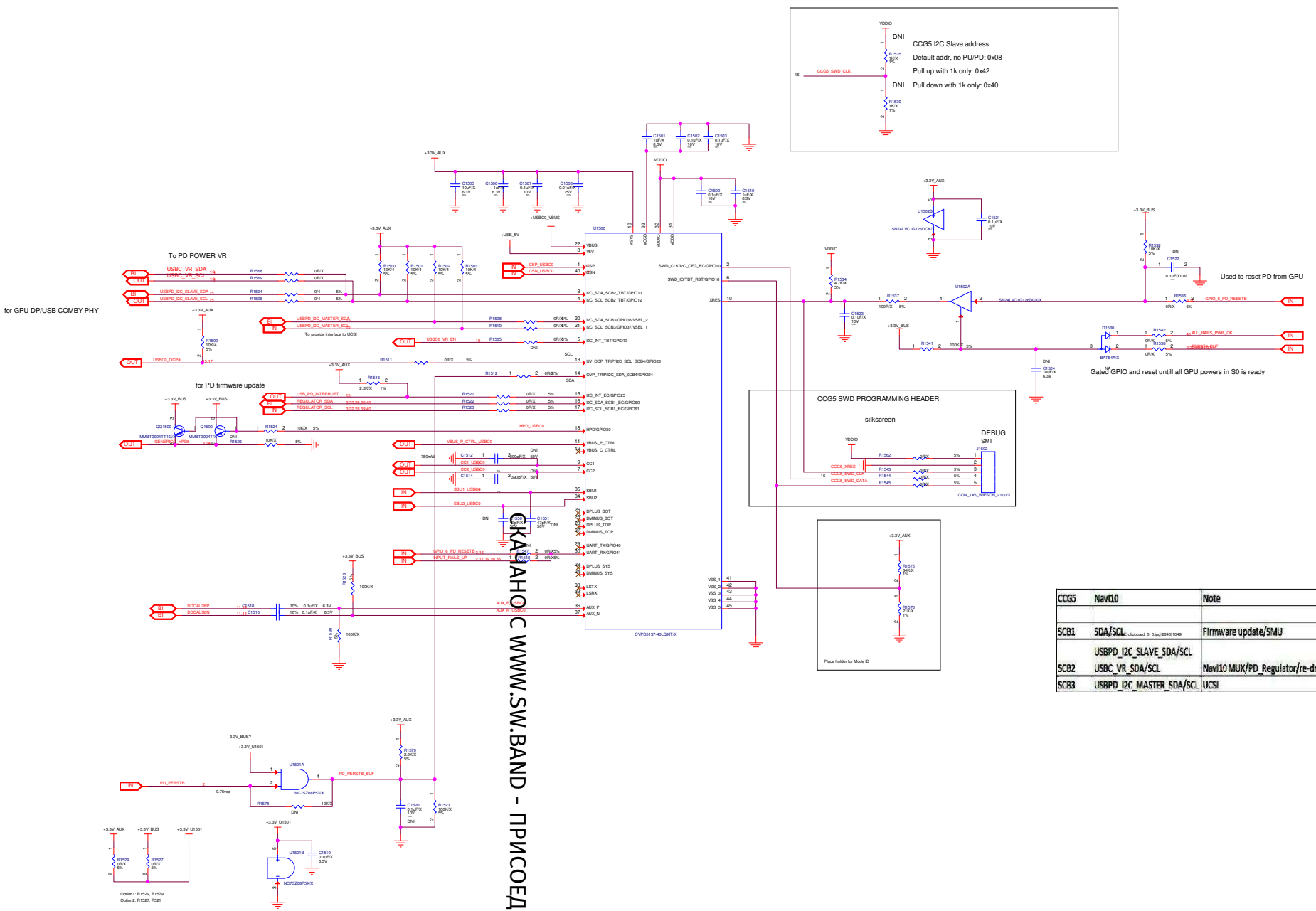

CCG5	Navi10	Note
SCB1	SDA/SCL	Firmware update/SMU
SCB2	USBPD_I2C_SLAVE_SDA/SCL USBC_VR_SDA/SCL	Navi10 MUX/PD_Regulator/re-driver
SCB3	USBPD_I2C_MASTER_SDA/SCL	UCSI

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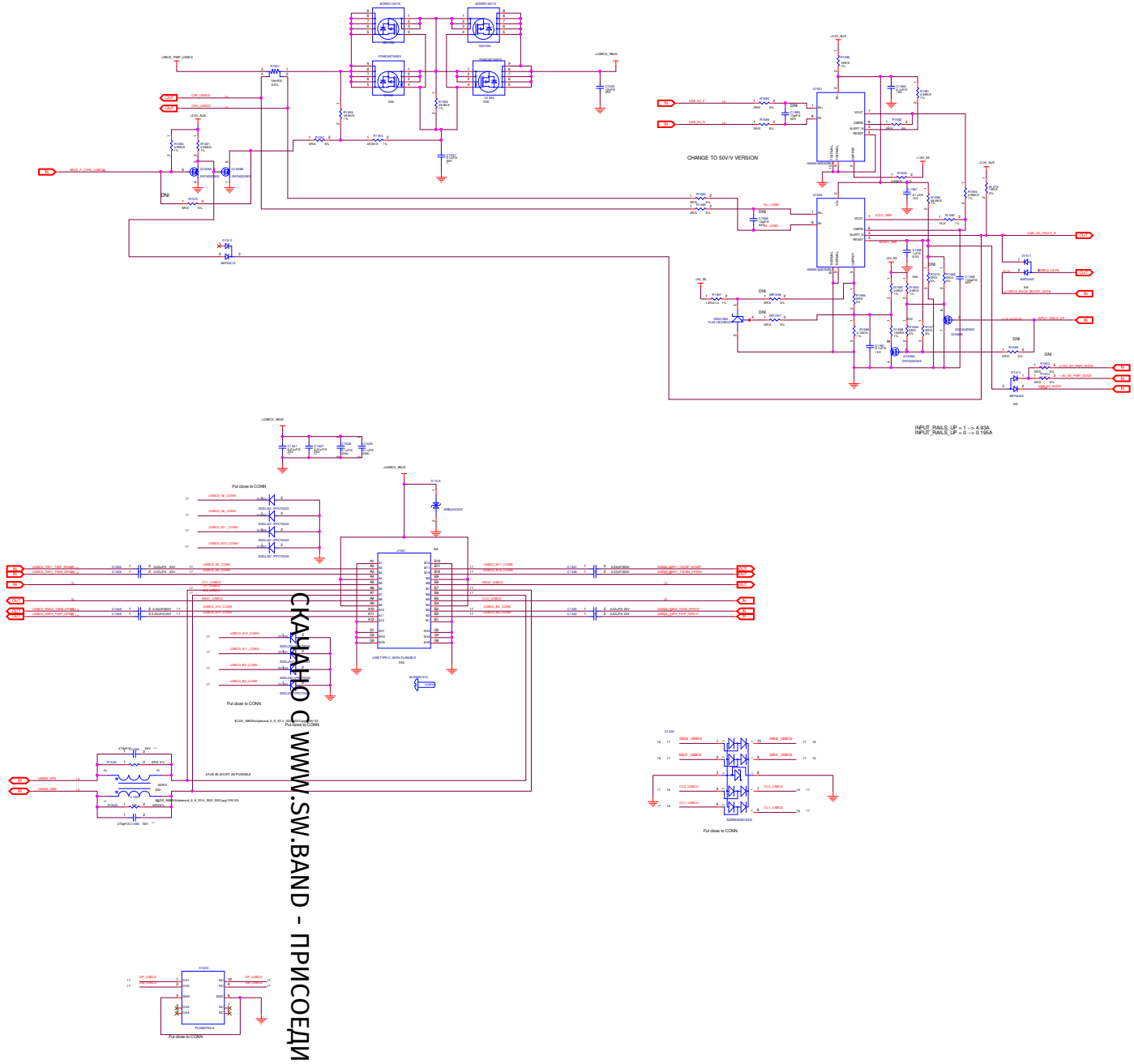

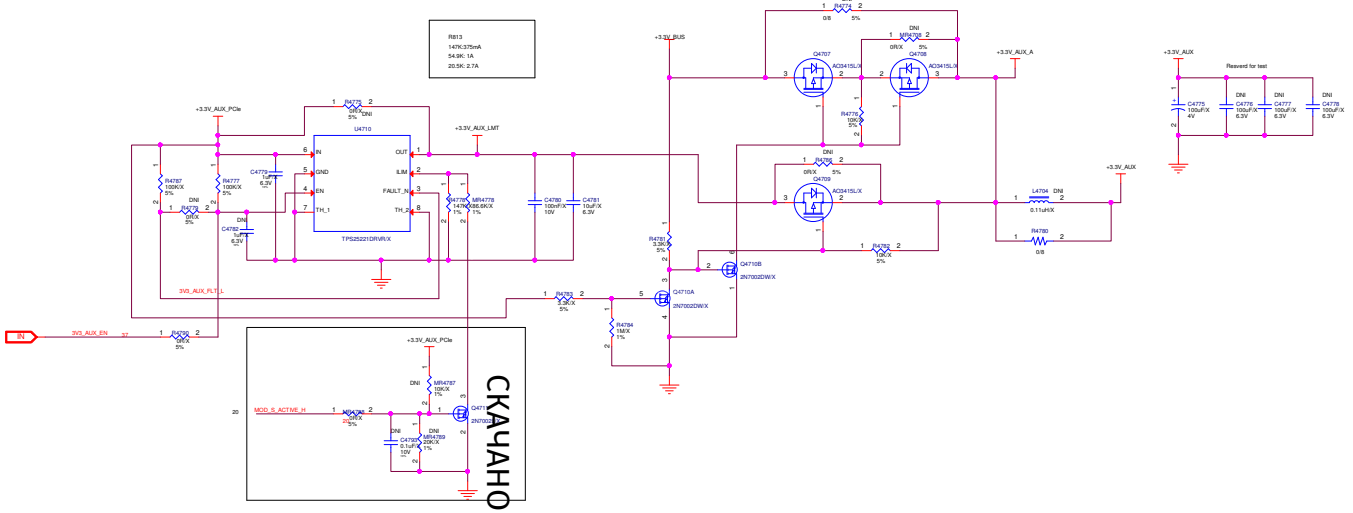

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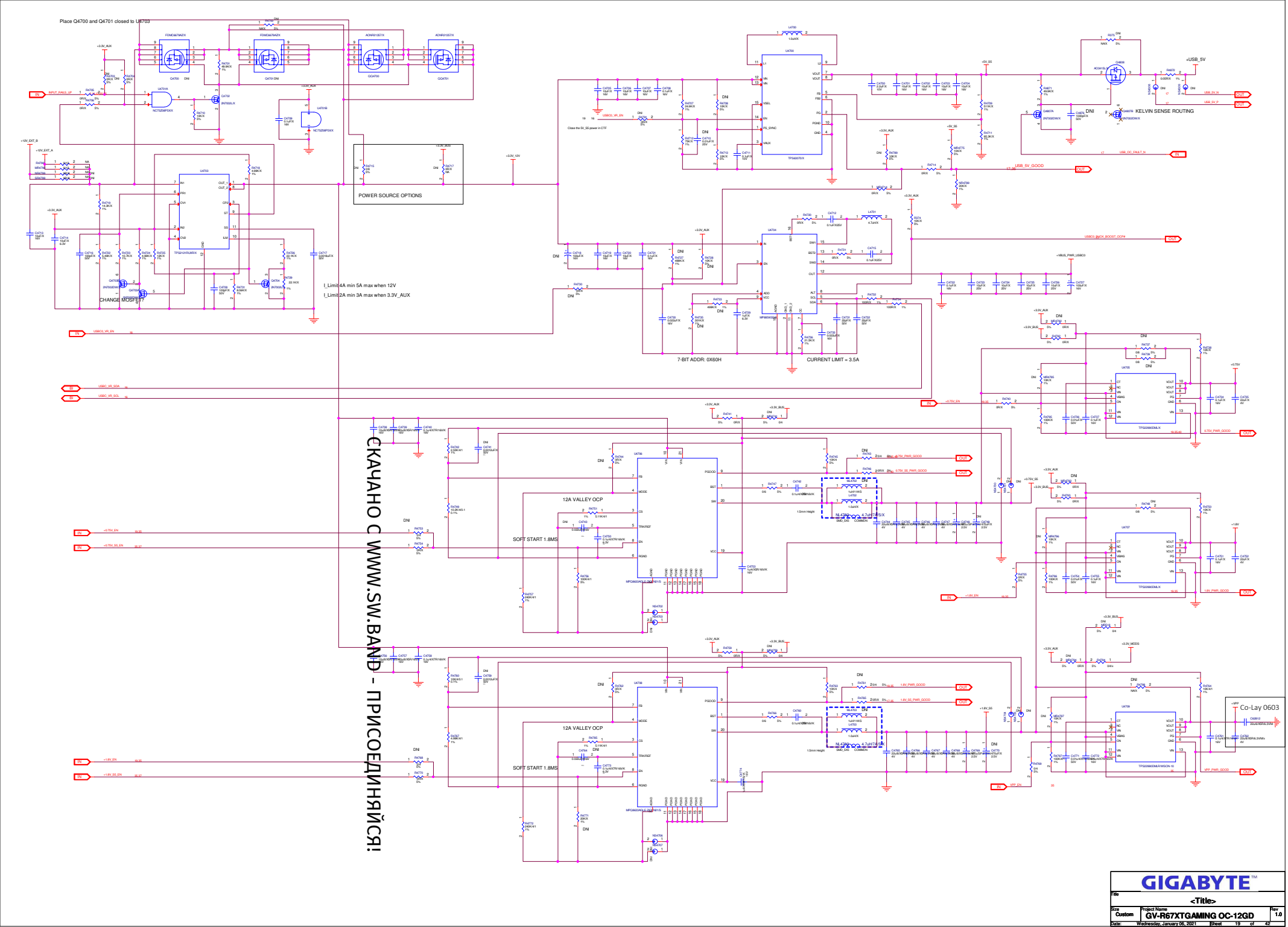

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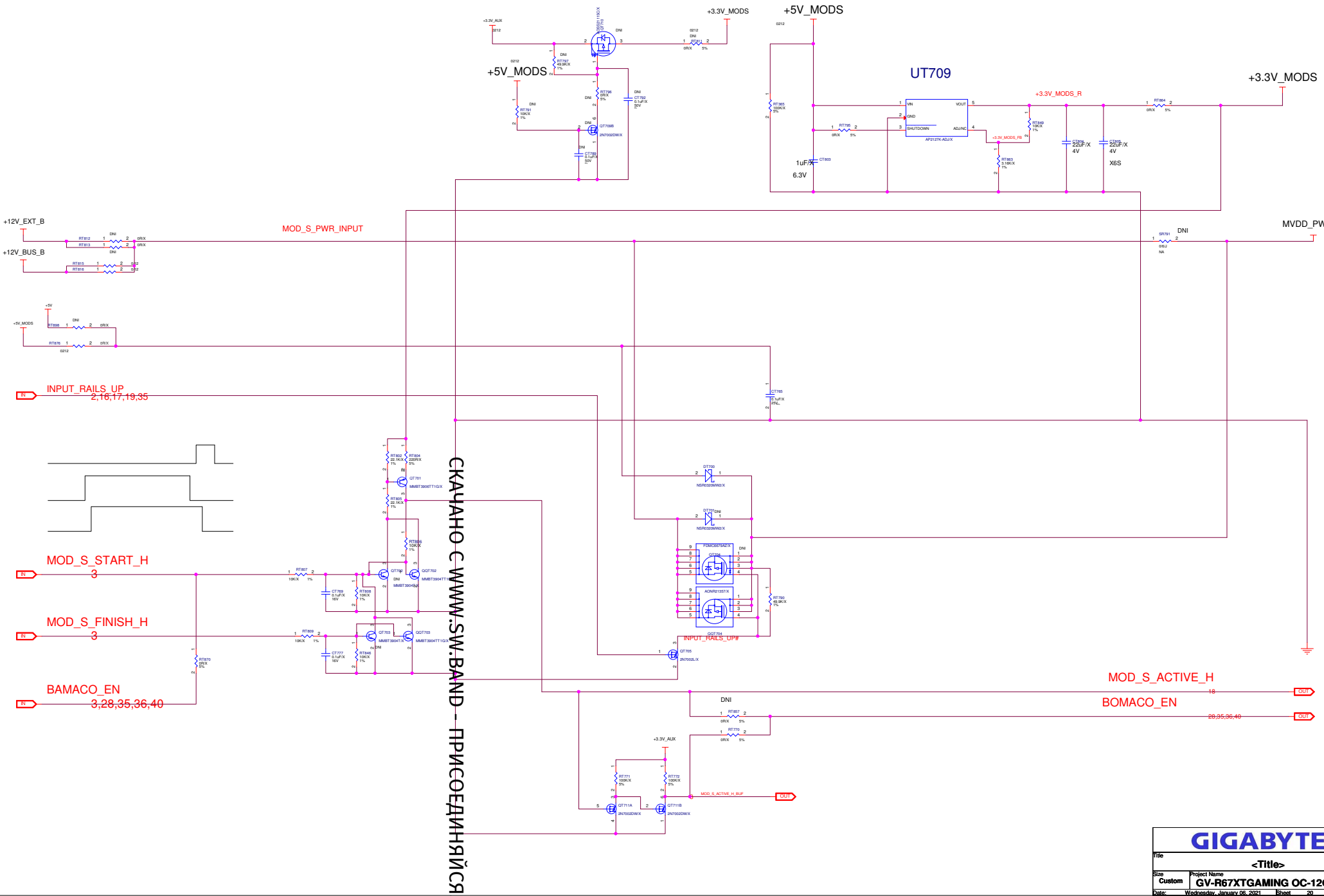

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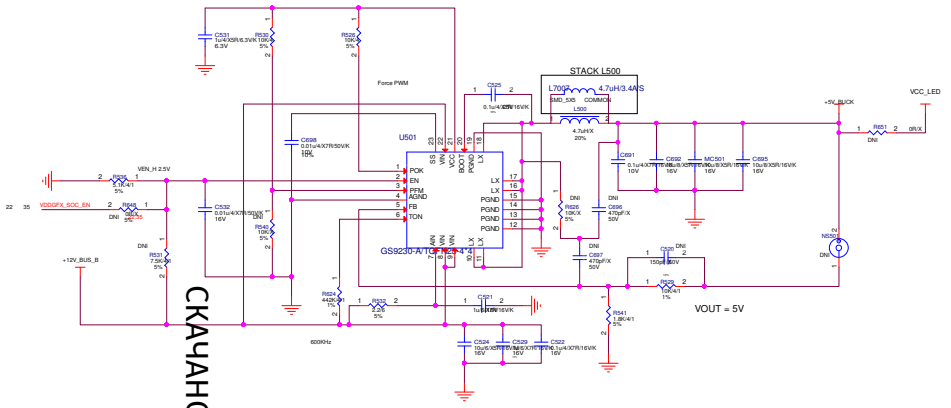

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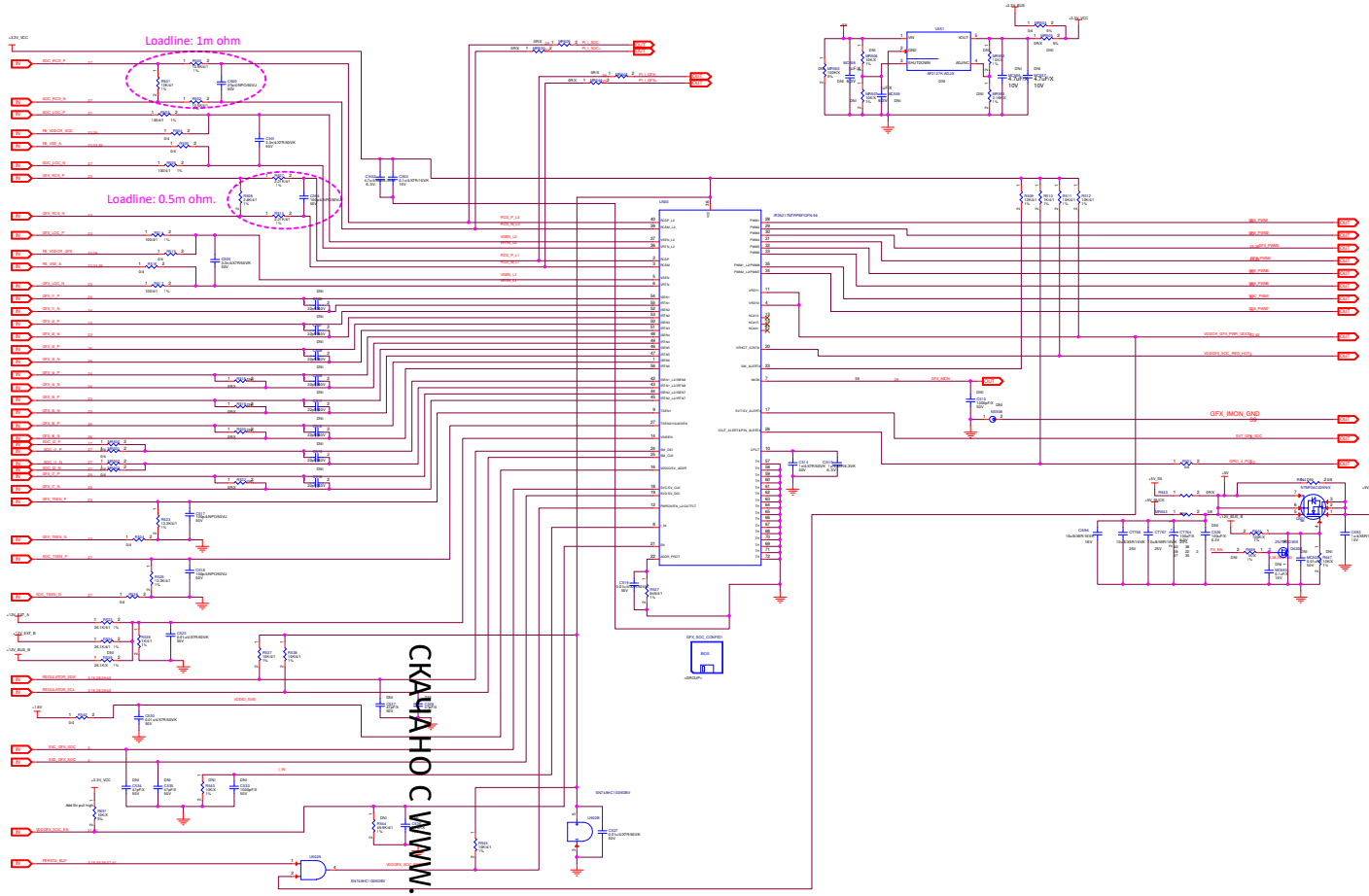

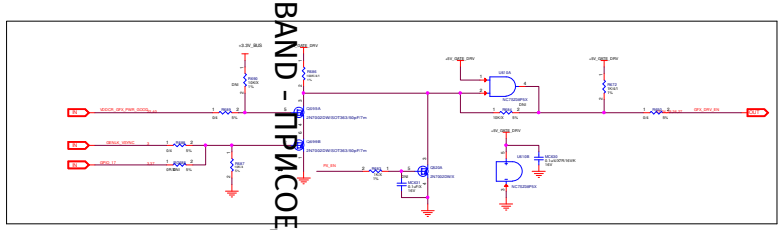

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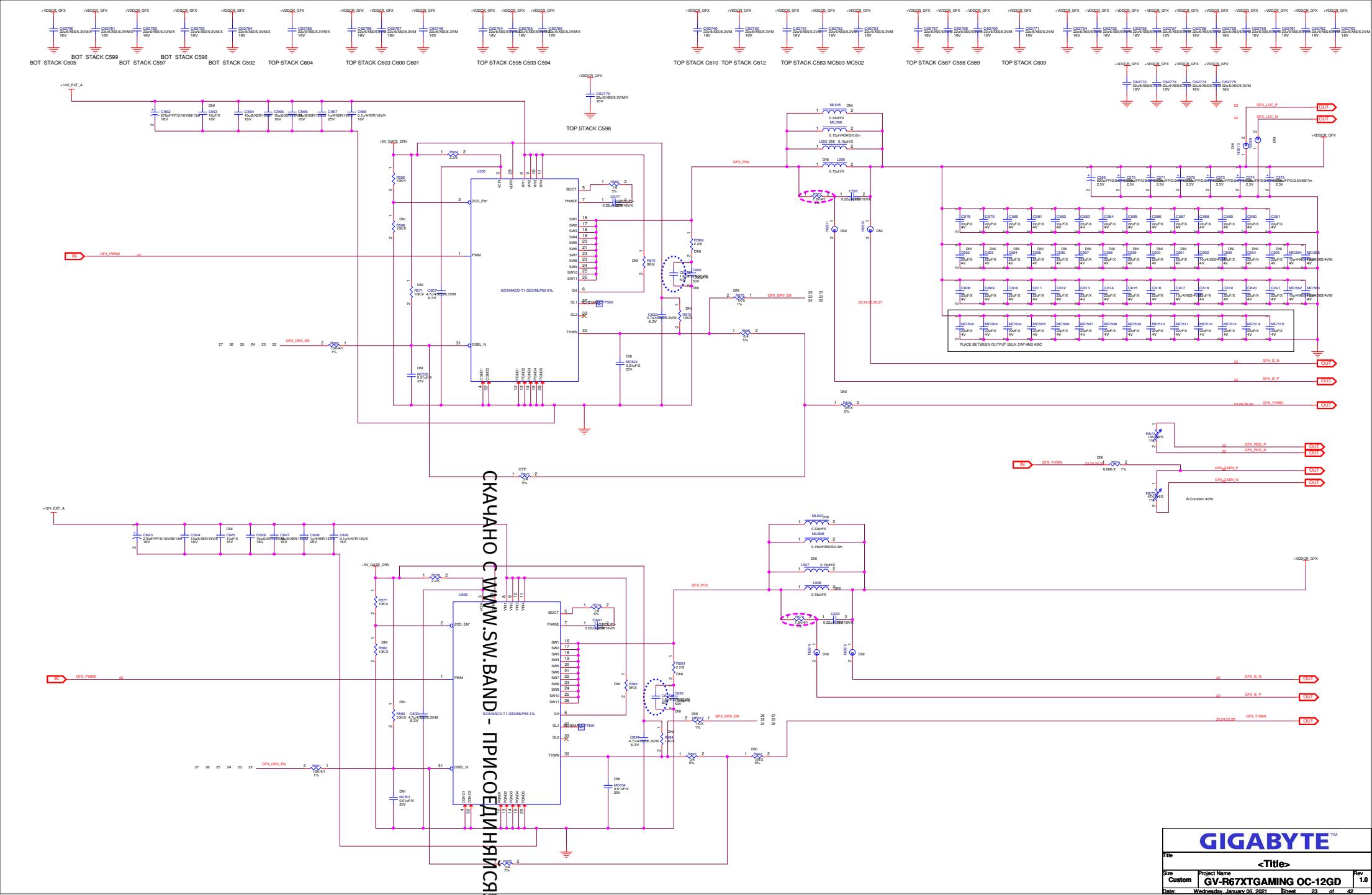

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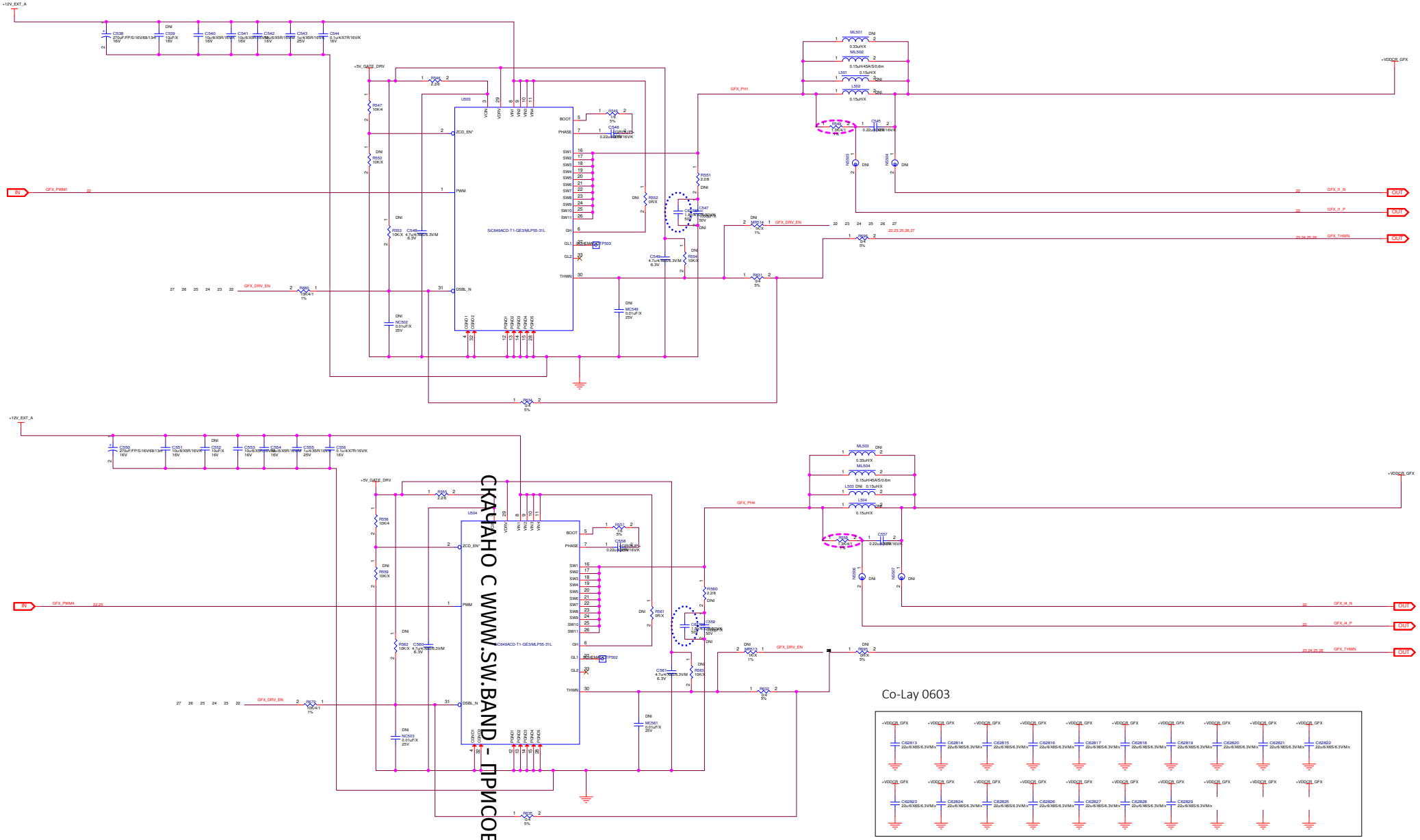

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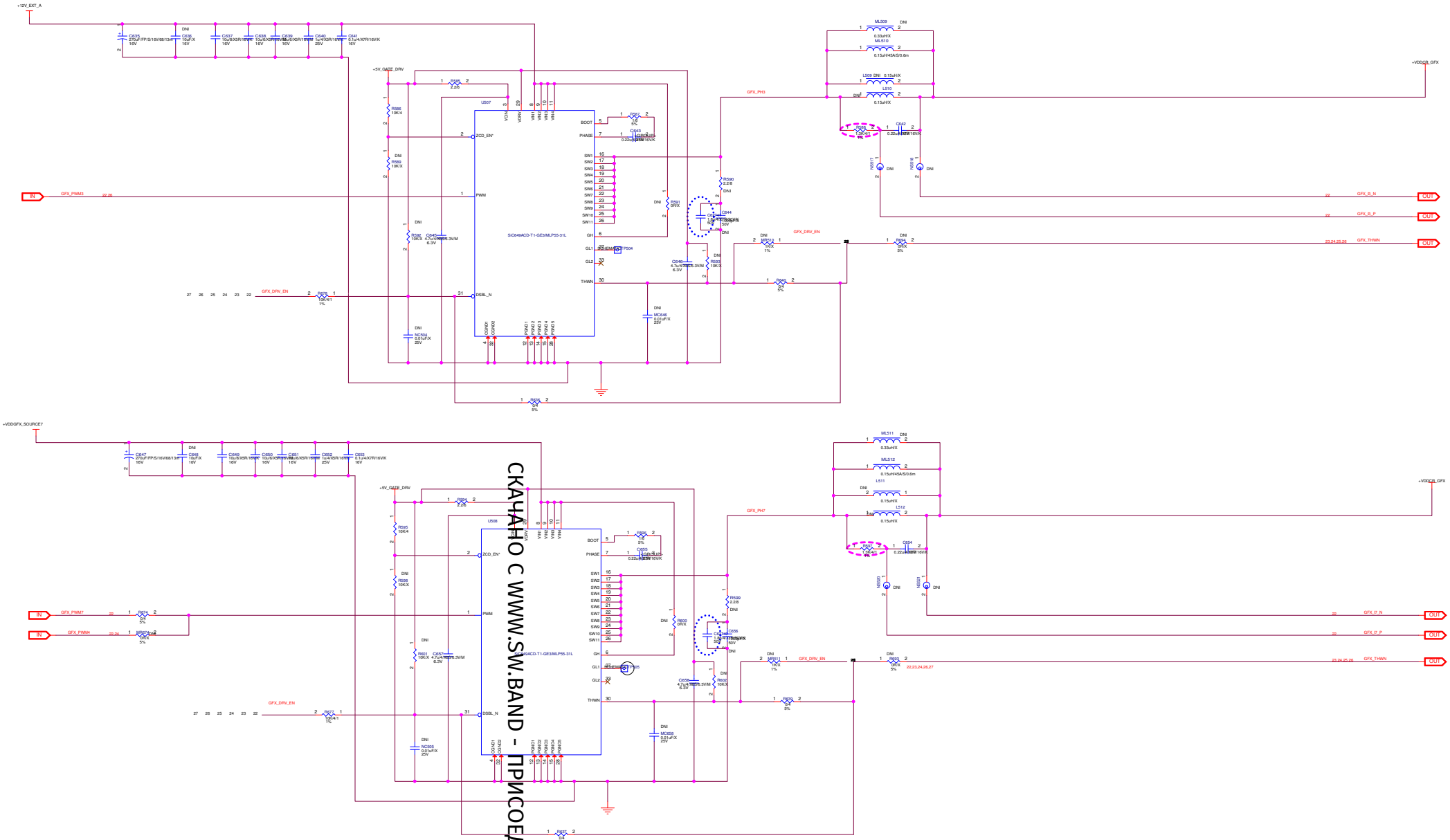

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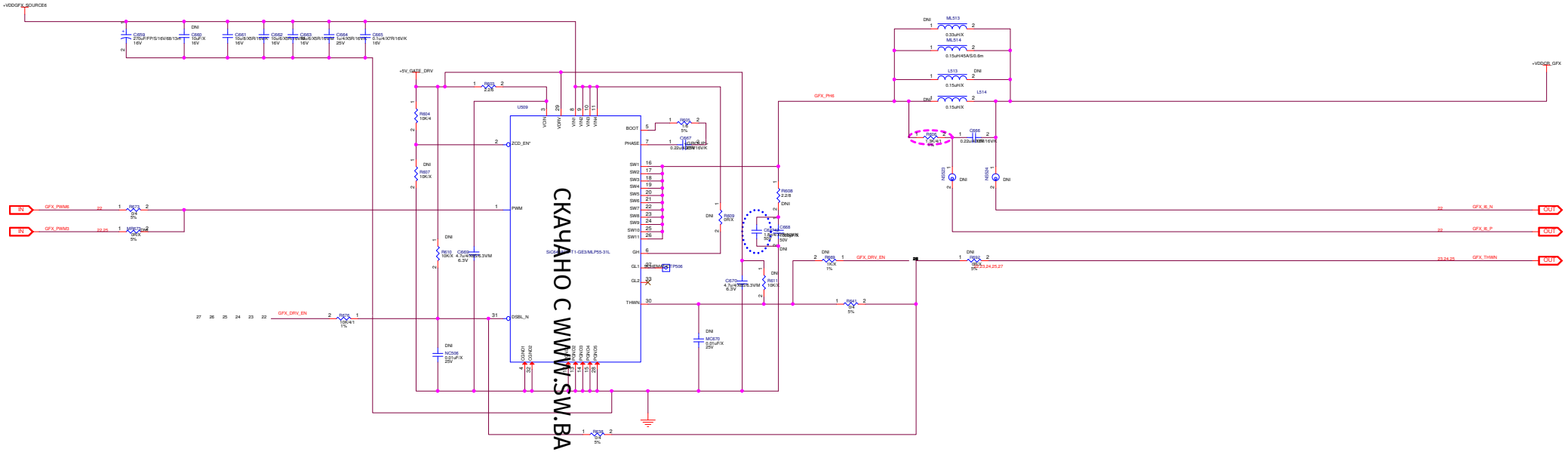

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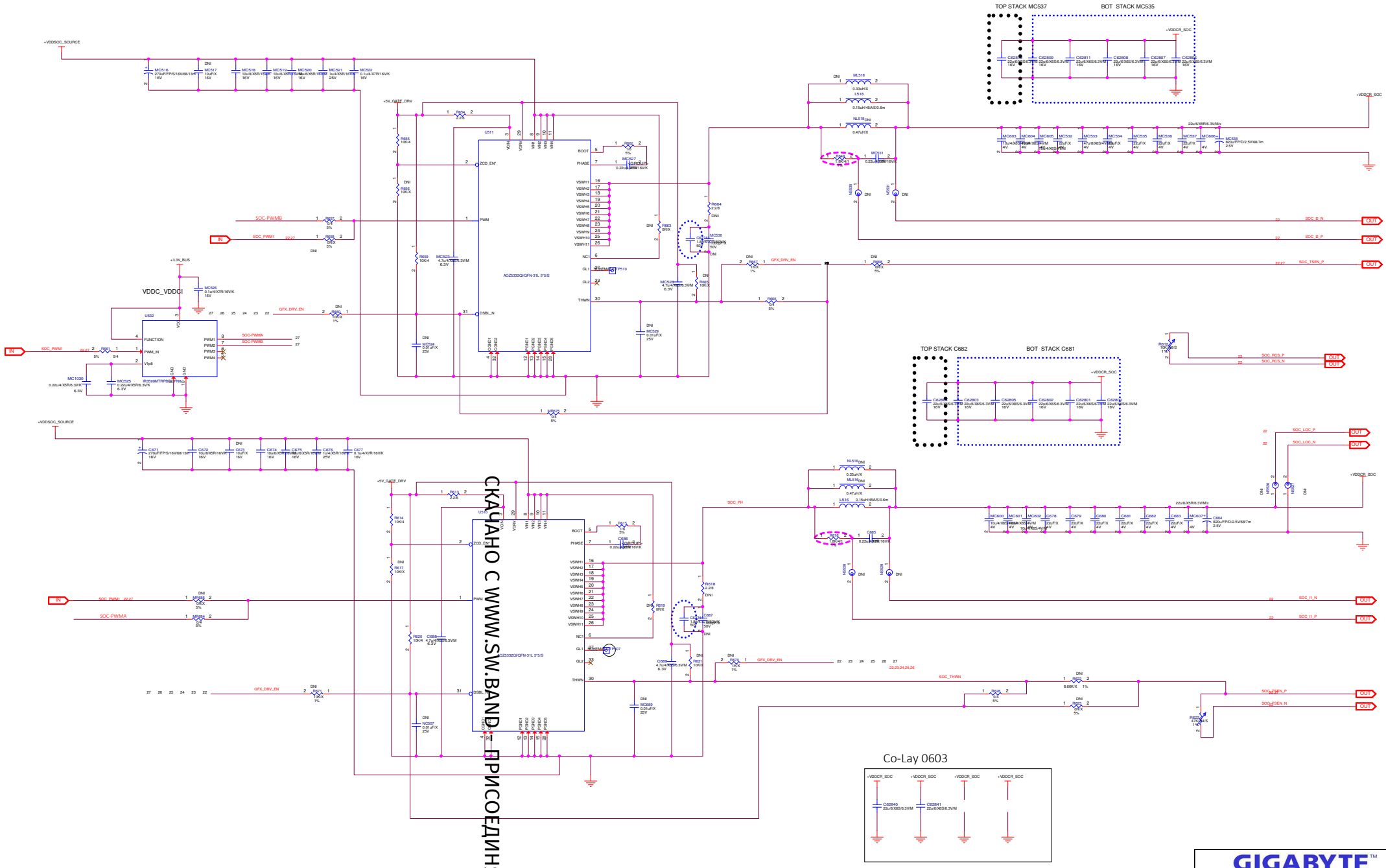

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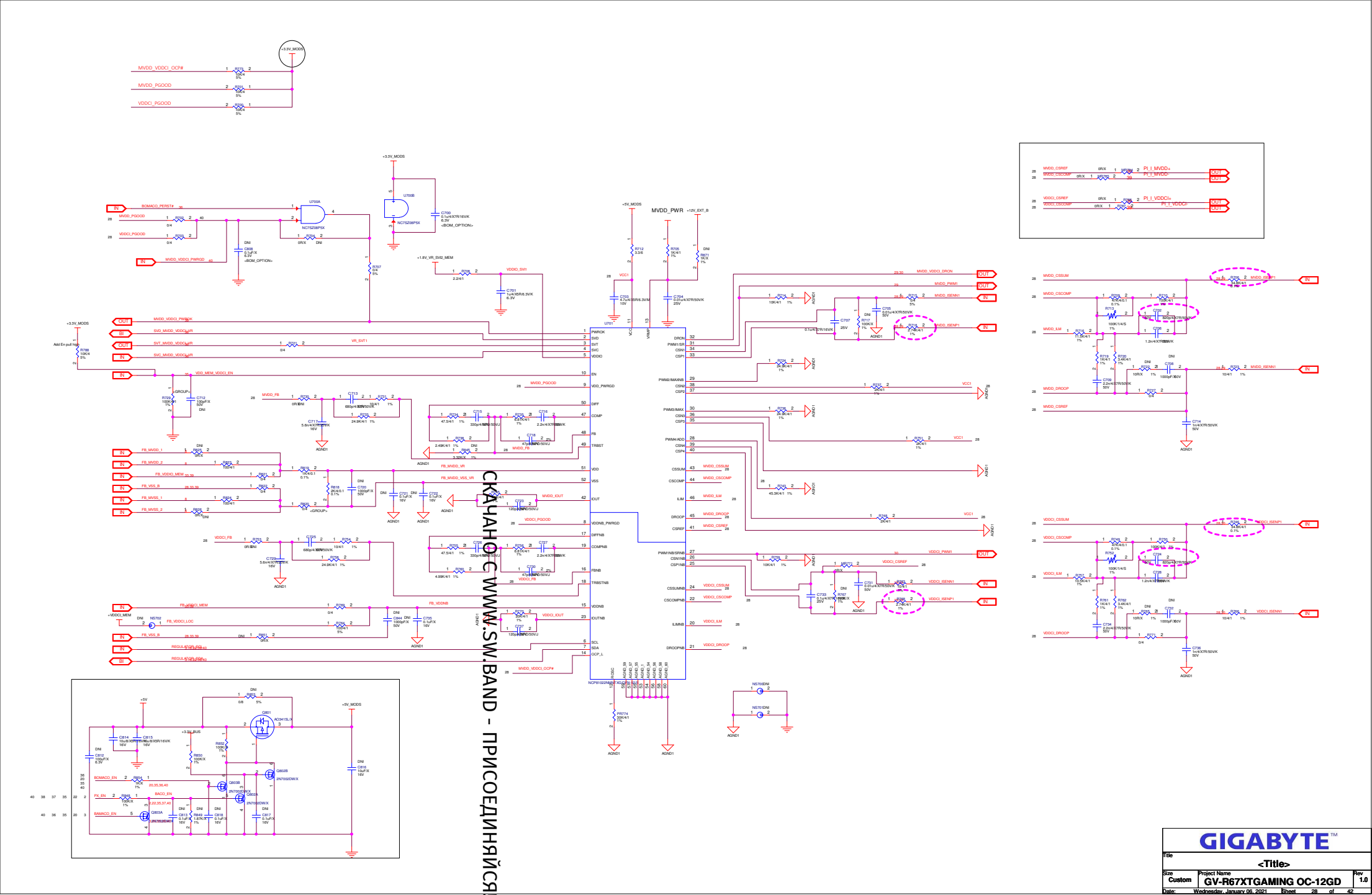

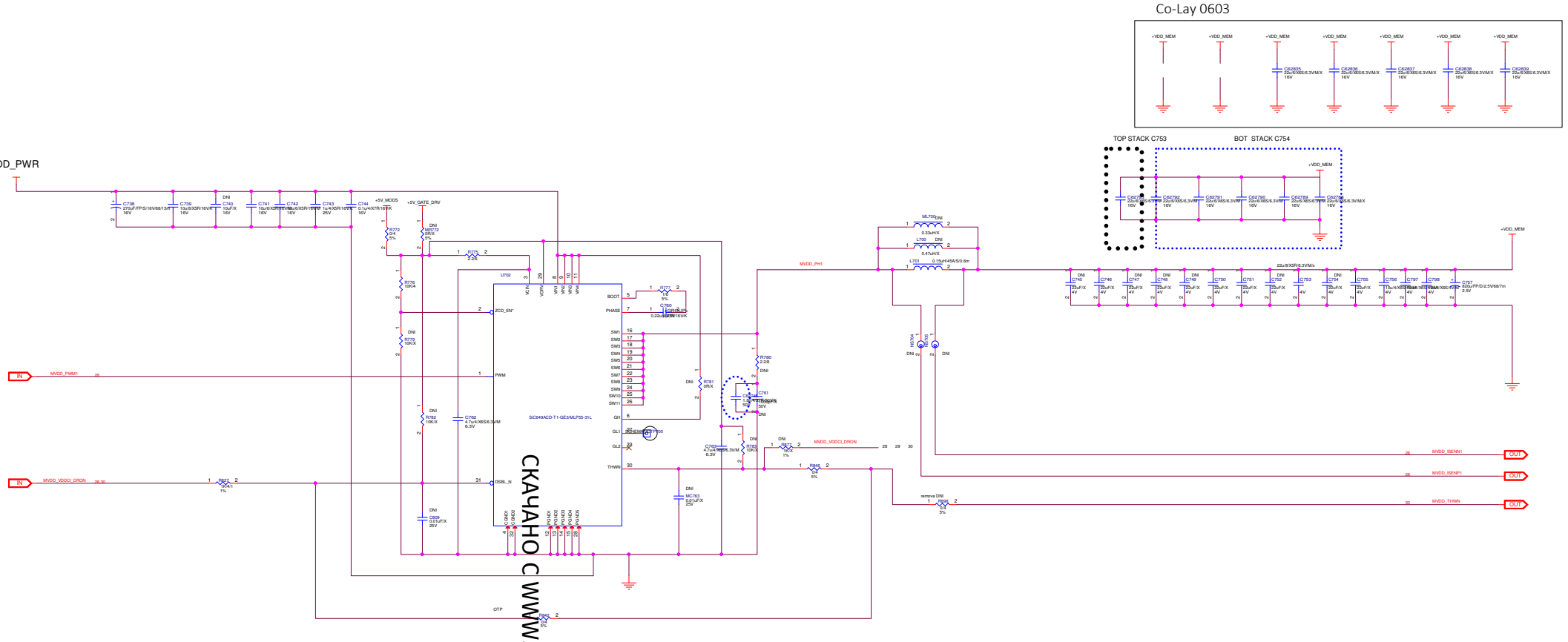

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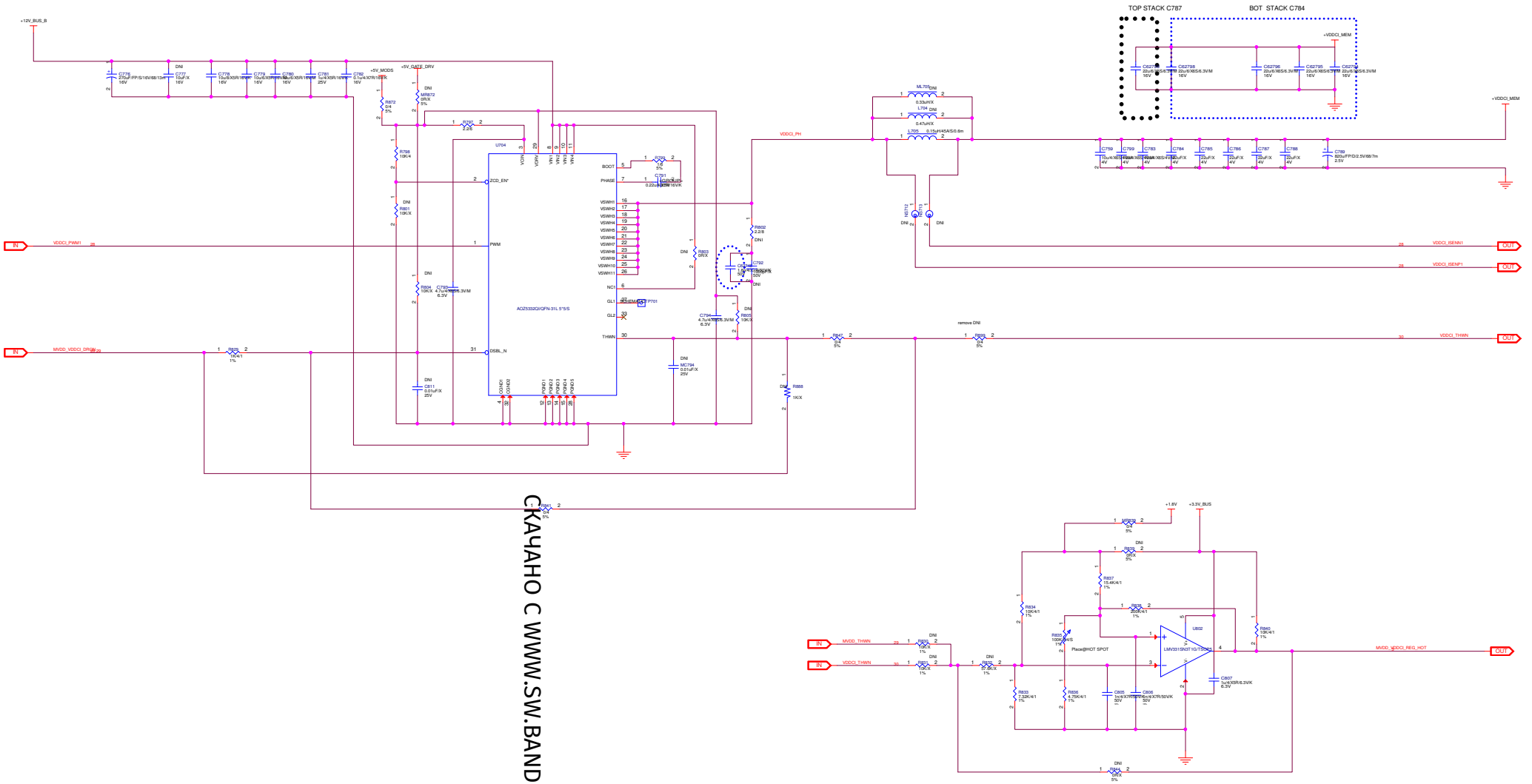

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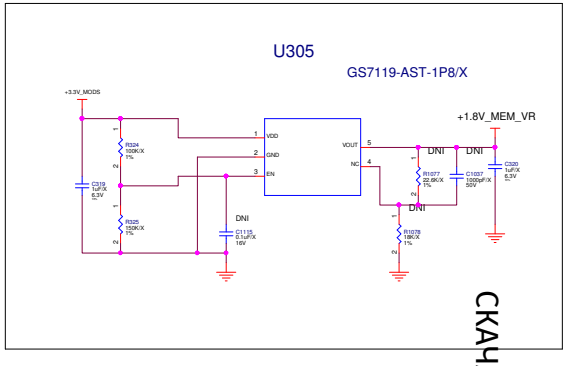
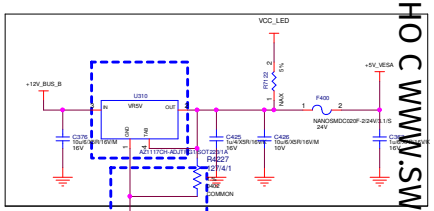
+1.8V FOR BOMACO





VOUT = 1.25X(1+R2/R1)

= 1.25 X (1 + 383/127)

= 5.02V

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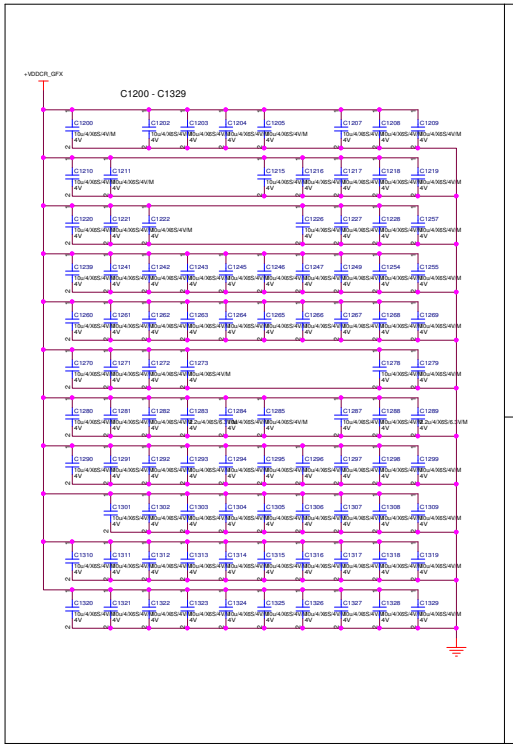

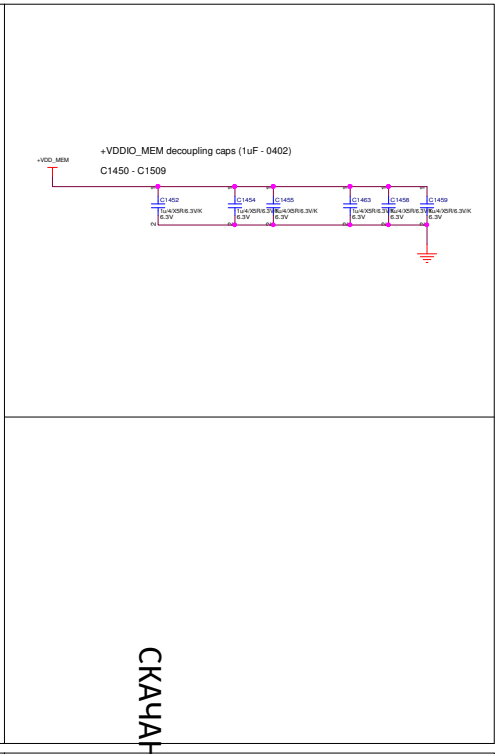

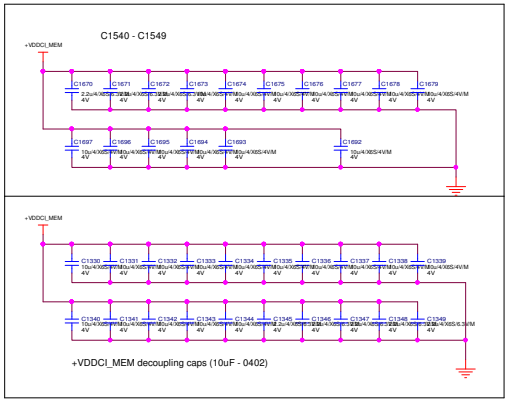

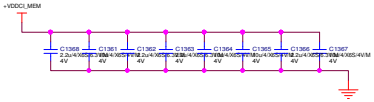

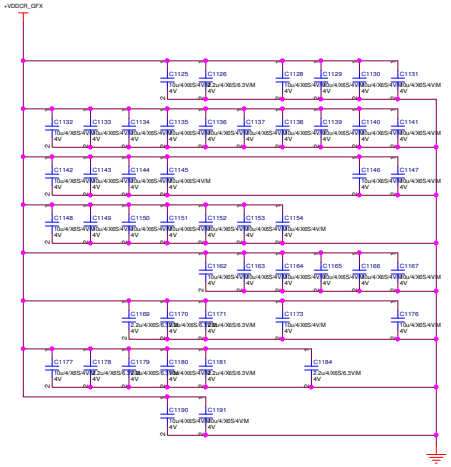

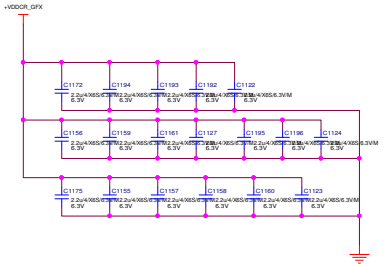

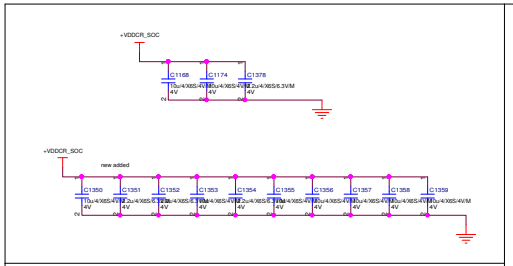

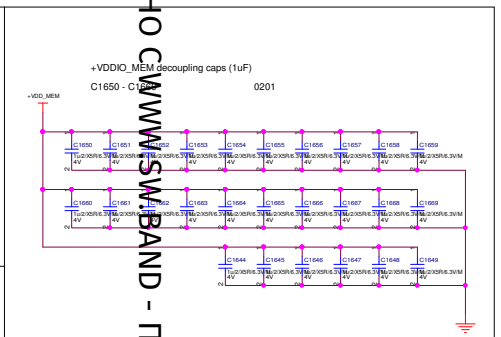

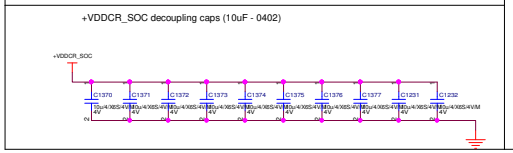

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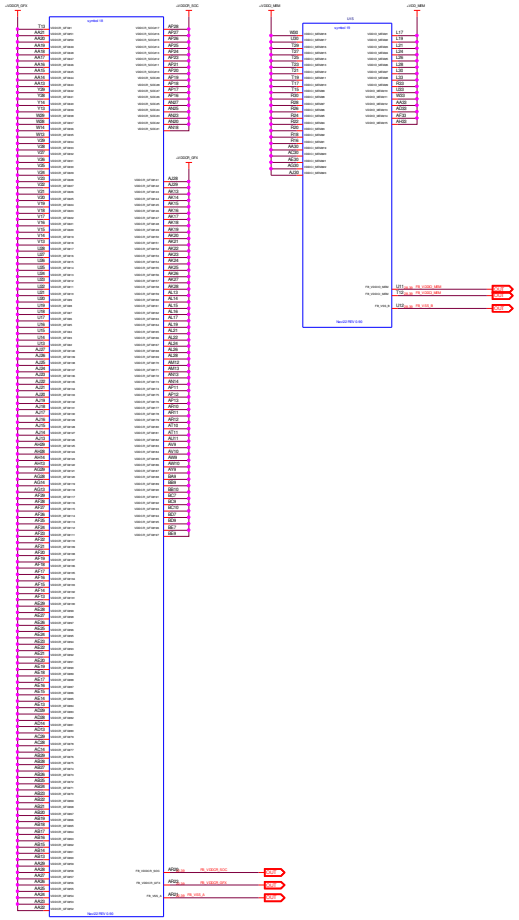

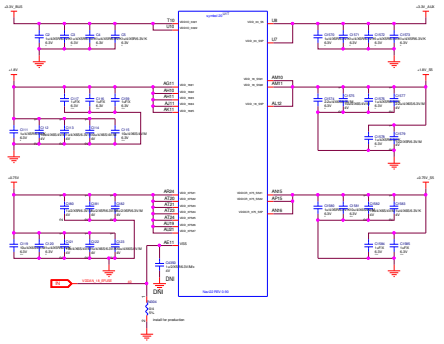

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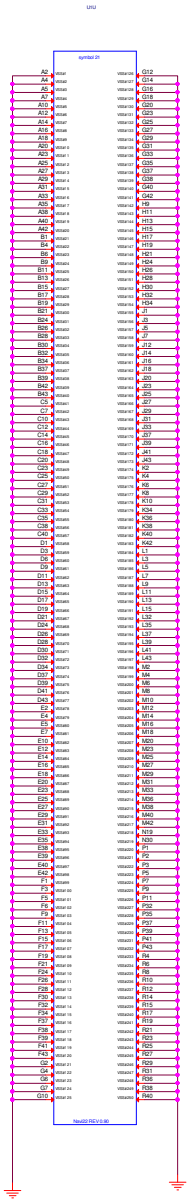

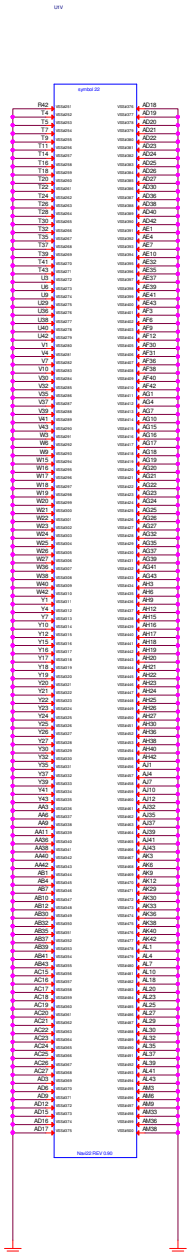

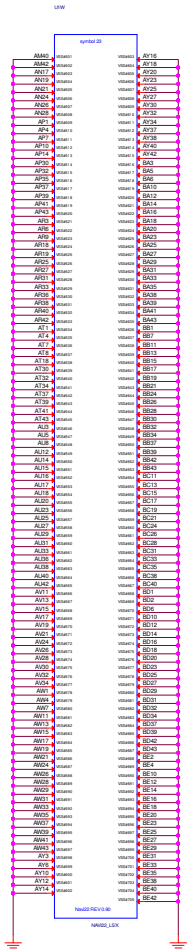

СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

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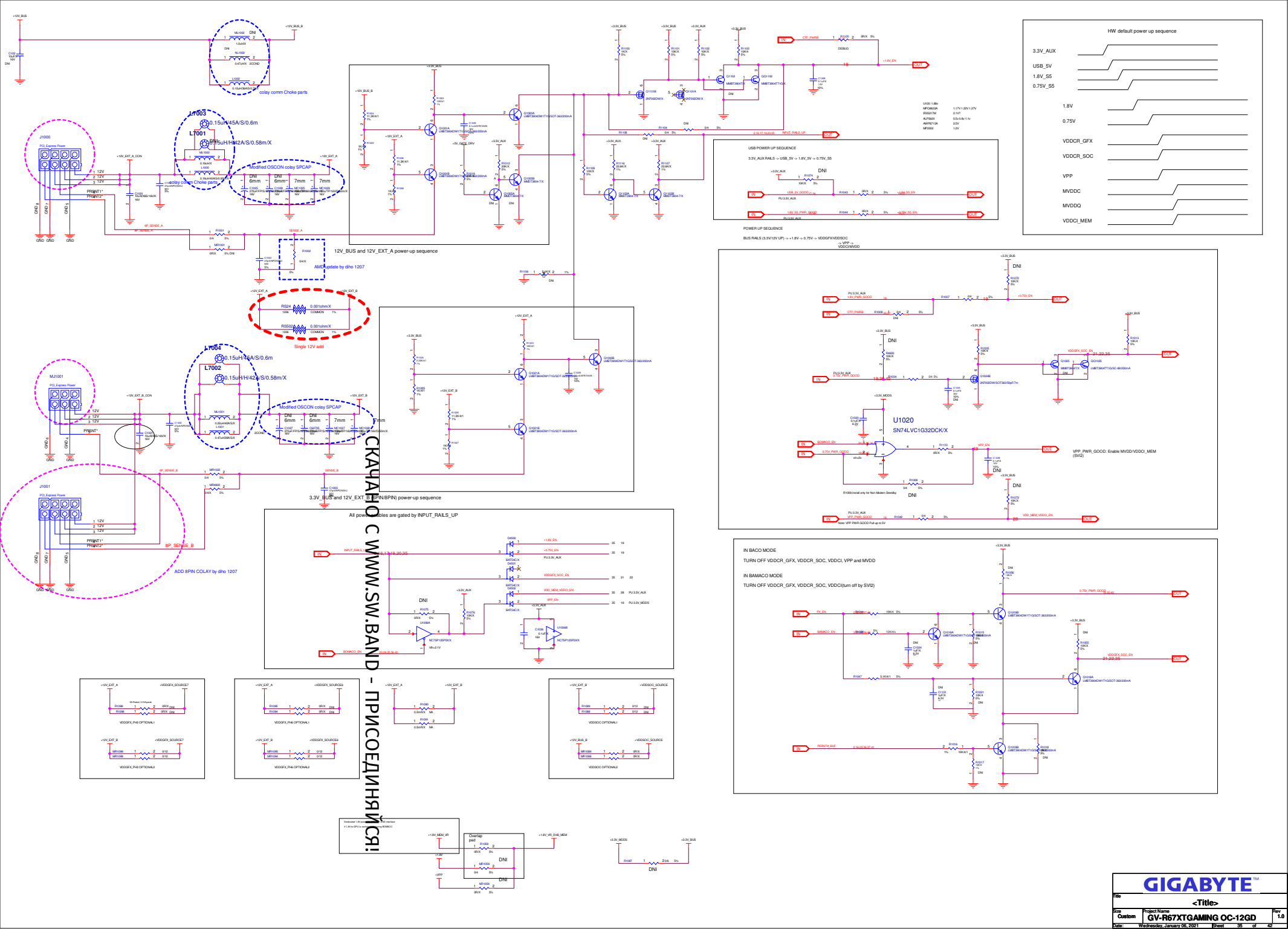

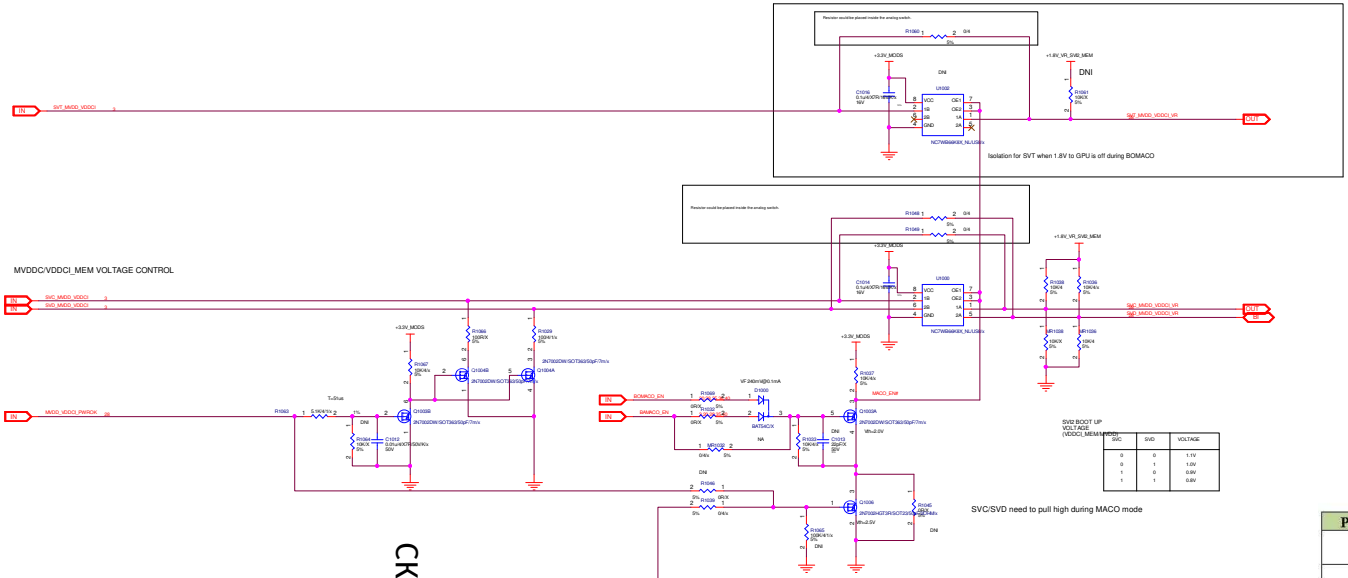

Table 3 Power Rails Status at BAMACO Mode

Power Rail at ASIC	Used logic	ON/OFF Status
VDDCR_GFX	GFX IP, SDMA, GFXCLK CLKIP	OFF
VDDCR_SOC	SYS IPs	OFF
VDDCI_MEM	MEM PHY	OFF
VDDIO_MEM	MEM PHY	ON
MVDDC/MVDDQ/VPP	DRAM	ON
VDDCR_BACO	NBIO/THM/MP1/USB	ON
VDDIO_PCIE	PCIe PHY	ON
VDDAN_C	PHY	ON
VDDAN_18	PLL, PHY, etc	ON
VDDIO_18	1.8V GPIO, I2C, etc	ON
VDDIO_33	3.3V GPIO	ON
VDDAN_*_EFUSE	EFUSE	ON
VDDCR_S5*	USB	ON

Table 4 Key Signals at Different PM Modes

Signals	Default	BOCO	BOMACO
PX_EN	LOW	N/A	N/A
MACO_EN	N/A	N/A	N/A
PERSTb	HIGH	1->0->1	1->0->1
PWR_EN	HIGH	LOW	LOW
BOMACO_EN	LOW	LOW	HIGH

Table 5 Platform to Support BOMACO

BOMACO_EN	SVC PU/PD	SVD PU/PD
HIGH	PU	PU
LOW	bootVID	bootVID

Table 3 Power Rails Status at BOMACO Mode

Power Rail at ASIC	Used logic	ON/OFF Status for BOMACO-A
VDDCR_GFX	GFX IP, SDMA, GFXCLK CLKIP	OFF
VDDCR_SOC	SYS IPs	OFF
VDDCI_MEM	MEM PHY	OFF
VDDIO_MEM	MEM PHY	ON
MVDDC/MVDDQ/VPP	DRAM	ON
VDDCR_BACO	NBIO/THM/MP1/USB	OFF
VDDIO_PCIE	PCIe PHY	OFF
VDDAN_C	PHY	OFF
VDDAN_18	PLL, PHY, etc	OFF
VDDIO_18	1.8V GPIO, I2C, etc	OFF
VDDIO_33	3.3V GPIO	OFF
VDDAN_*_EFUSE	EFUSE	OFF
VDDCR_S5*	USB	ON

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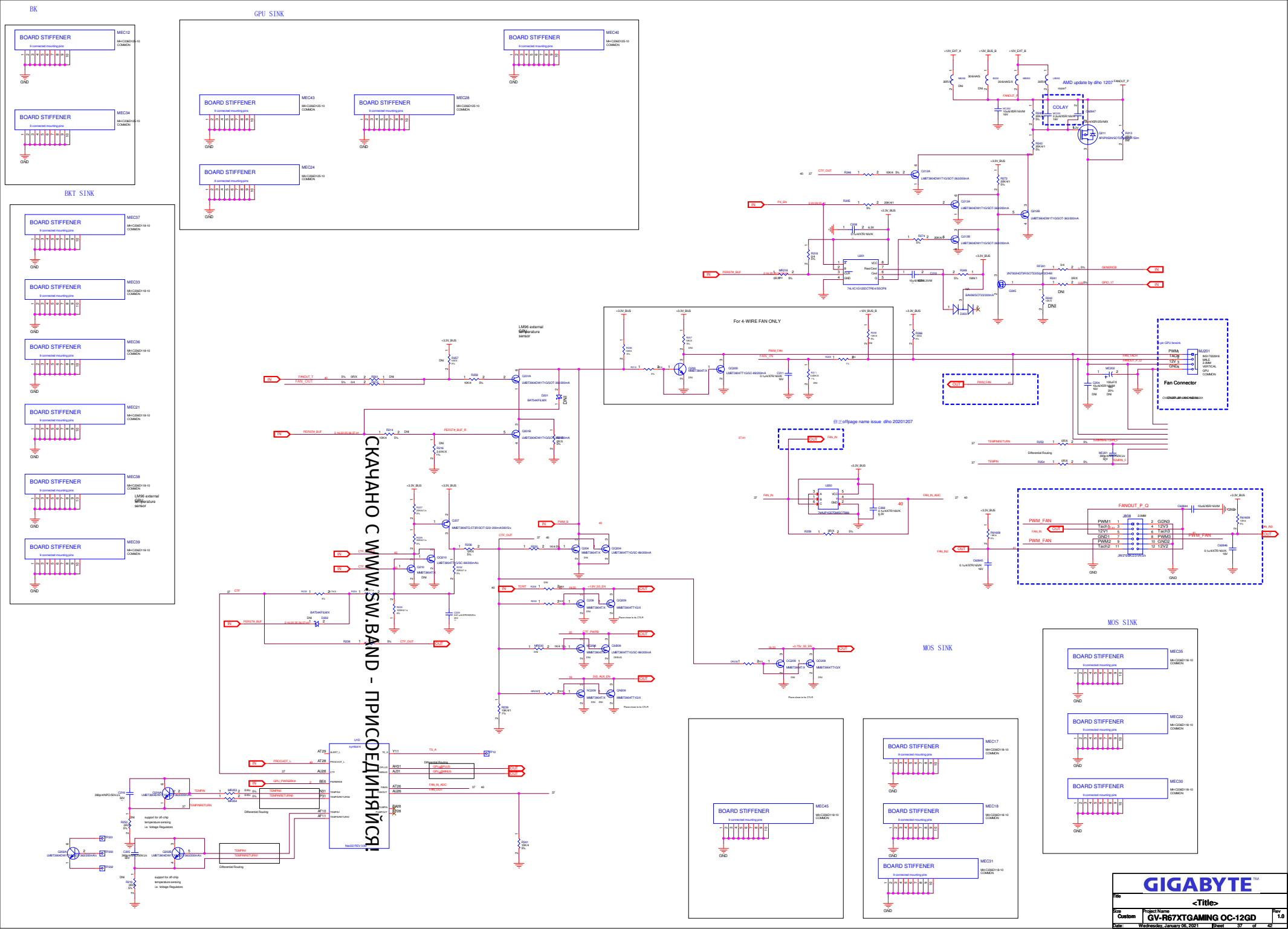

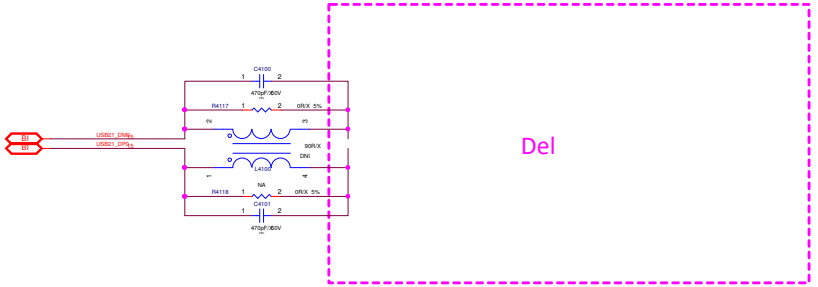

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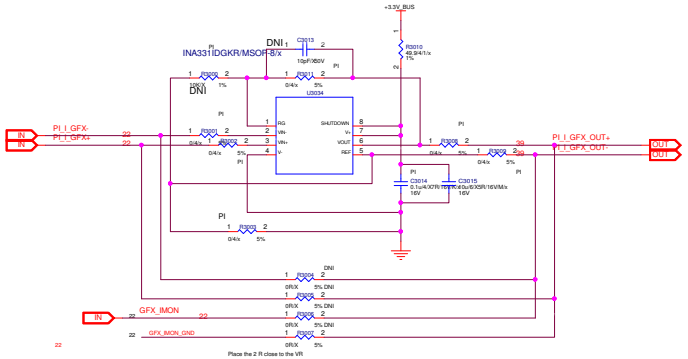

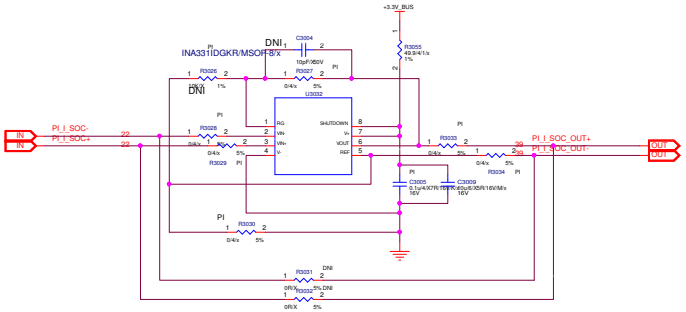

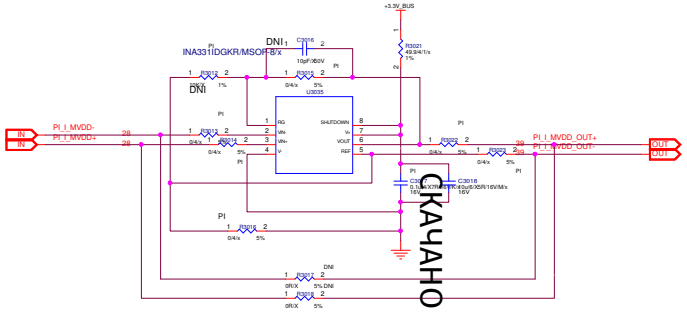

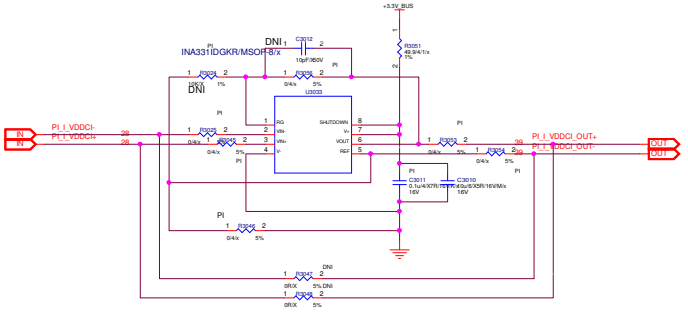

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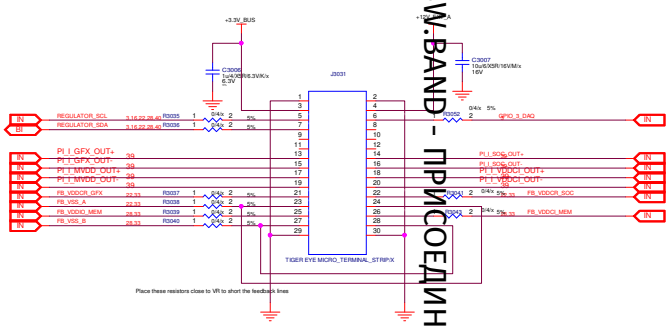

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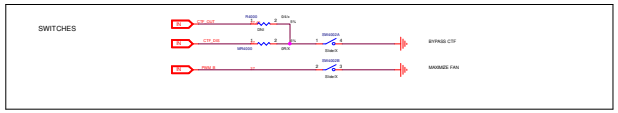

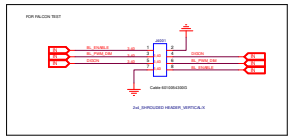

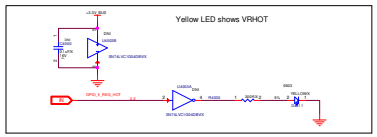

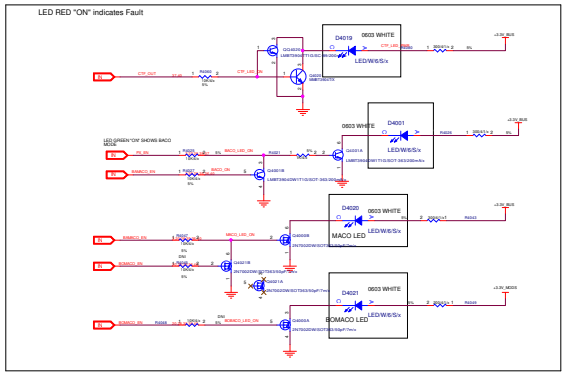

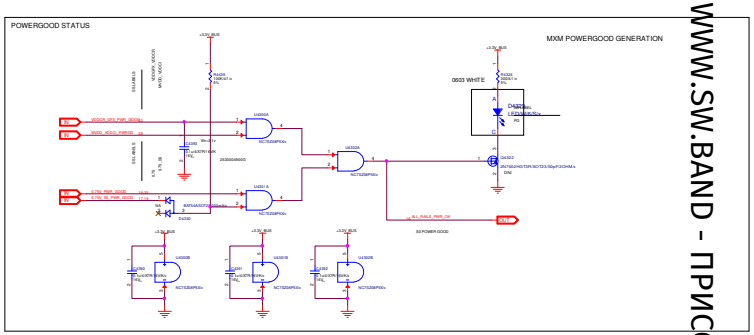

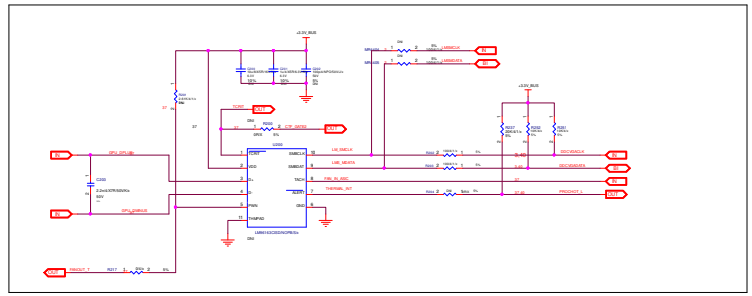

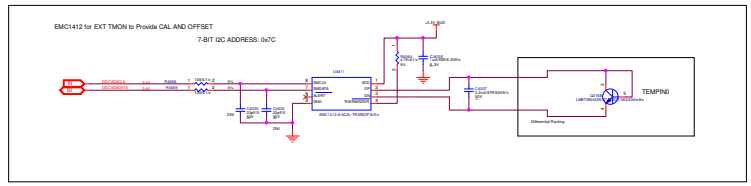

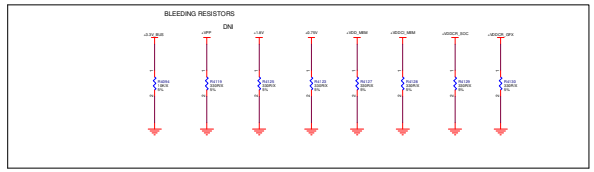

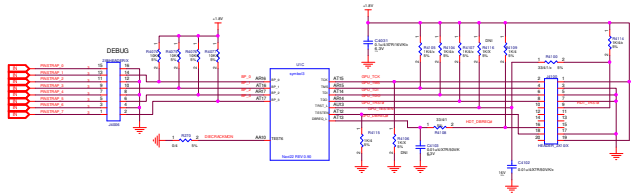

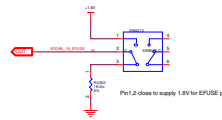

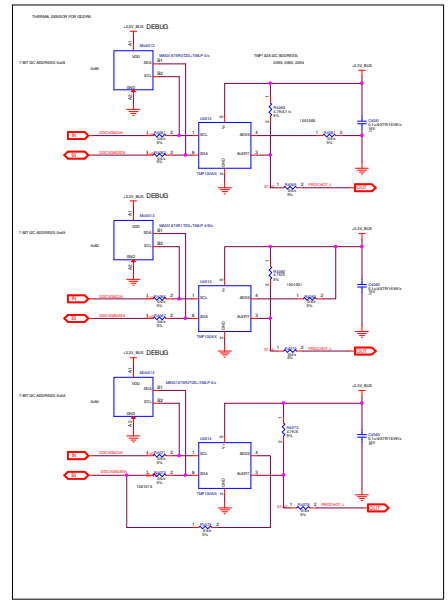

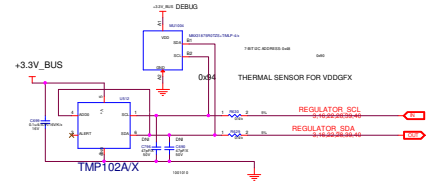

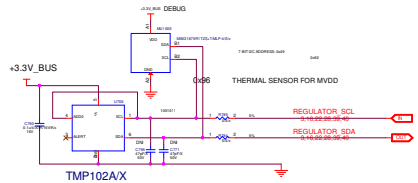

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Page63: MCU

(5V - Ref 2.5V) / 5mA = 500 Ohm
(12V - Ref 2.5V) / 5mA = 1.9K Ohm

U7102
HT32F52342/LQFP48/S

12V ADC Measure

GPIO

Winforce LED CONNECT

3V3 sence for MKT demo

MCU Slave

add by diho 20181224

QD SKU CABLE

CON2 EVT上件for FW 燒錄，PVT移除

STACK L7005

add by diho

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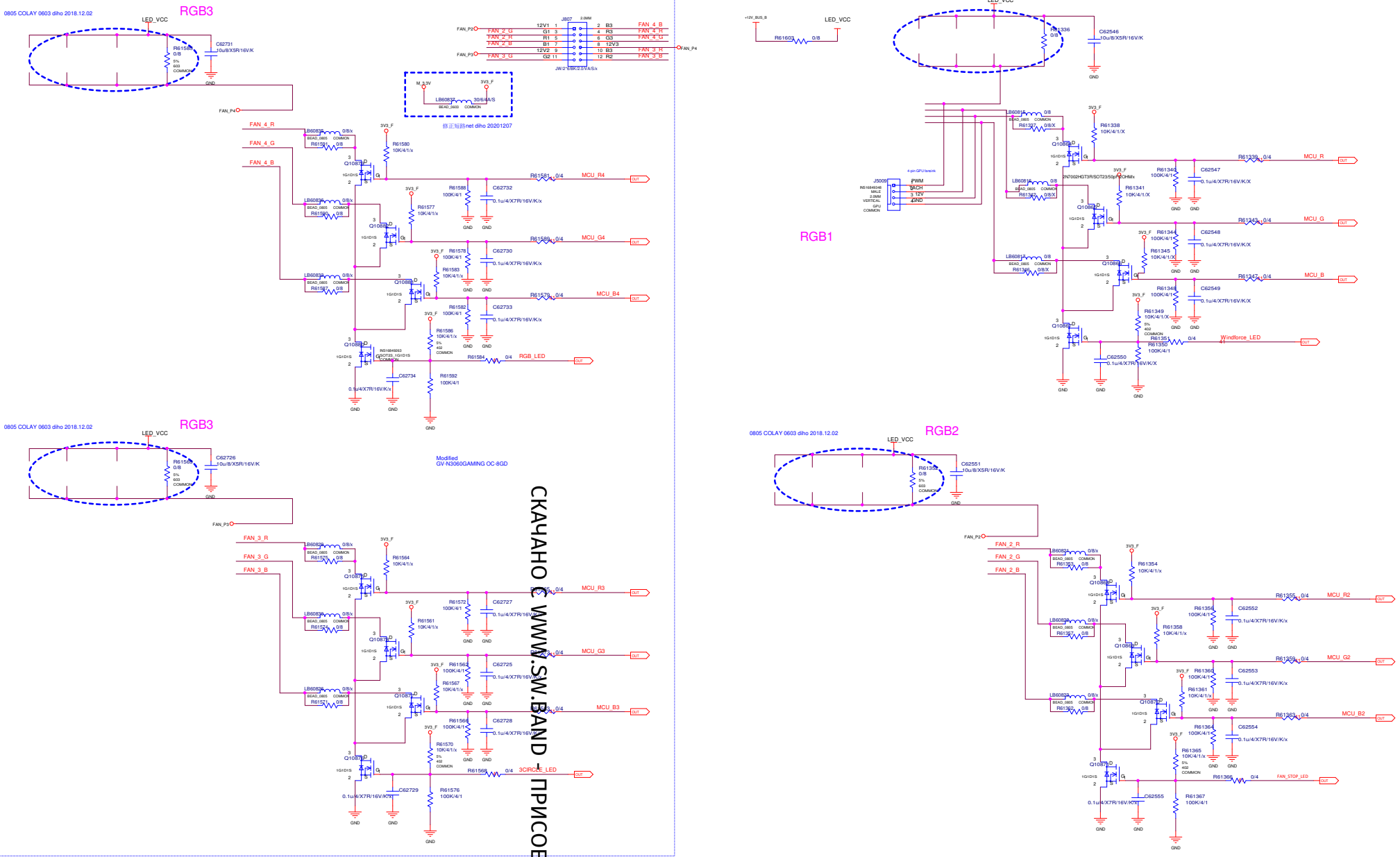

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