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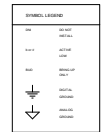
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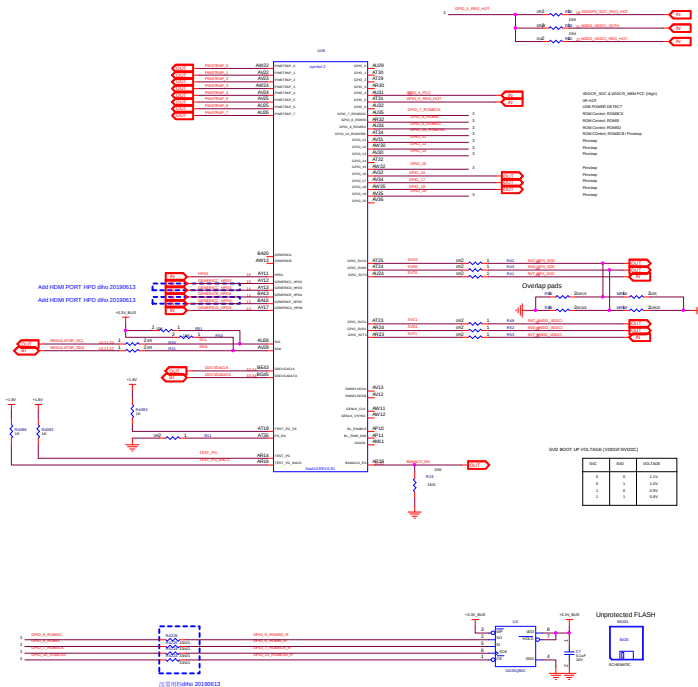
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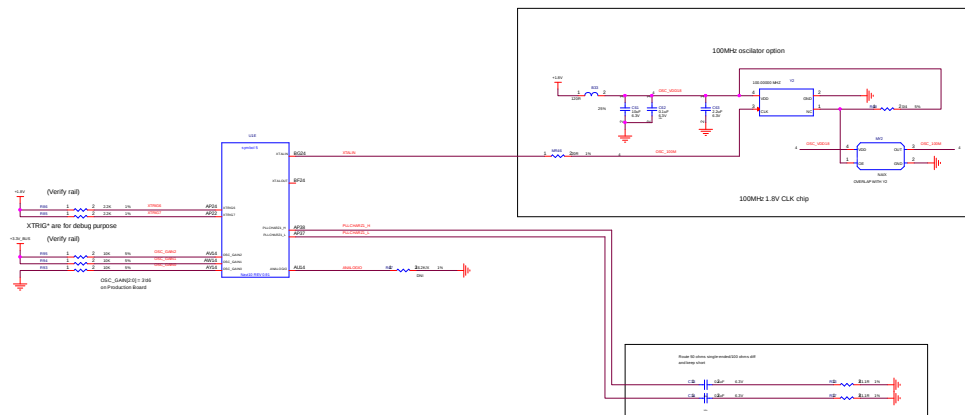
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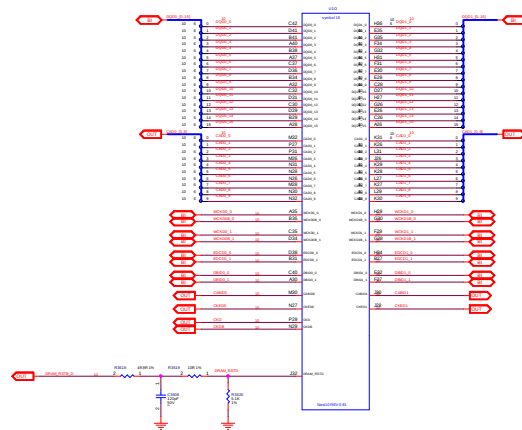
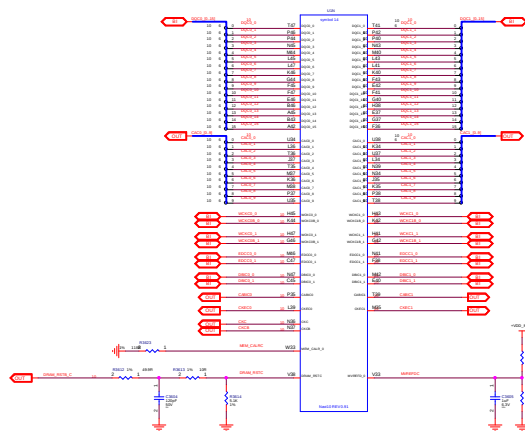


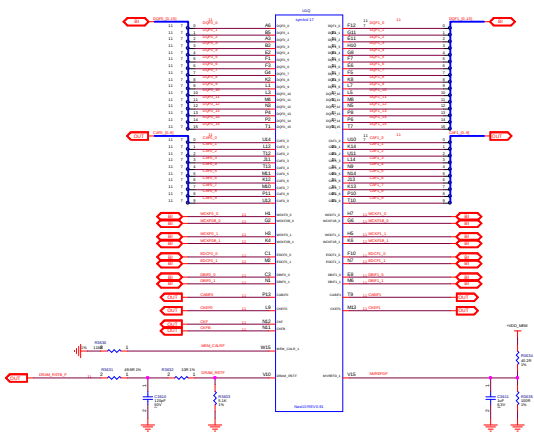
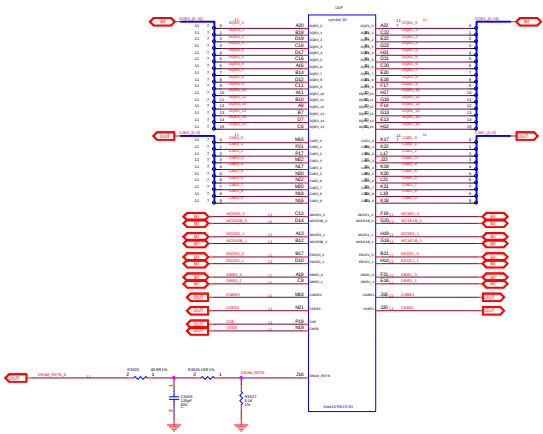


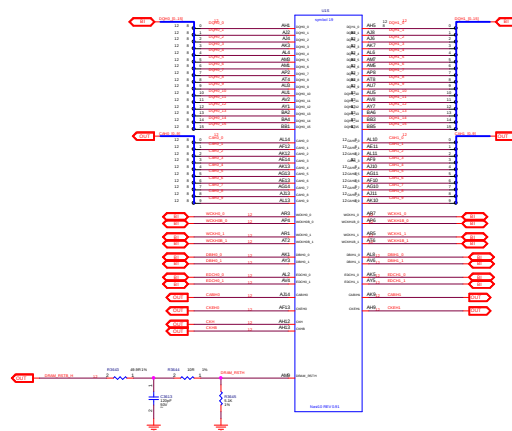
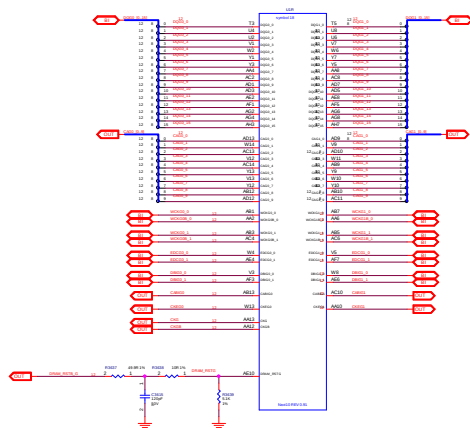
User	Internal Default Value	Definition
BIF	0	STRAP_BIF_GENA_DIS_A 0. BIF GENA is disabled 1. BIF GENA is enabled
	0	PINSTRAP_BIF_CLK_PM_EN 0. BIF clock power management is disabled 1. BIF clock power management is enabled
	0	PINSTRAP_BIF_LC_TX_SWING 0. BIF LC TX swing is disabled 1. BIF LC TX swing is enabled
	0	PINSTRAP_BIF_VGA_DIS 0. BIF VGA is disabled 1. BIF VGA is enabled
DCE	0	PINSTRAP_AUD_PORT_CONN[0] Number of audio capacitor display outputs 0. 0 capacitors connected 1. 1 capacitor connected 2. 2 capacitors connected 3. 3 capacitors connected
	0	PINSTRAP_AUD[1:0] 0. BIF audio is disabled 1. BIF audio is enabled
	0	PINSTRAP_AUD[1:0] 0. BIF audio is disabled 1. BIF audio is enabled
	0	PINSTRAP_AUD[1:0] 0. BIF audio is disabled 1. BIF audio is enabled
Platform	0	PINSTRAP_1 0. BIF is disabled 1. BIF is enabled
	0	PINSTRAP_2 0. BIF is disabled 1. BIF is enabled
	0	PINSTRAP_3 0. BIF is disabled 1. BIF is enabled
	0	PINSTRAP_4 0. BIF is disabled 1. BIF is enabled
SMU	1	PINSTRAP_SMBUS_ADDR 0. BIF is disabled 1. BIF is enabled
	1	PINSTRAP_BIOS_ROM_EN 0. BIF BIOS ROM is disabled 1. BIF BIOS ROM is enabled
	1	PINSTRAP_BIOS_ROM_EN 0. BIF BIOS ROM is disabled 1. BIF BIOS ROM is enabled
	1	PINSTRAP_BIOS_ROM_EN 0. BIF BIOS ROM is disabled 1. BIF BIOS ROM is enabled

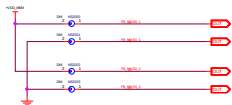
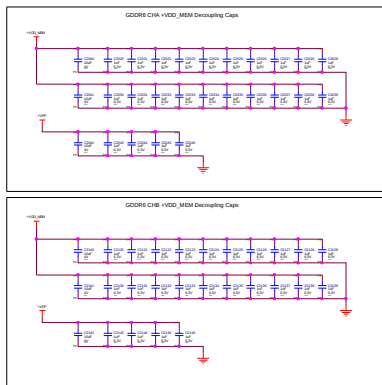
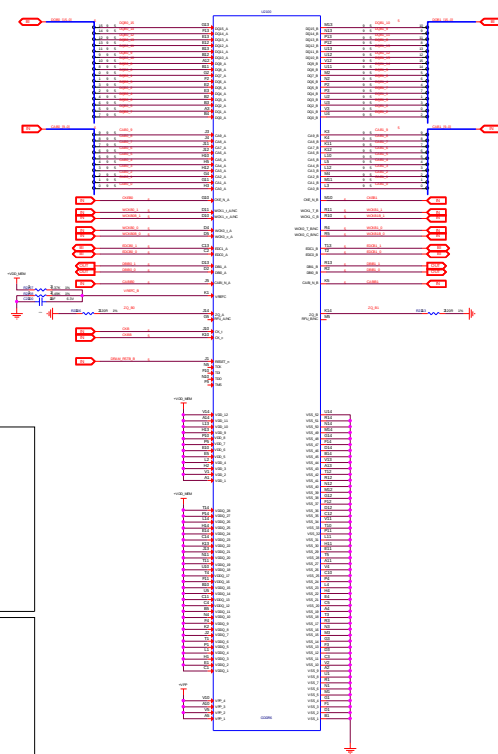




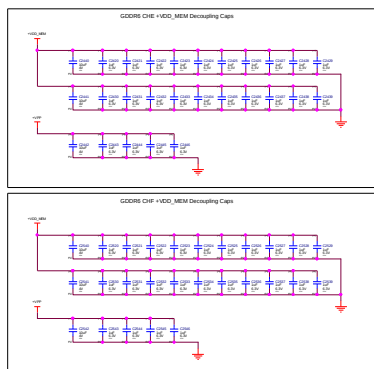
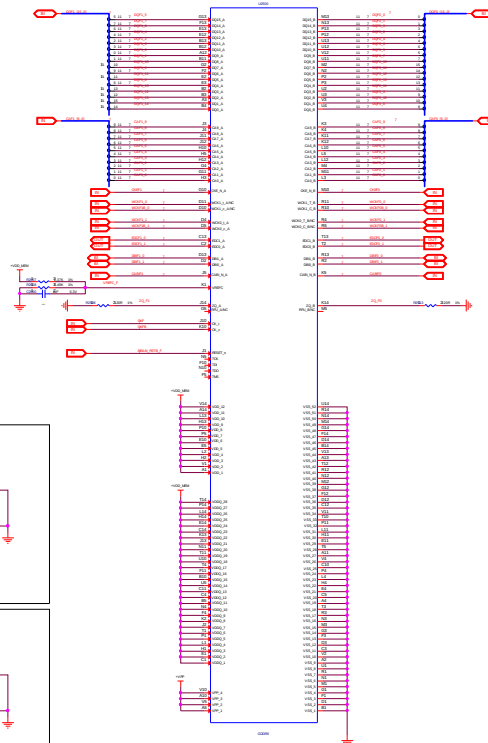












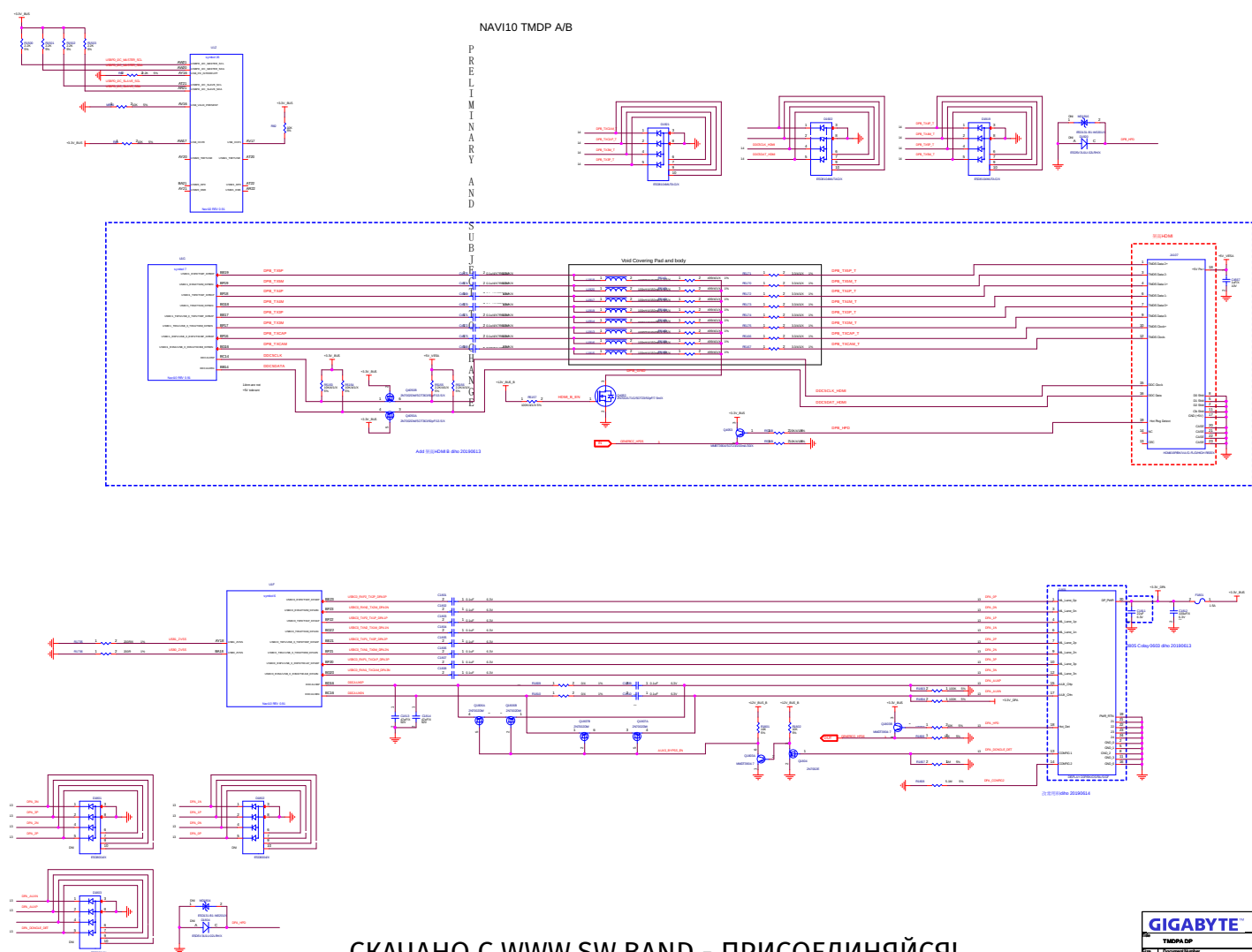
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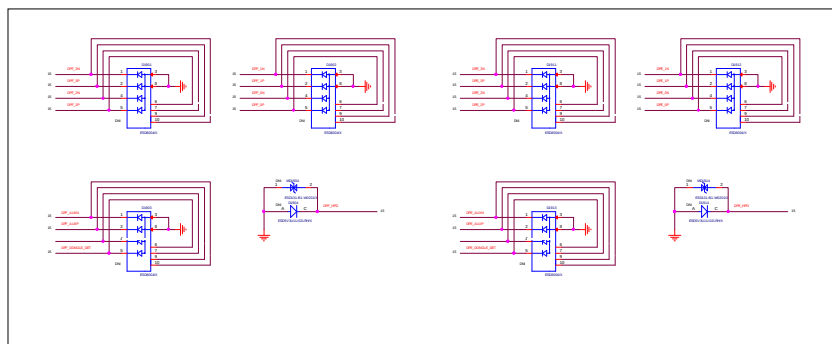
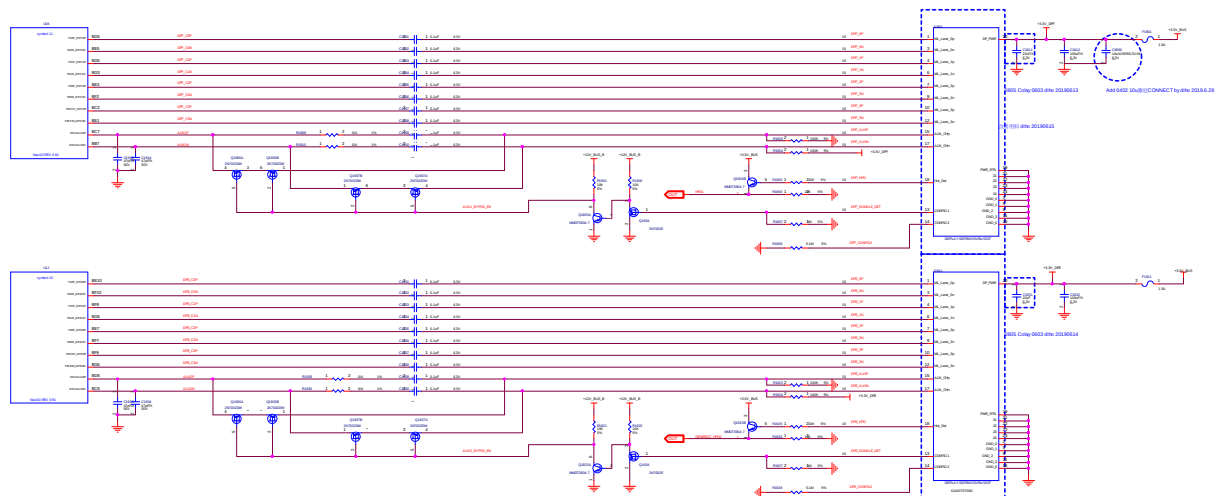
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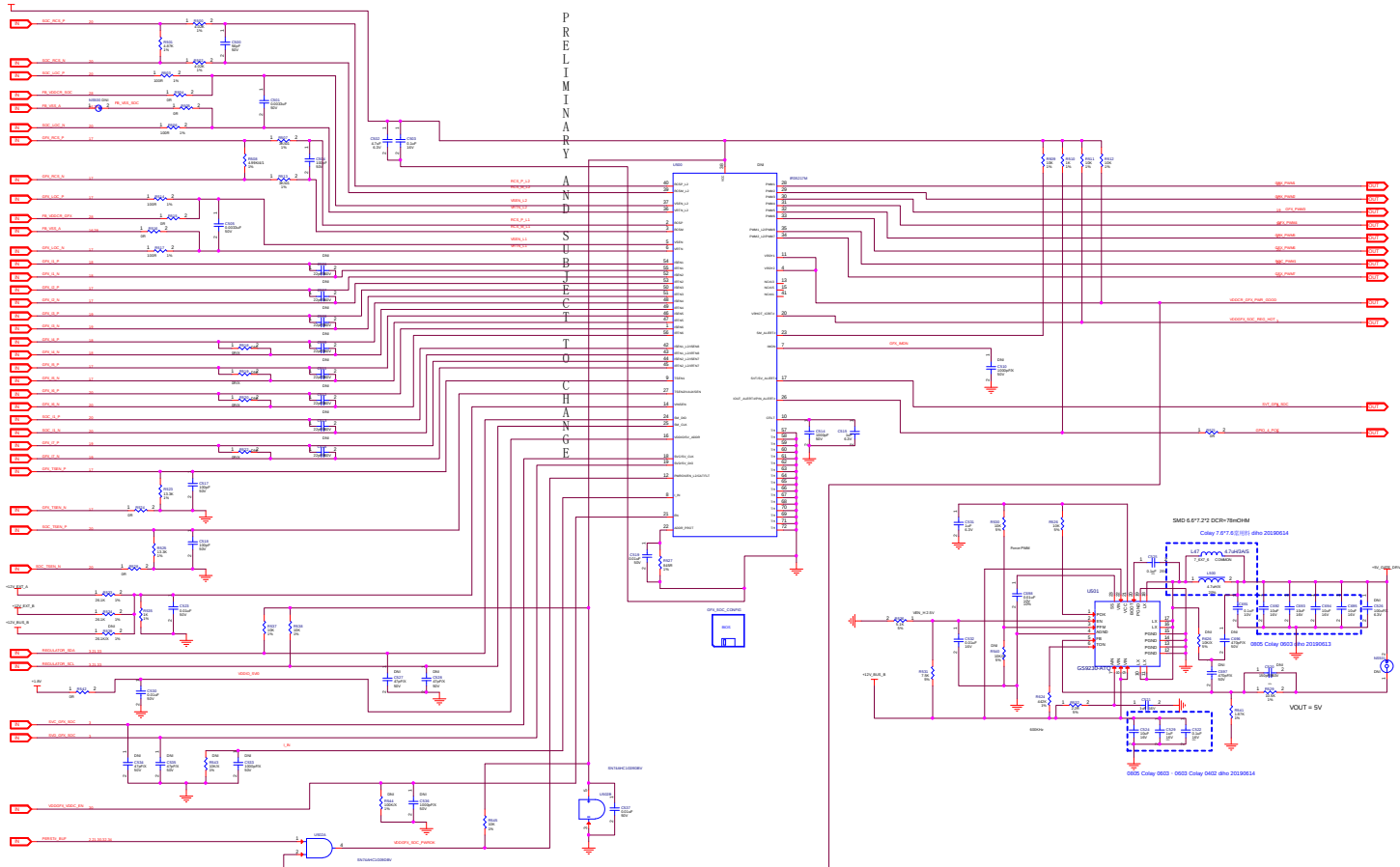


СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

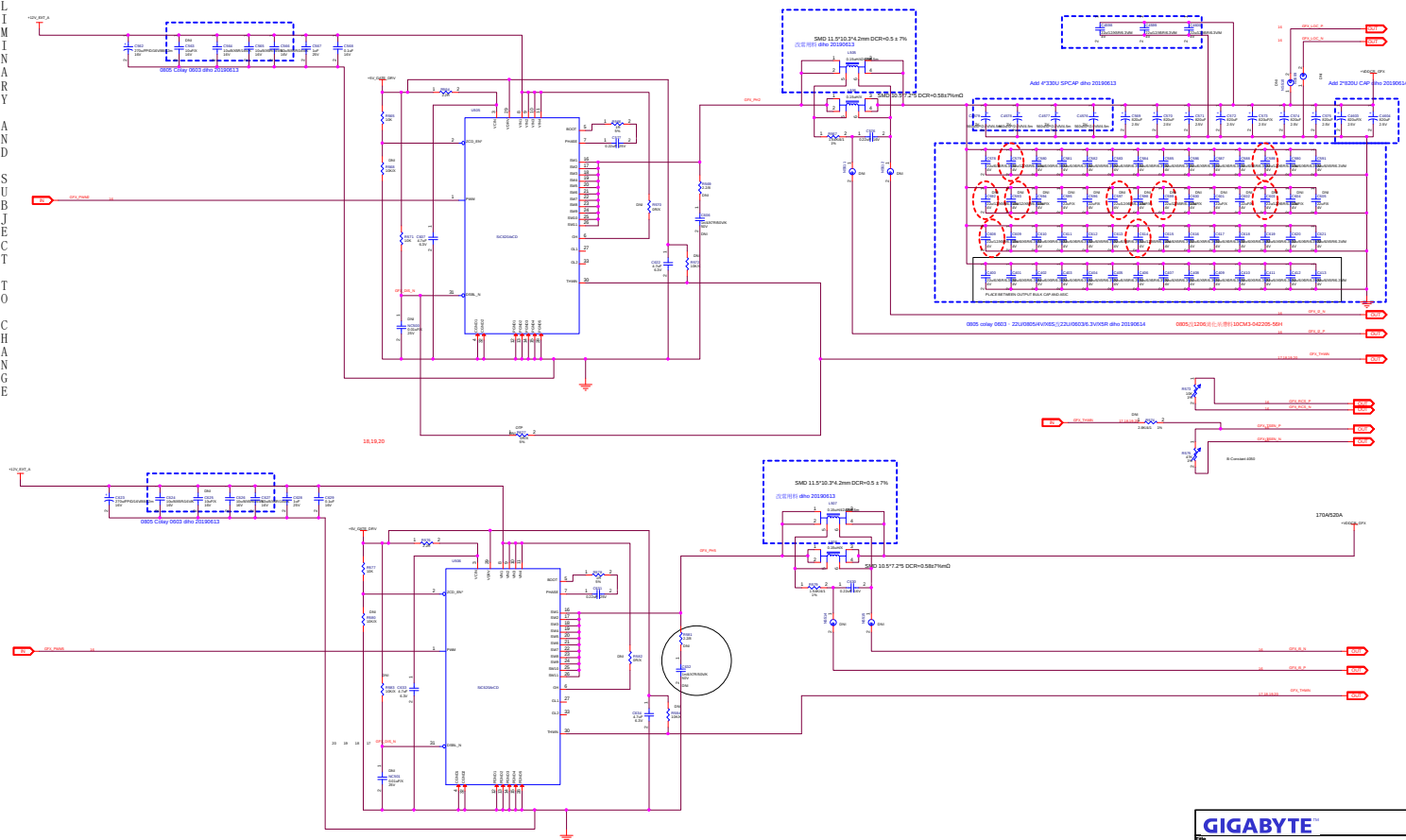


(9) NAVI10 TMDP E/F





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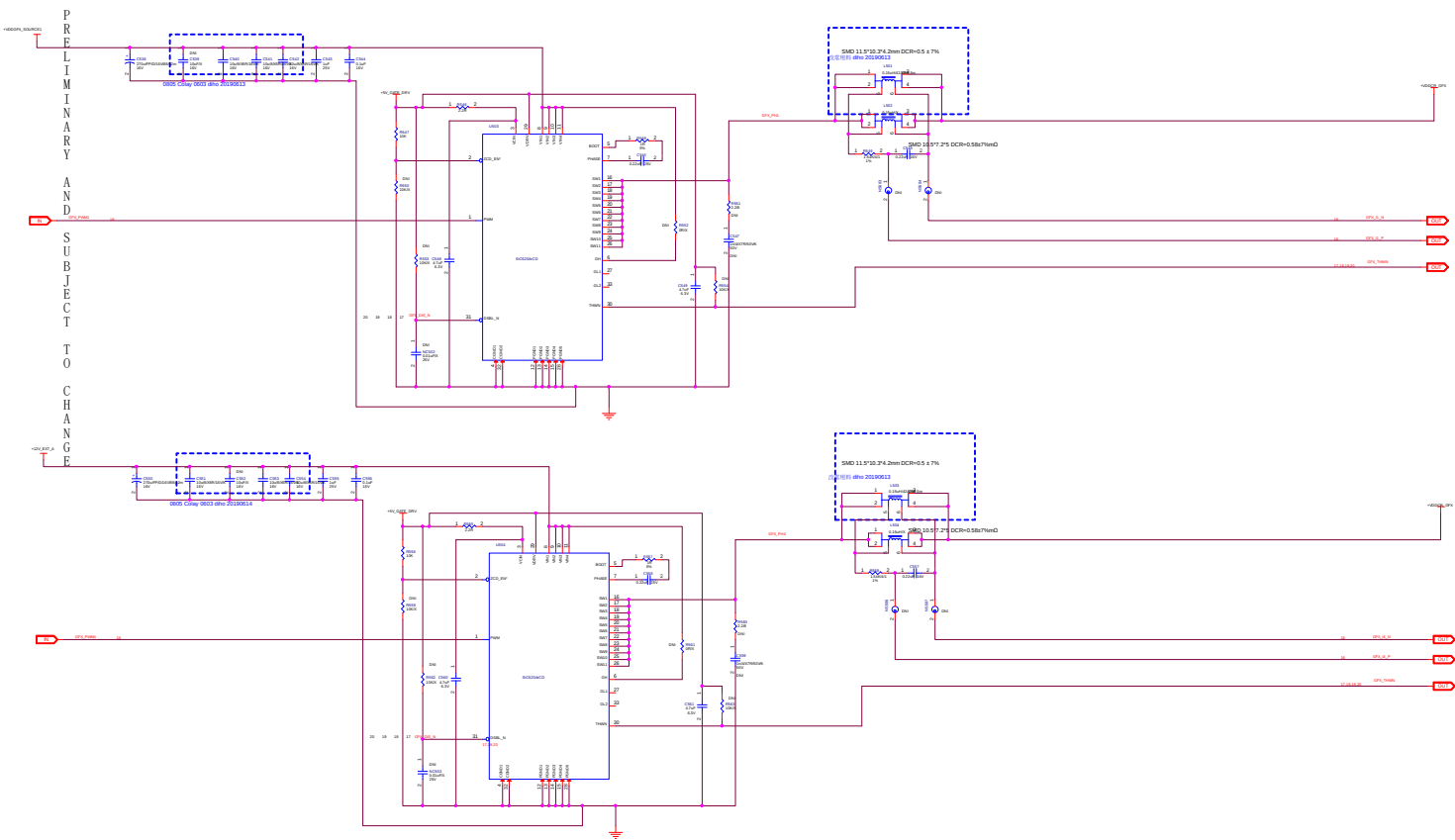
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GIGABYTE

Document Number: **GV-N57XTGAMING-80D**

Rev: 1.0

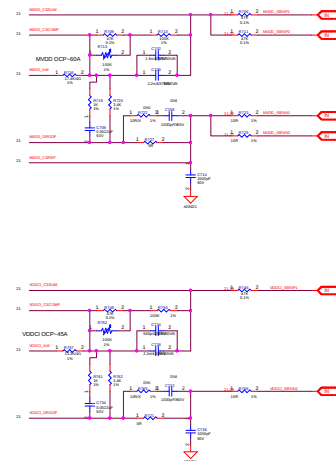
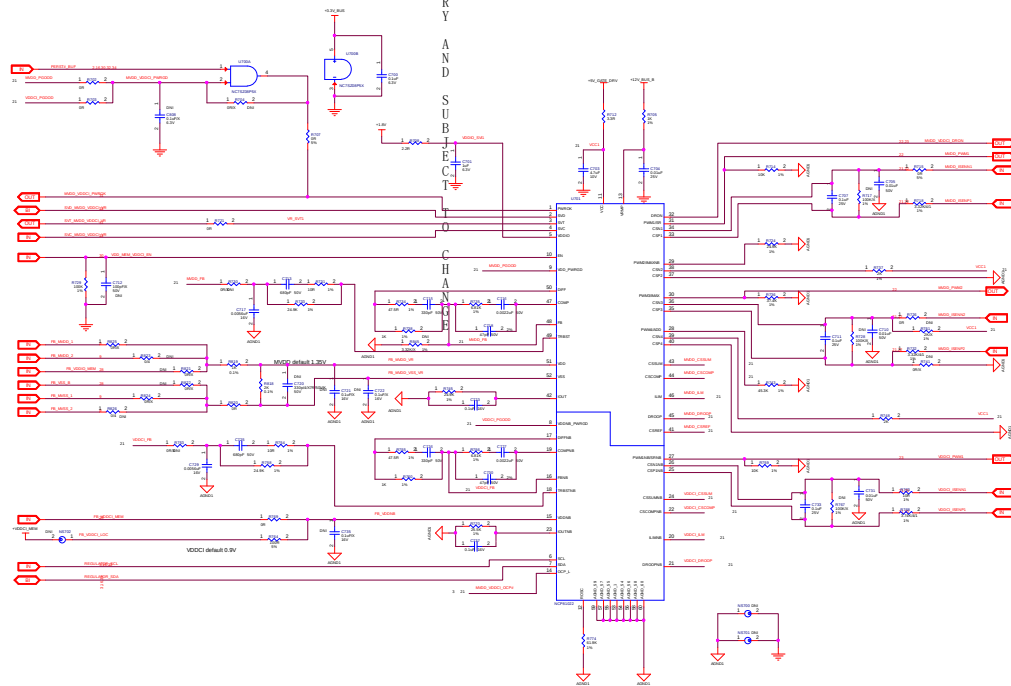
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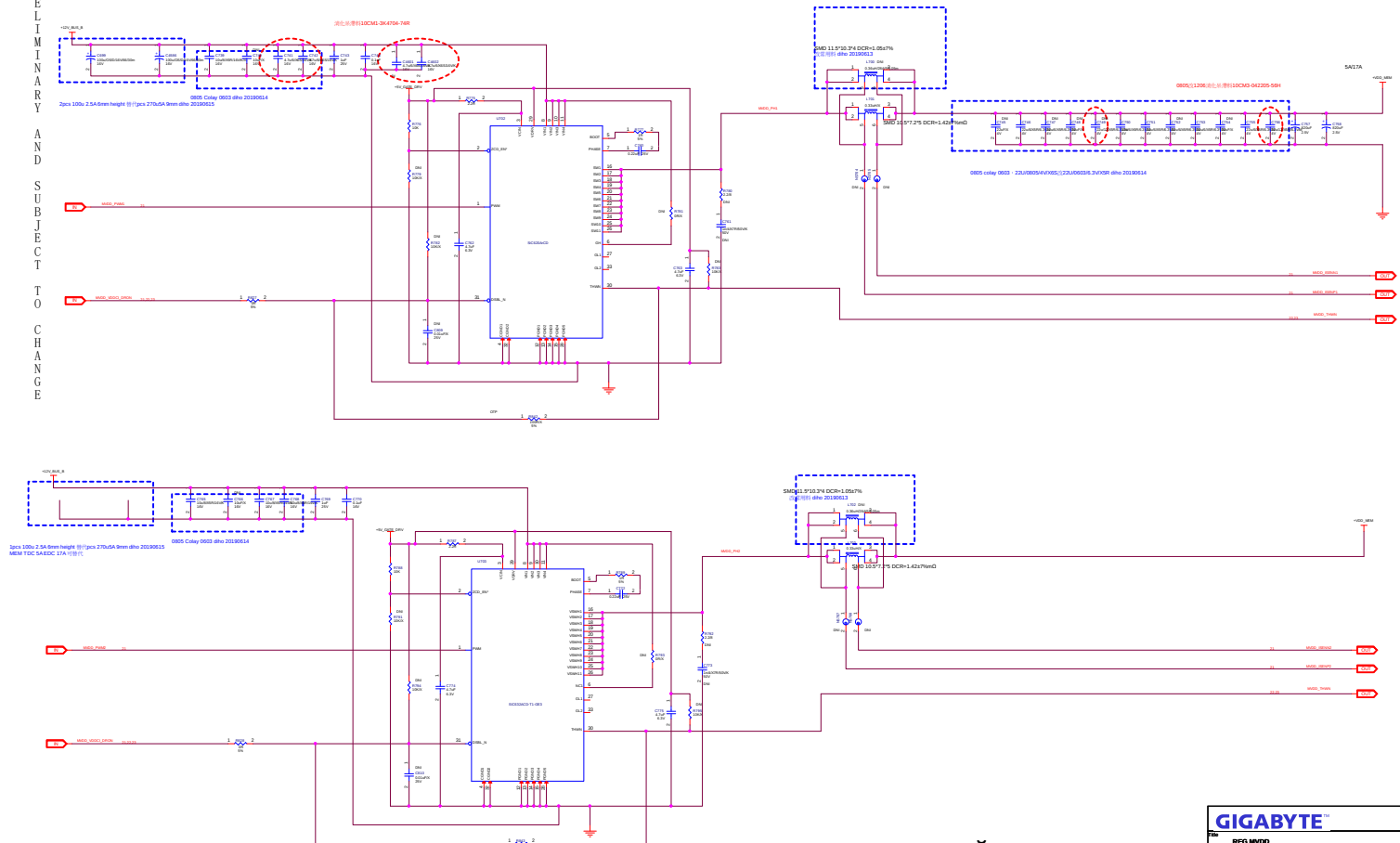
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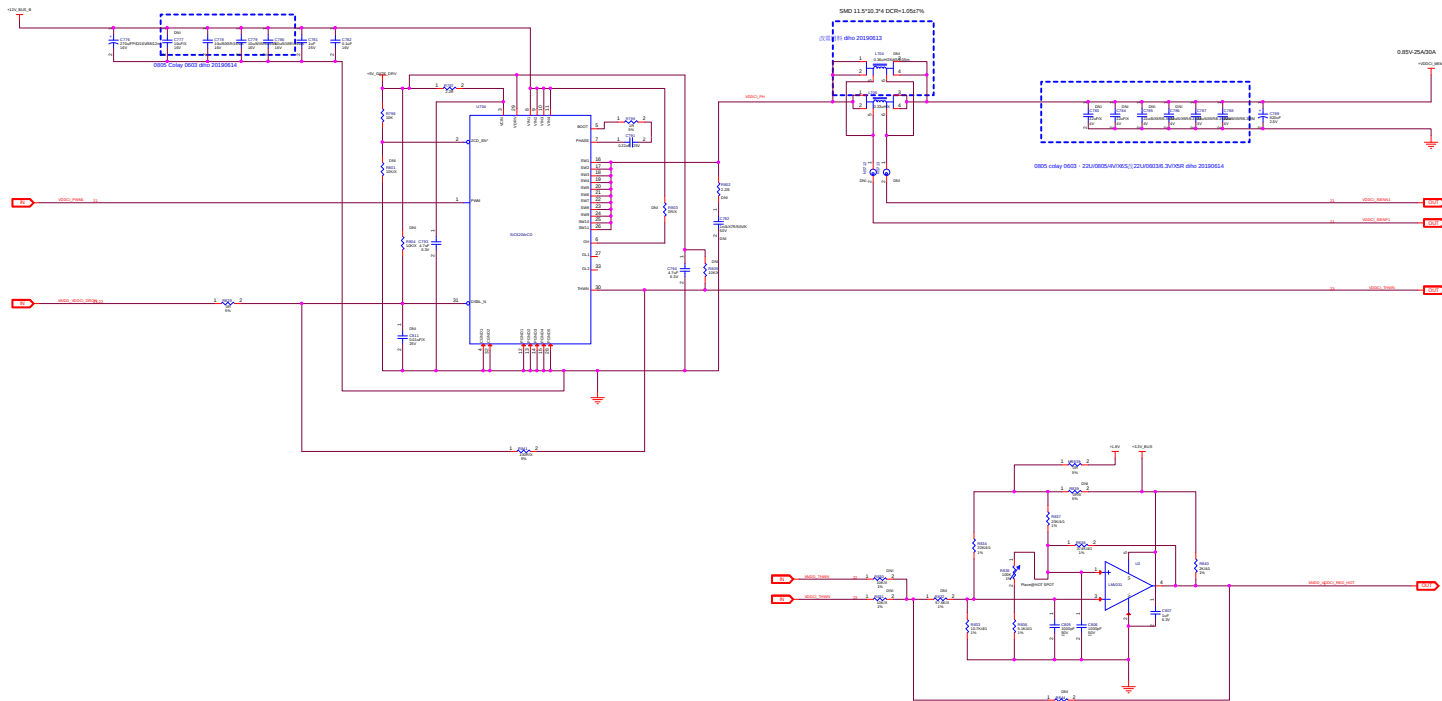
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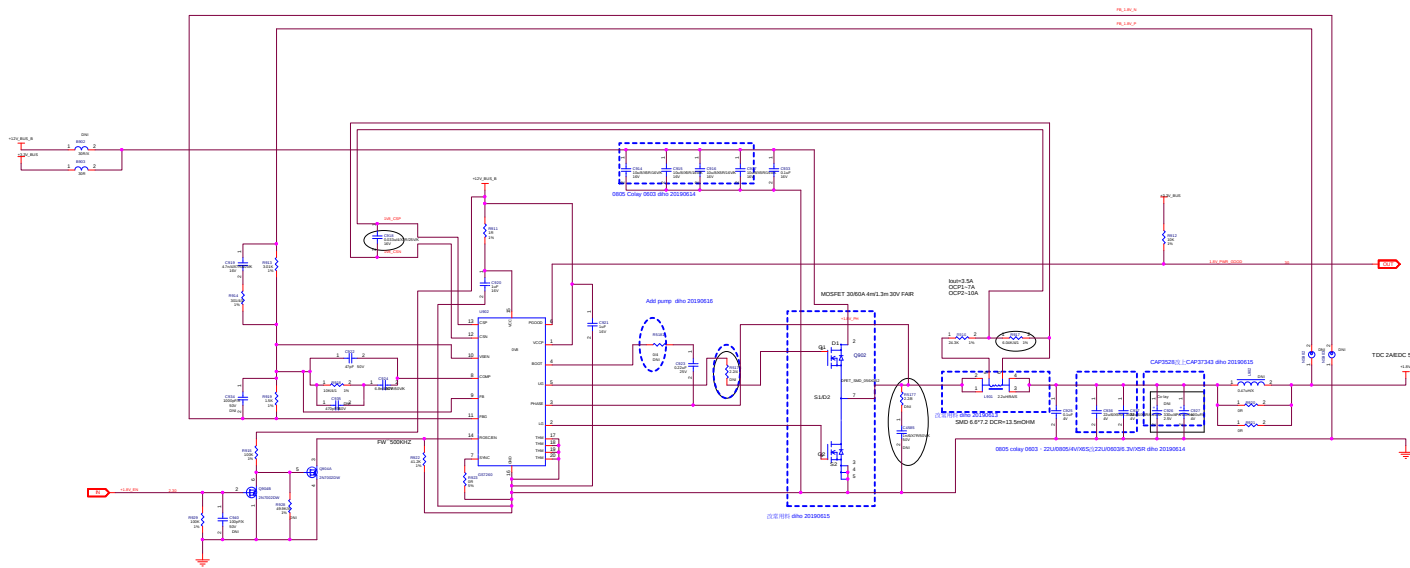


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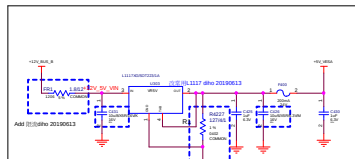


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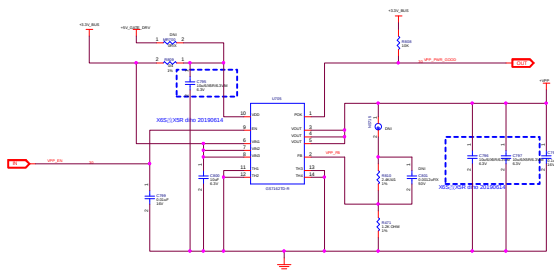




REGULATOR FOR +5V RAILS
IOUT = 500mA



$$V_{OUT} = 1.25 \times (1 + R2/R1) \\ = 1.25 \times (1 + 383/127) \\ = 5.02V$$



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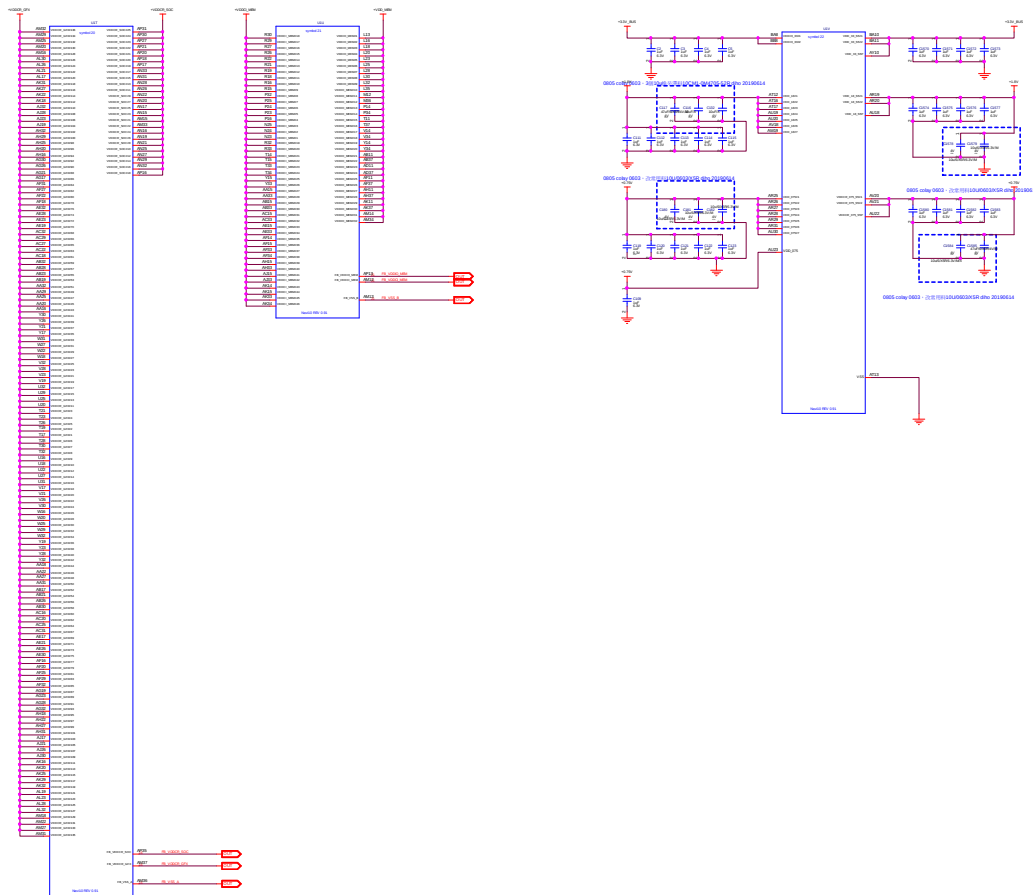
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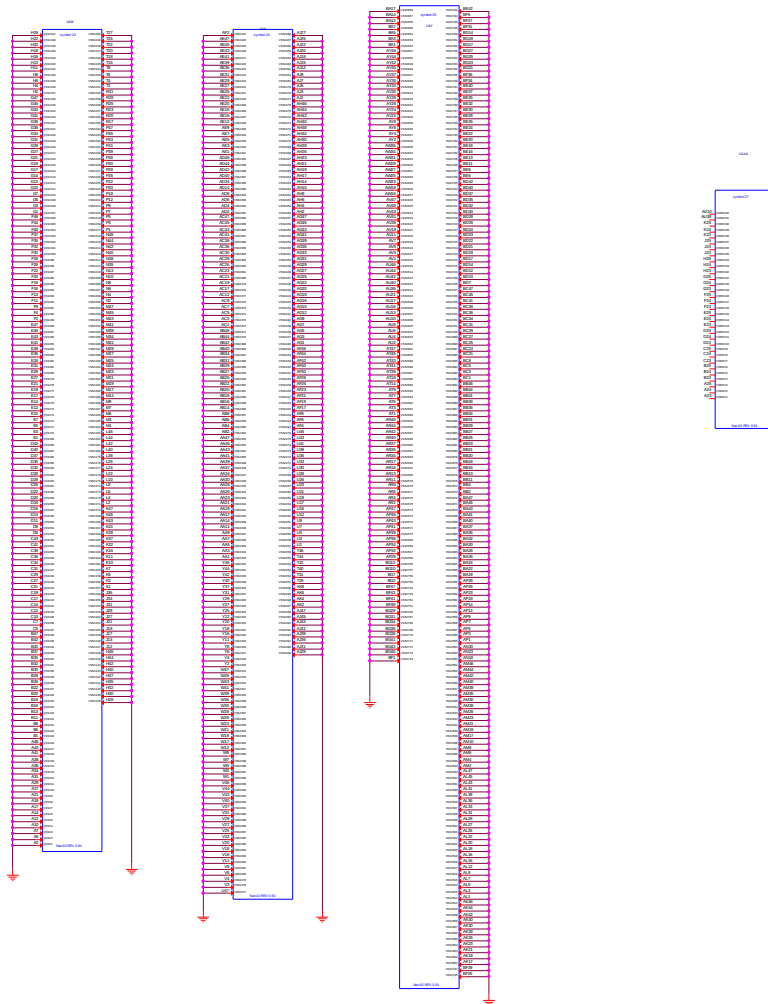
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Doc: Document Number: GV-HS7XTGAMING-R00

Rev: 1.0 Date: February 14, 2019 Sheet: 26 of 36

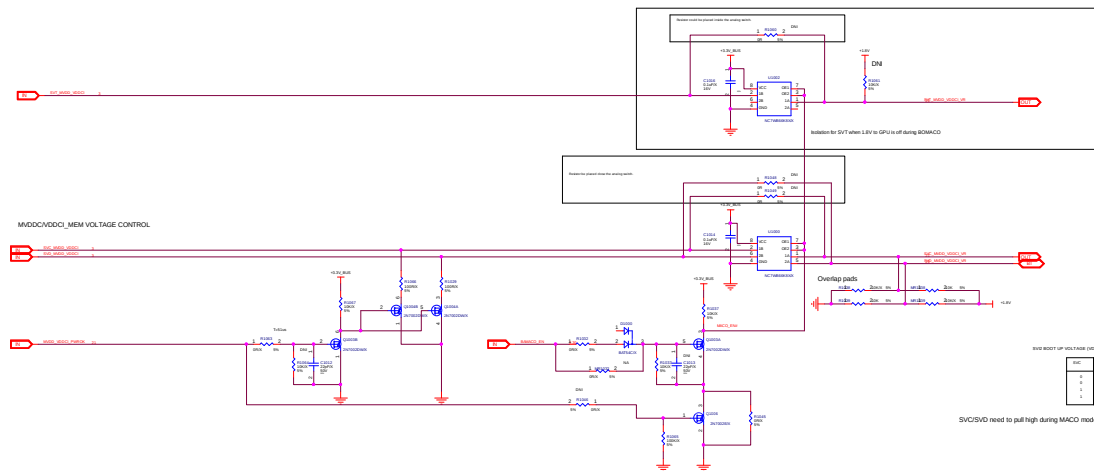






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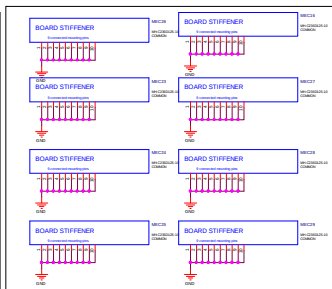
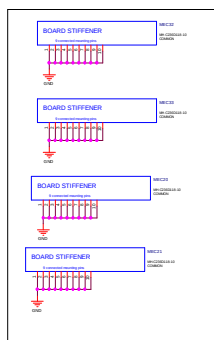




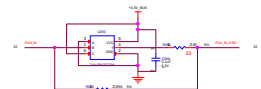
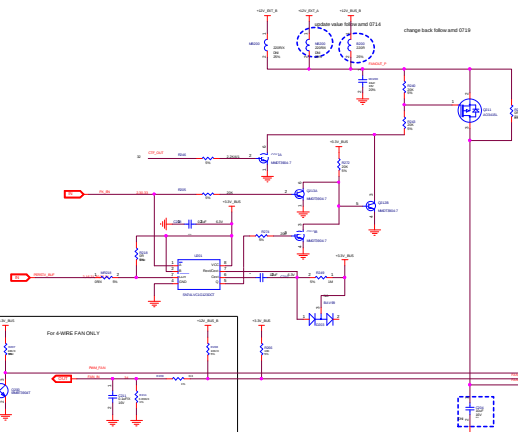
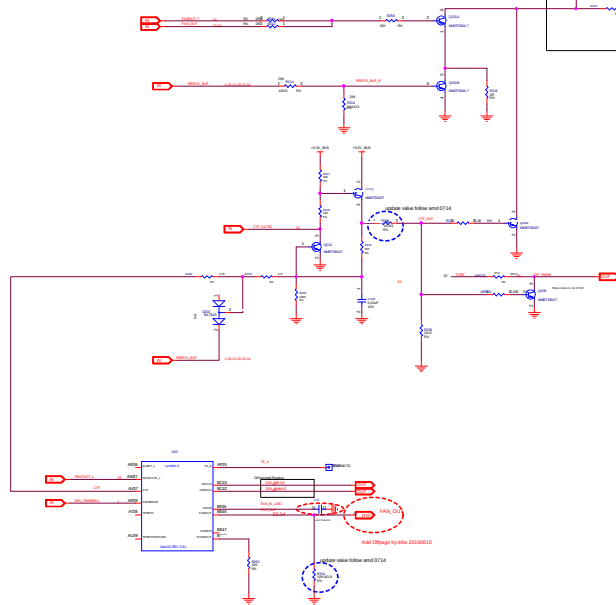
DRT SINK

20190619, by dtho 20190619

GPU SINK

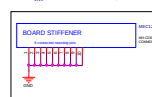


1.000V reference (GPU)

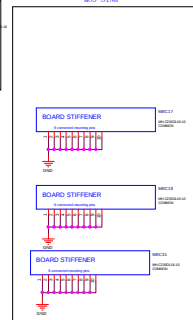


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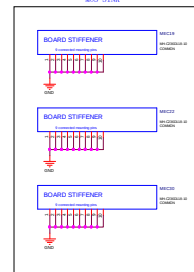
改常用螺絲孔 by dtho 20190619

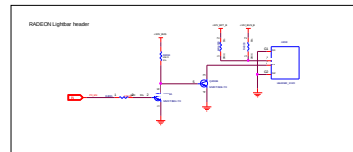
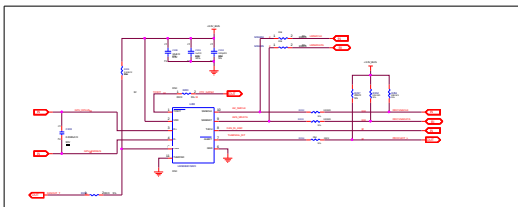
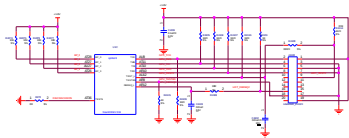


MIS SINK

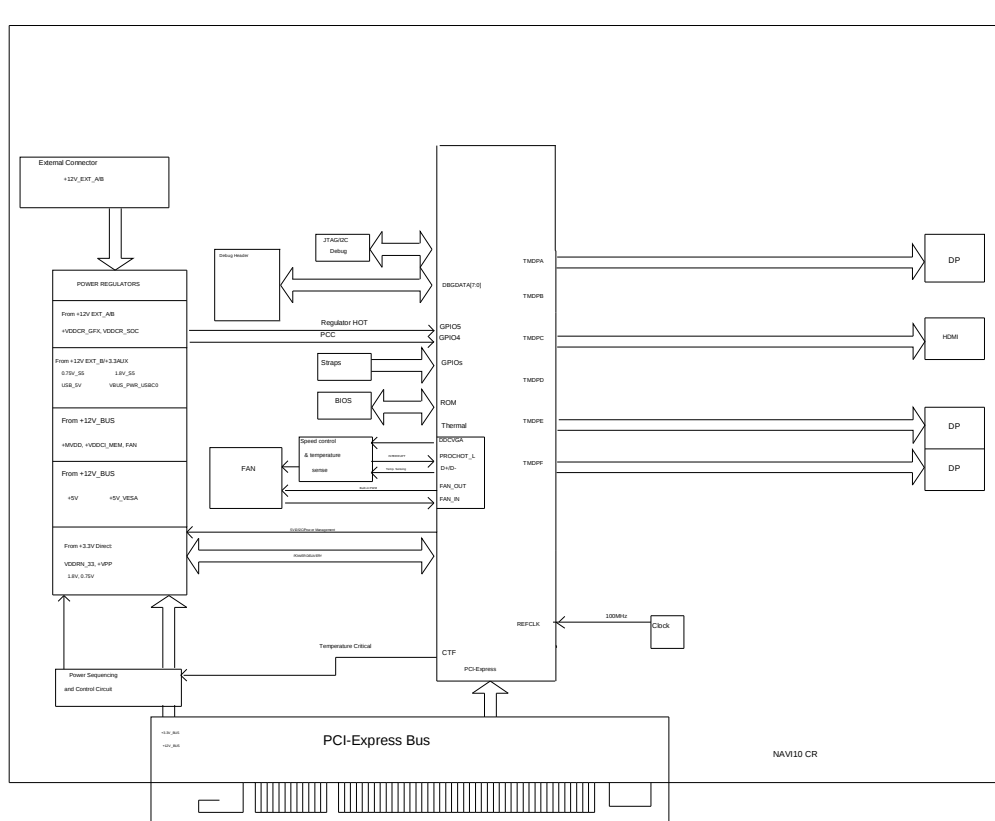


MIS SINK









СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!