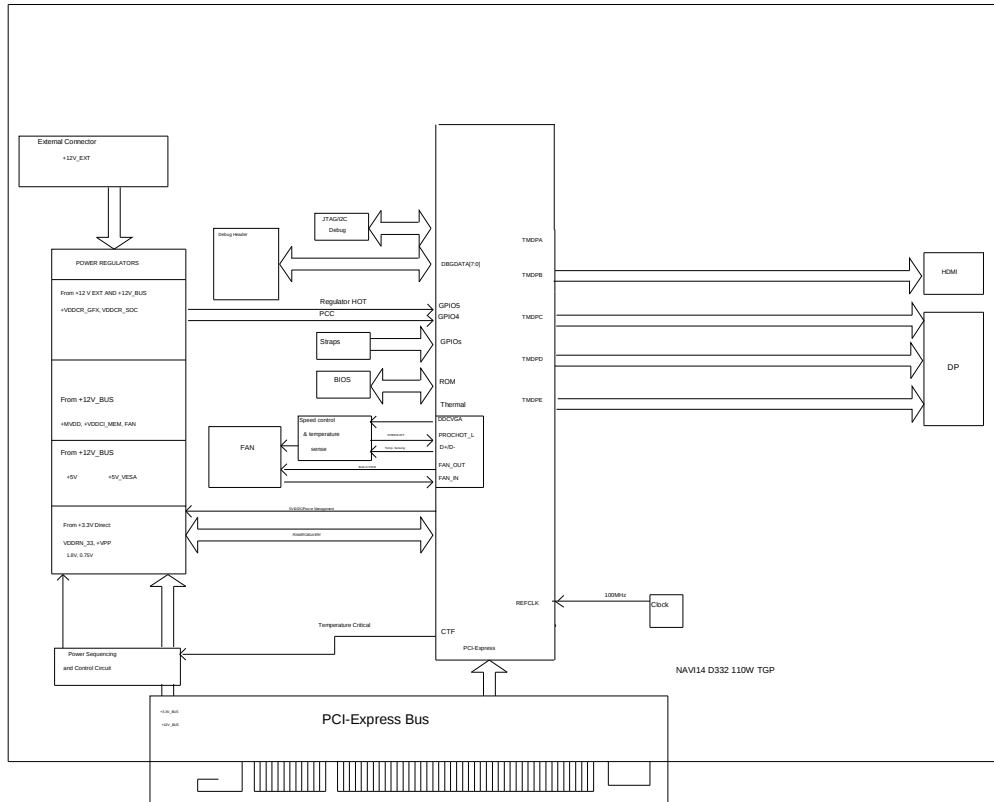
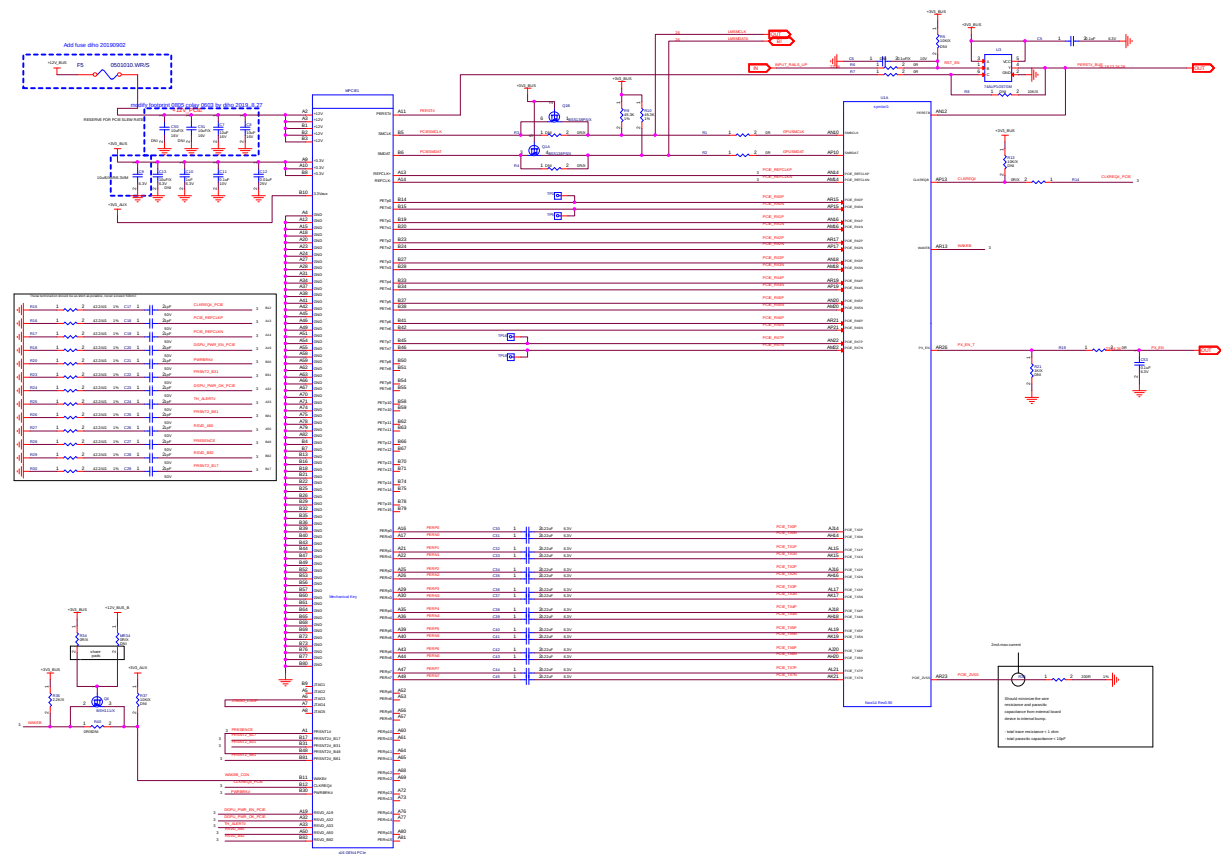


NAVI14 GDDR6 4PCS,10L 3xDPs +1xHDMI
105-D332xx-00B RevB Desktop board(110W TGP)

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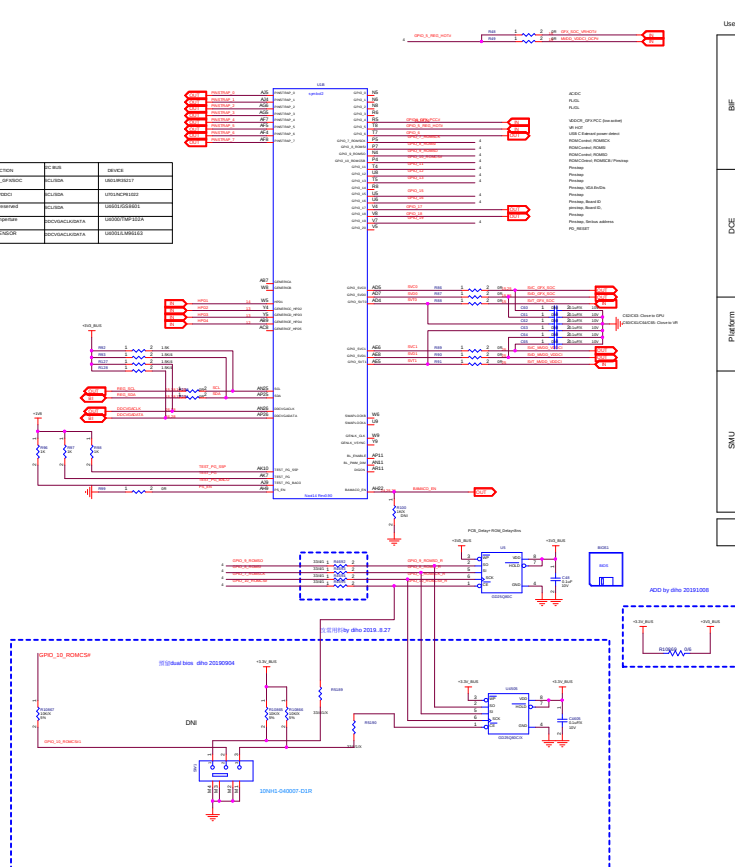
SHEET NO.	SHEET NAME	SHEET NO.	SHEET NAME
1	TOC	26	DEBUG CIRCUITS
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16	REG VDDGFX		
17	REG VDDSDC		
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19	REG VDDCMMIO CONTL		
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23	MECHANICAL AND THERMAL		
24	POWER MANAGEMENT		
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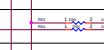
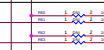
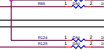


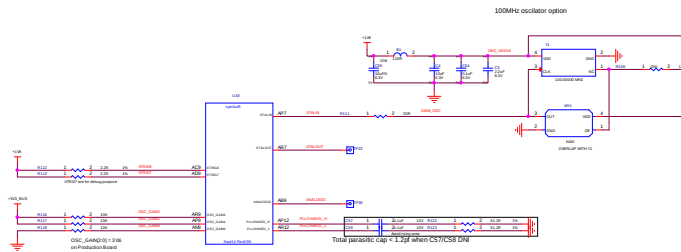


SYMBOLS USED	
One	ONE NOT MULTIPLY
None	NOT NOT NONE
Both	BOTH AND ONE OR
	Definition Glossary
	Answer Glossary

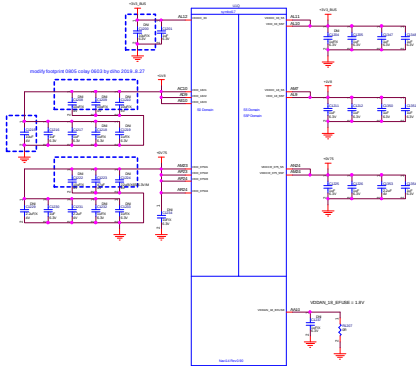
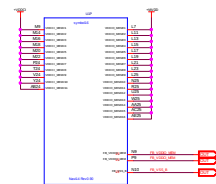
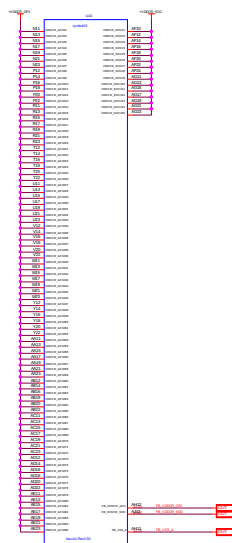
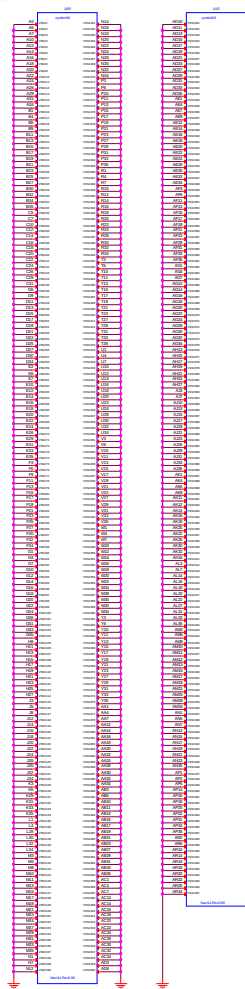
REF ID	REF ID	FUNCTION	PC BUS	DEVICE
0480	0480	VIDEO, GRAPHIC	ICLUSDA	U6013M0317
0422	0484	VIDEO/VIDEO	ICLUSDA	U701ACN122
0463	0484	PC/CGraphics	ICLUSDA	U6013G5881
0488	0490	GB Temperature	DOCVACUSDATA	U9000TAP122A
046C	0496	EXT SENSOR	DOCVACUSDATA	U9001LM9613



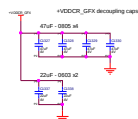
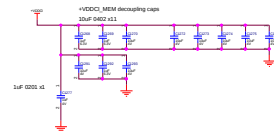
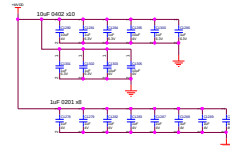
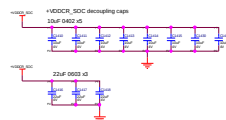
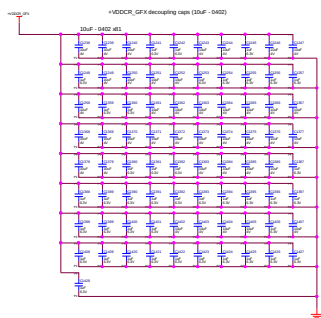
User	Internal Default Value	Definition
BIF		PINTSTR_BIF_GENA_DIS_A 0: PINTSTR_BIF is generated 1: PINTSTR_BIF is not generated
		PINTSTR_BIF_CLK_PM_EN 0: CLKPM2 (power management) capability is enabled 1: CLKPM2 (power management) capability is disabled
		PINTSTR_BIF_CLK_TX_SWING 0: Tx swing is enabled 1: Tx swing is disabled (not swing)
		PINTSTR_BIF_VGA_DIS 0: VGA controller is enabled 1: VGA controller is disabled (not VGA controller)
		PINTSTR_AUD_PORT_CONN2_I 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_O 0: Number of audio-capable display outputs
DOE		PINTSTR_AUD_PORT_CONN2_I 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_O 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_I 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_O 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_I 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_O 0: Number of audio-capable display outputs
Platform		PINTSTR_AUD_PORT_CONN2_I 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_O 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_I 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_O 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_I 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_O 0: Number of audio-capable display outputs
SAU		PINTSTR_AUD_PORT_CONN2_I 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_O 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_I 0: Number of audio-capable display outputs
		PINTSTR_AUD_PORT_CONN2_O 0: Number of audio-capable display outputs
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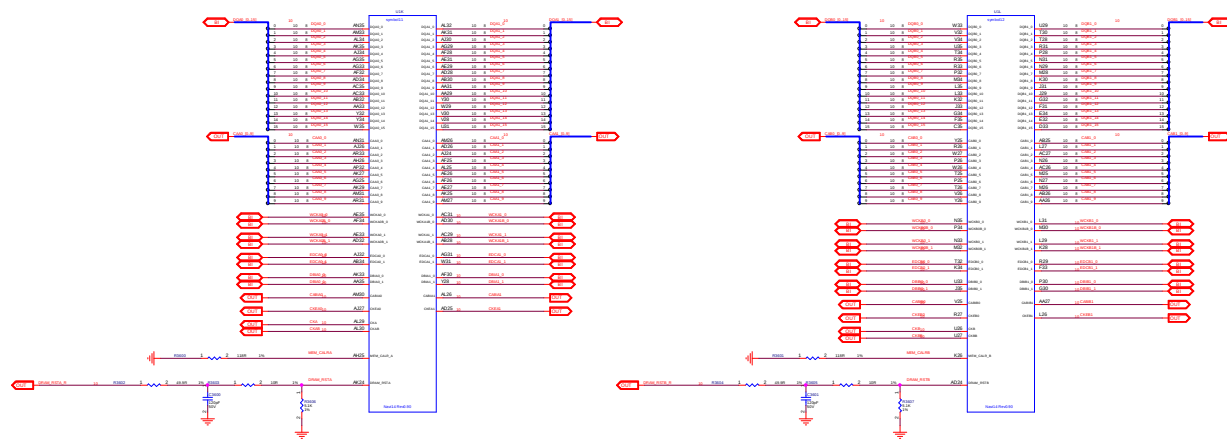


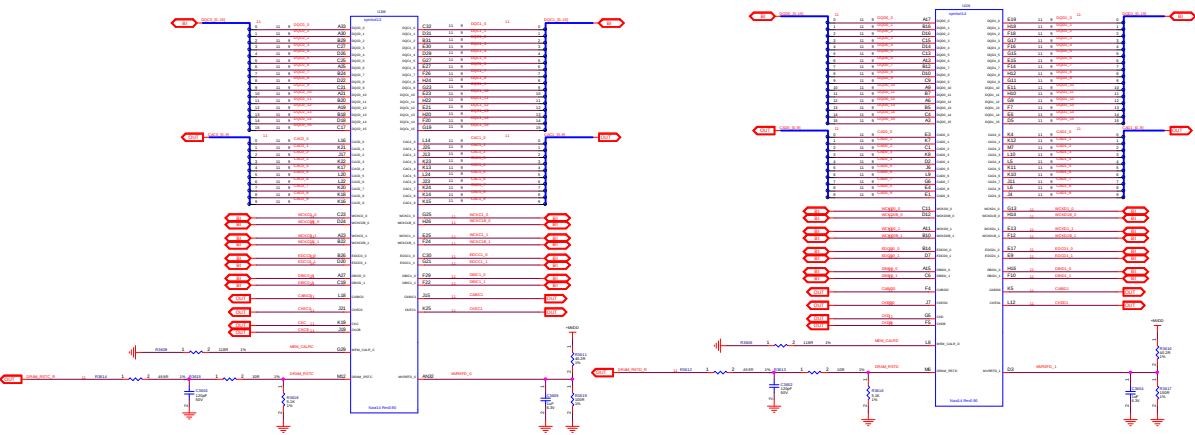
(8) NVIDIA POWER AND GND

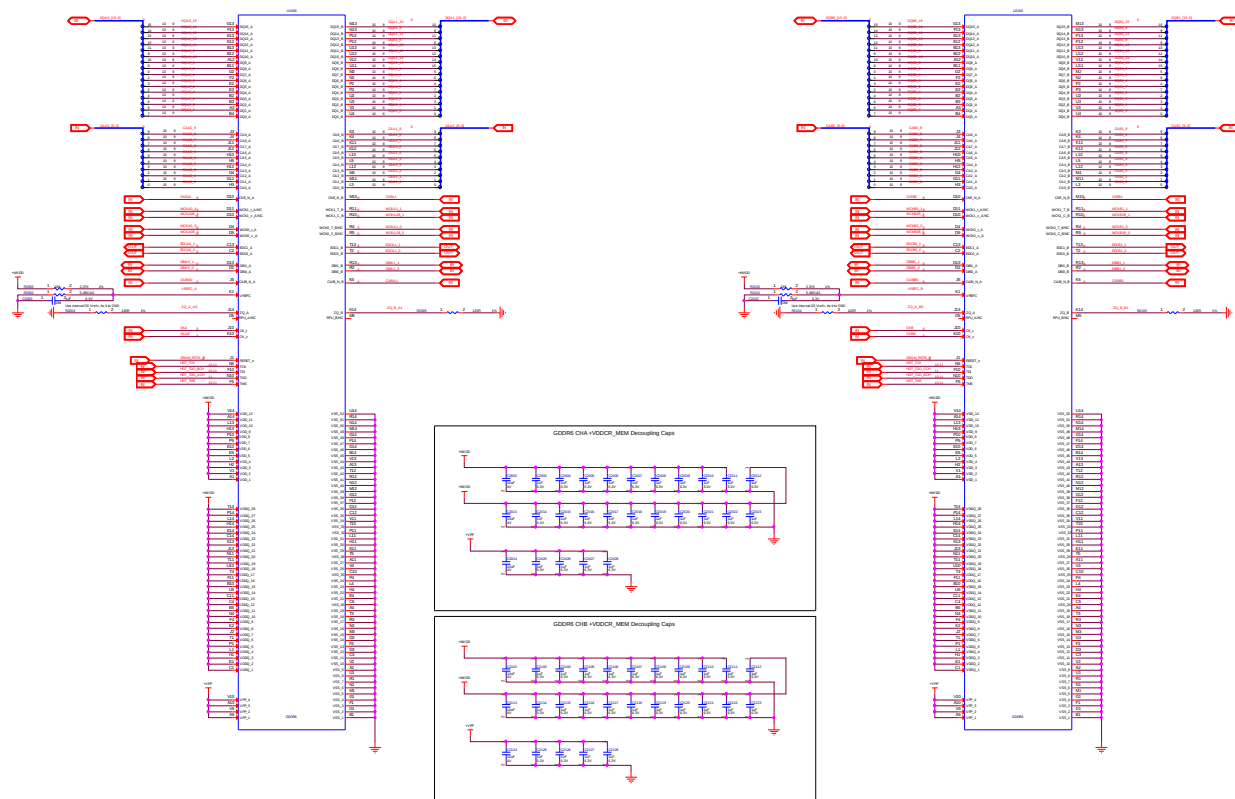


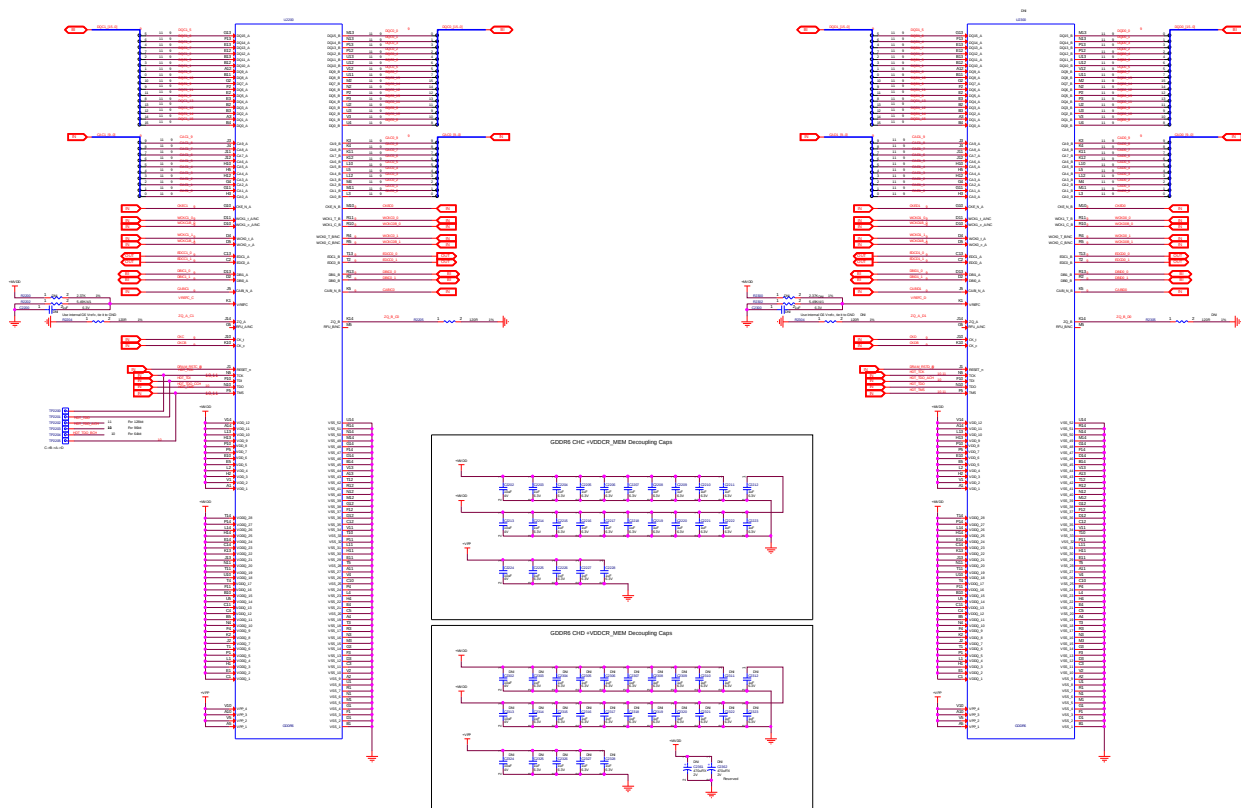
СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

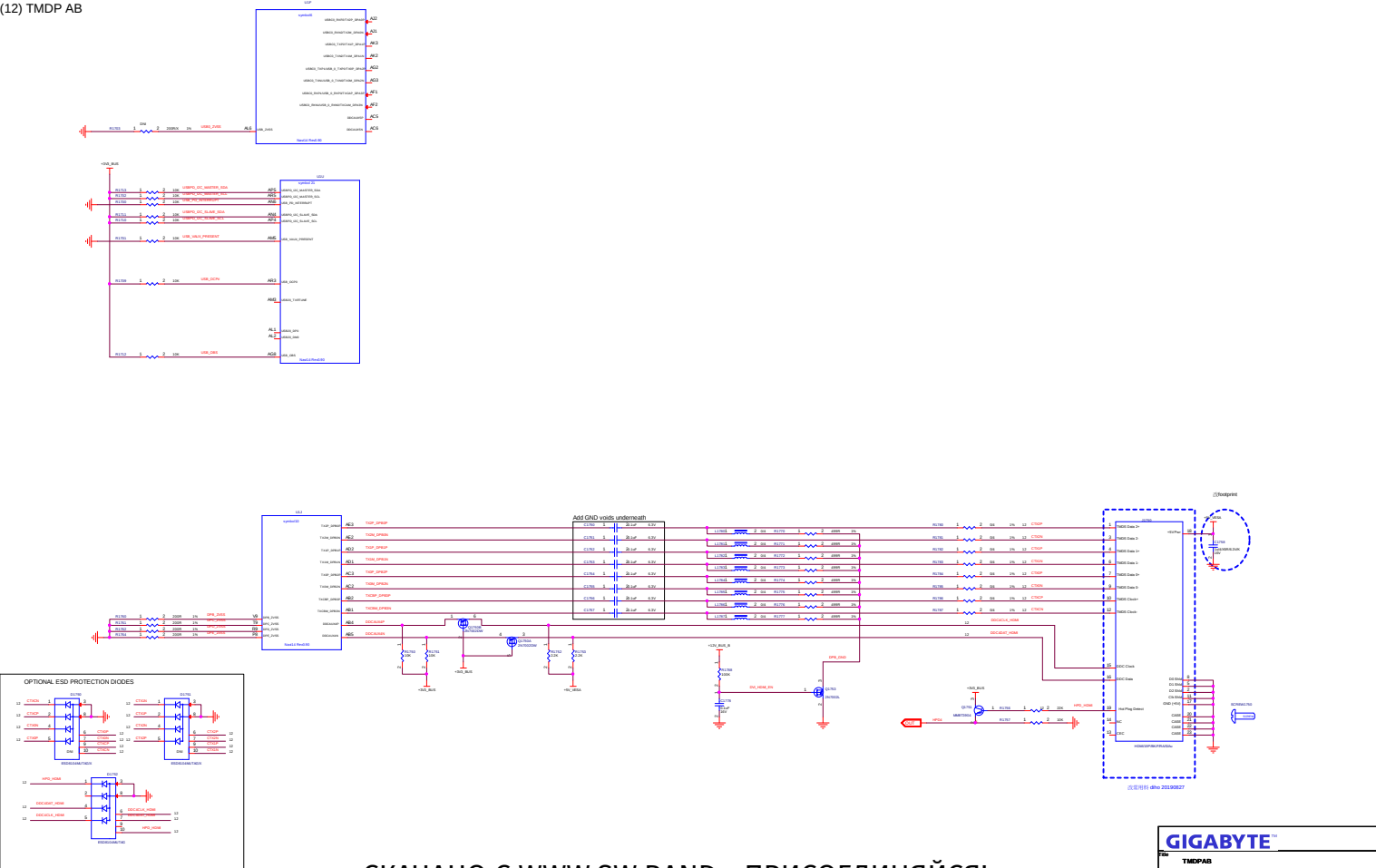


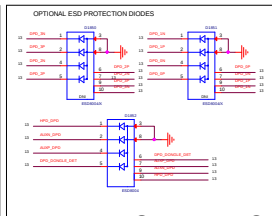
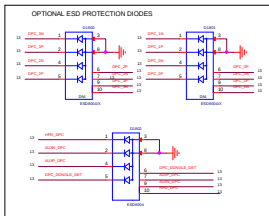
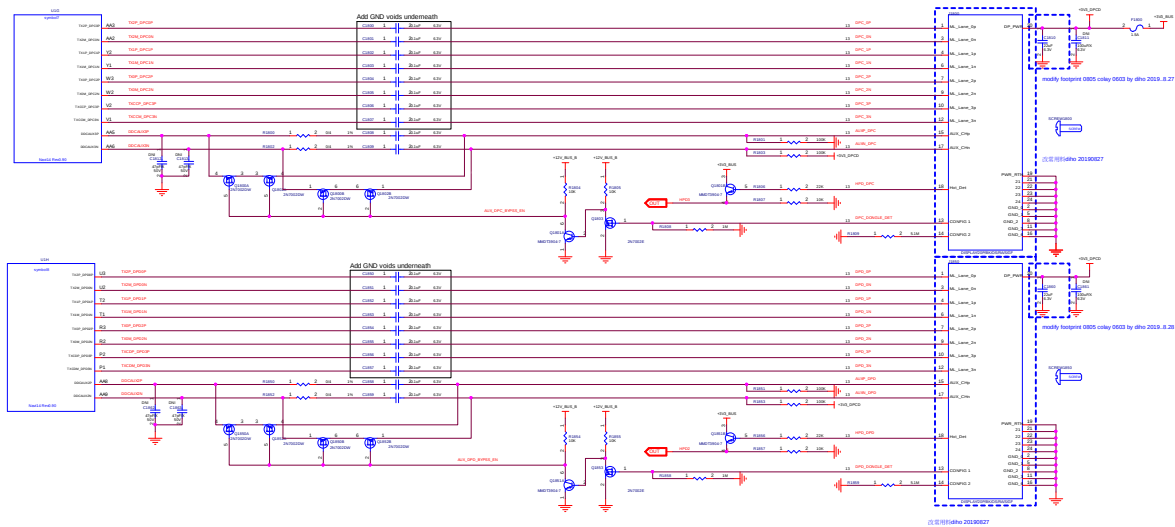




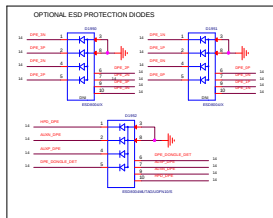
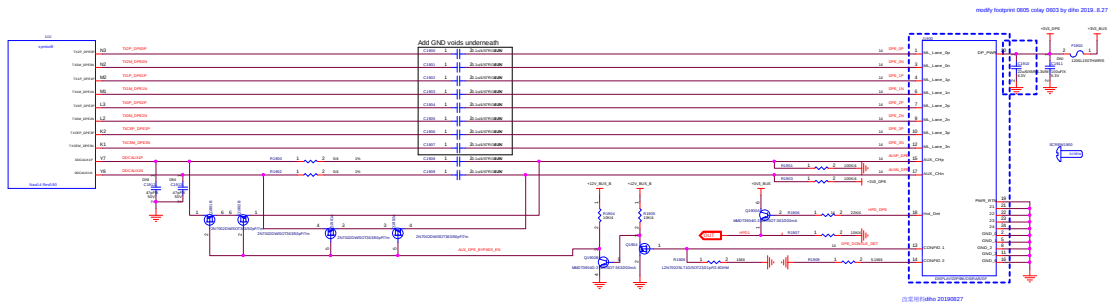








СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

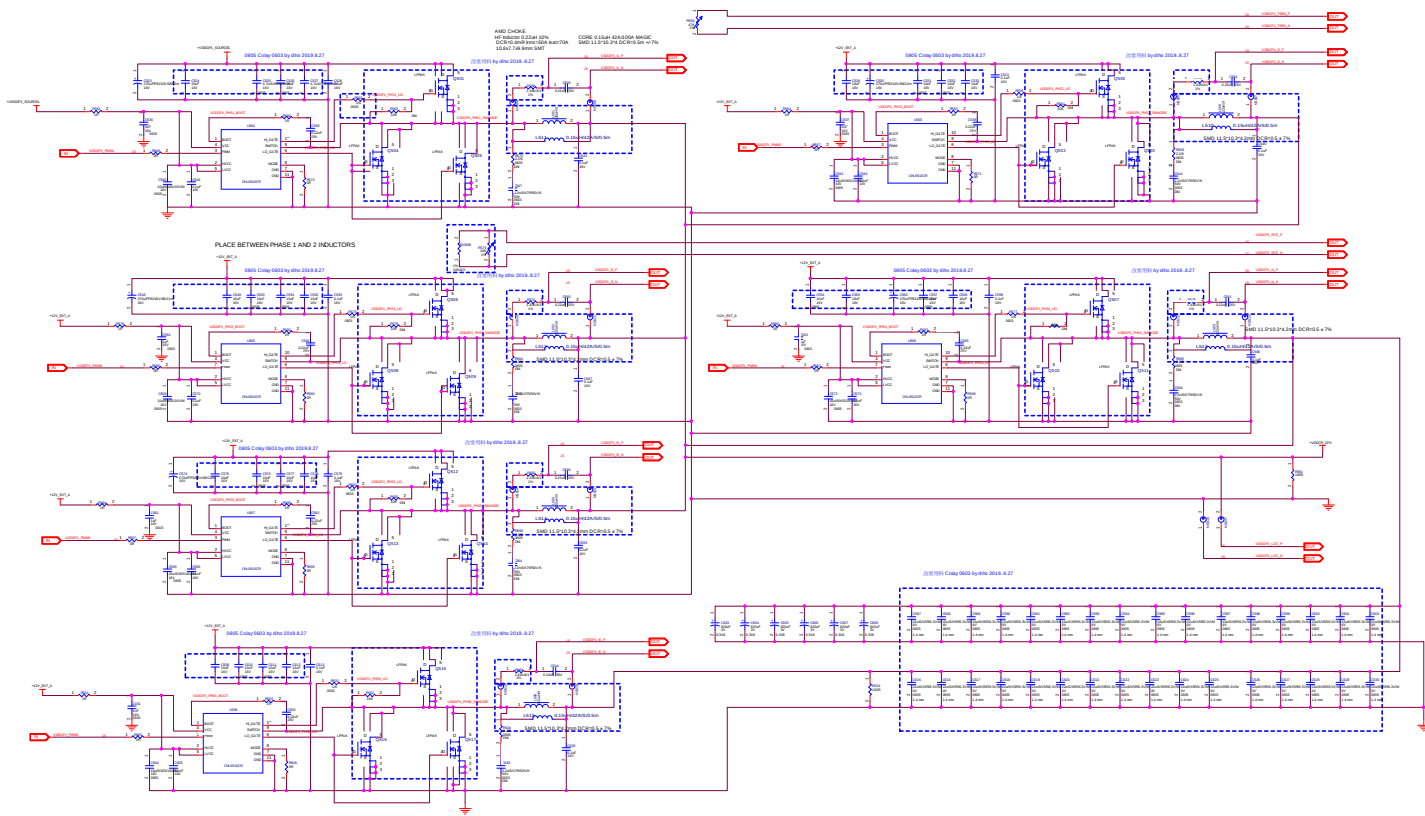


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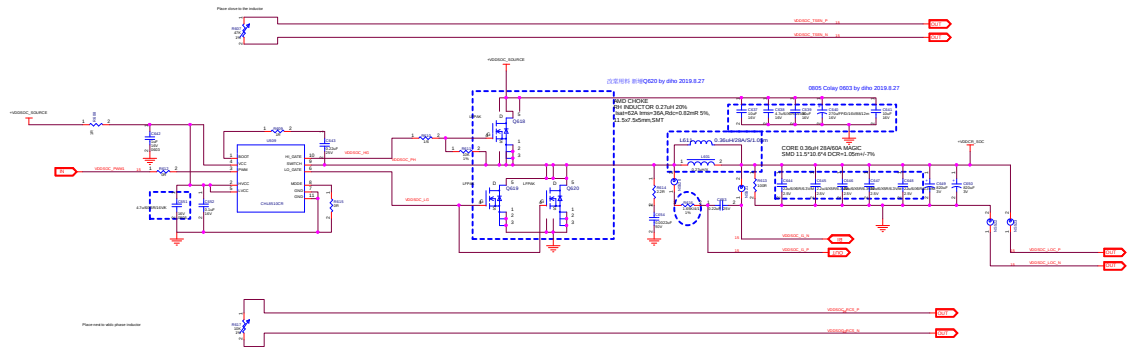


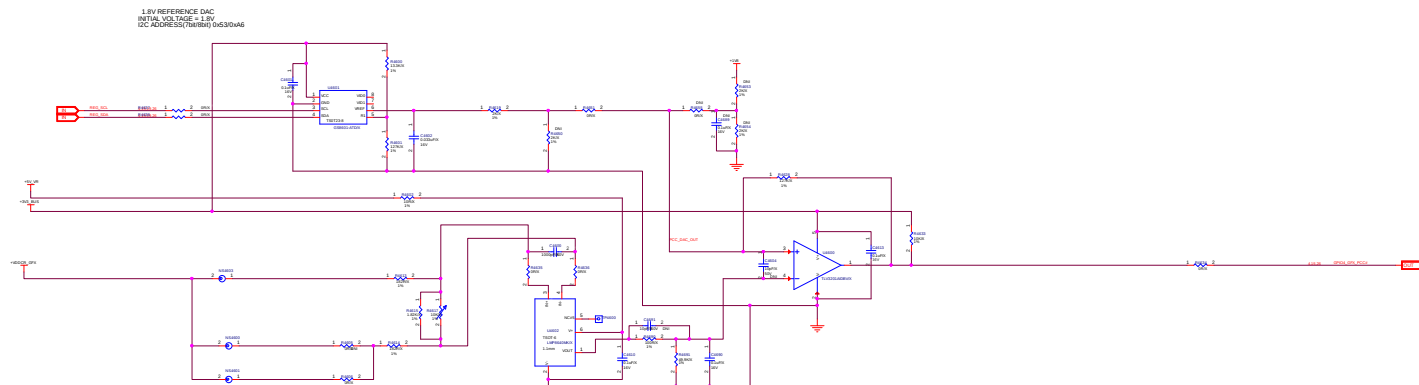
GIGABYTE

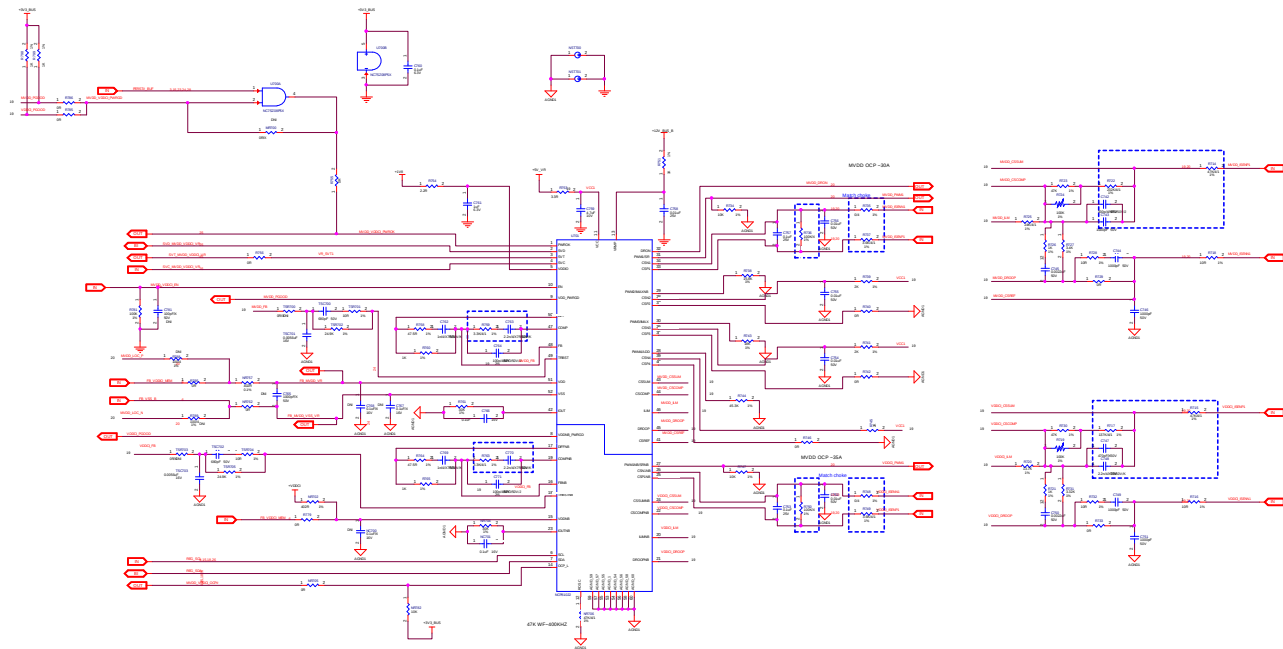
PLACE NEXT TO PHASE 3 HALF BRIDGE

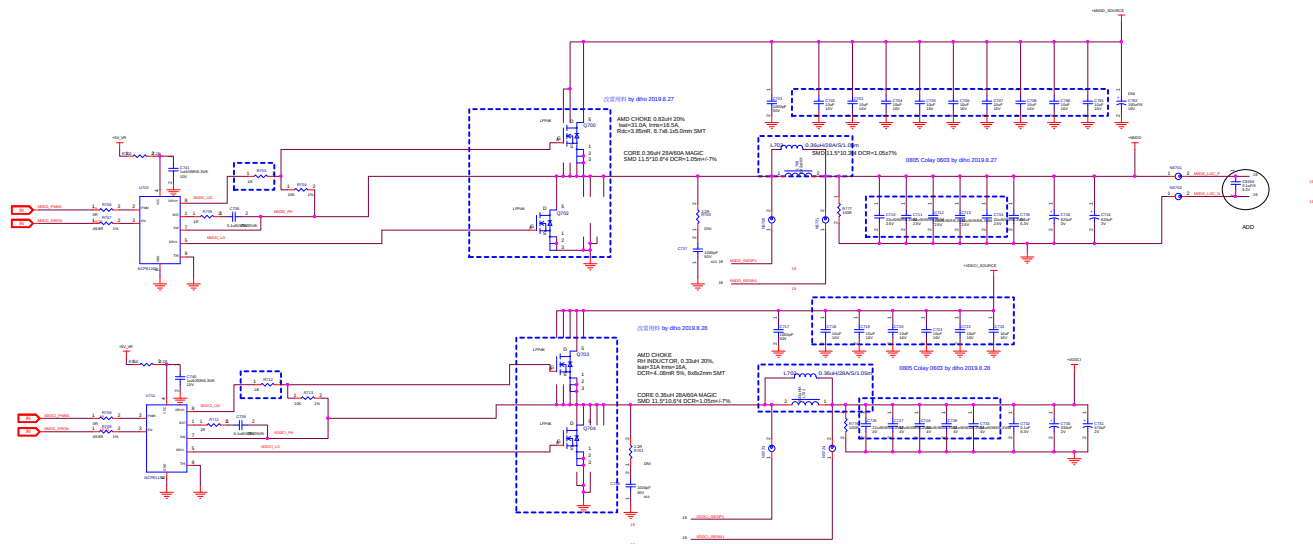


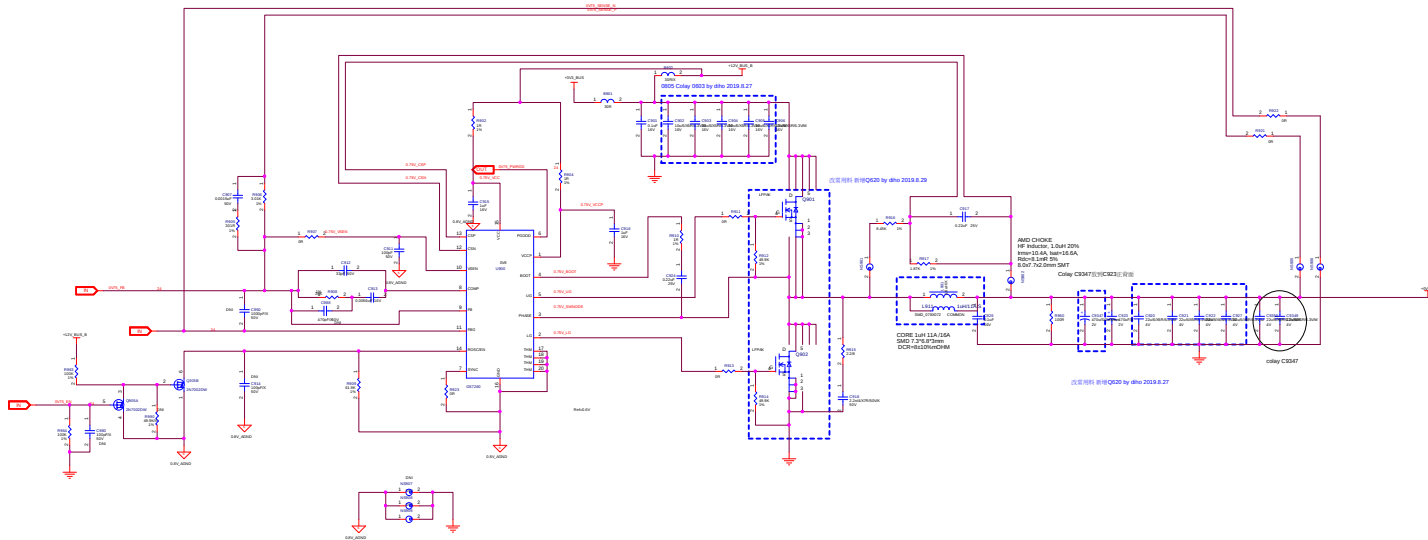
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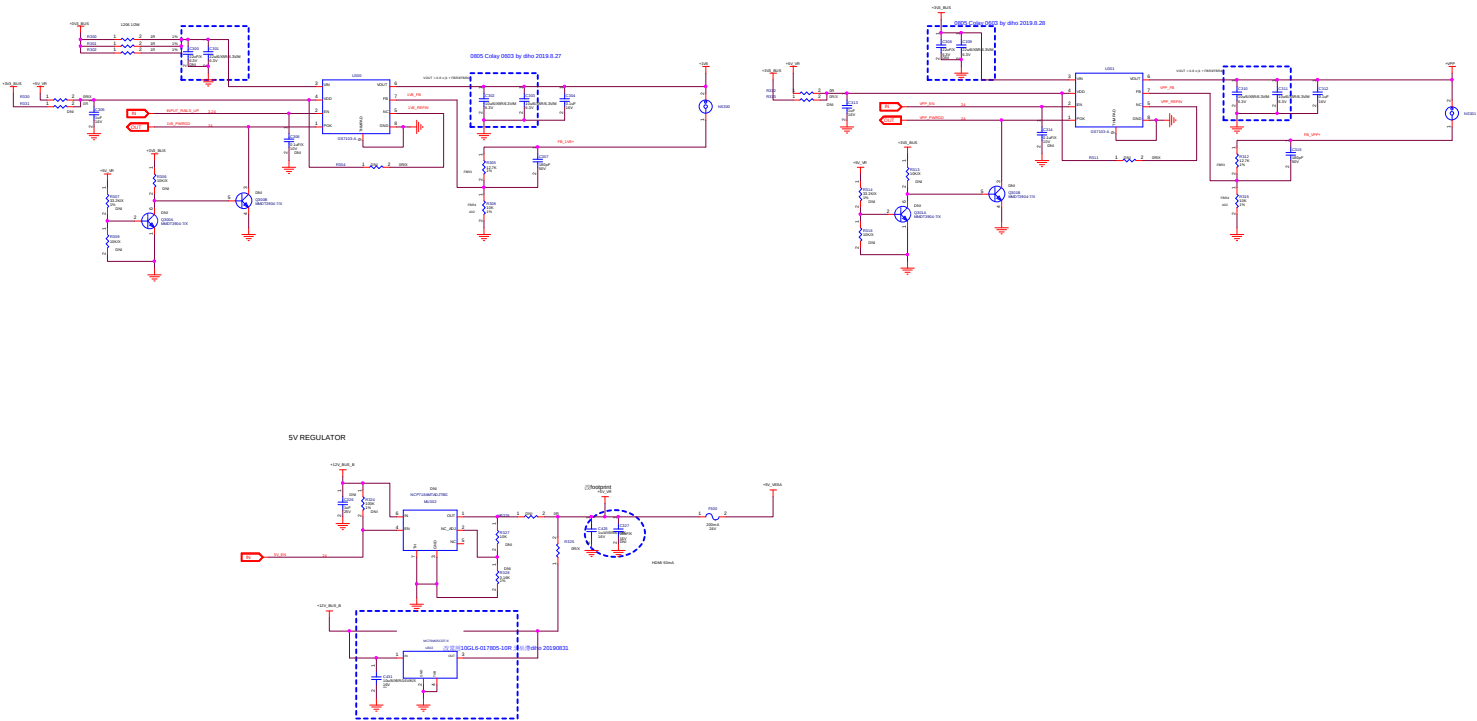


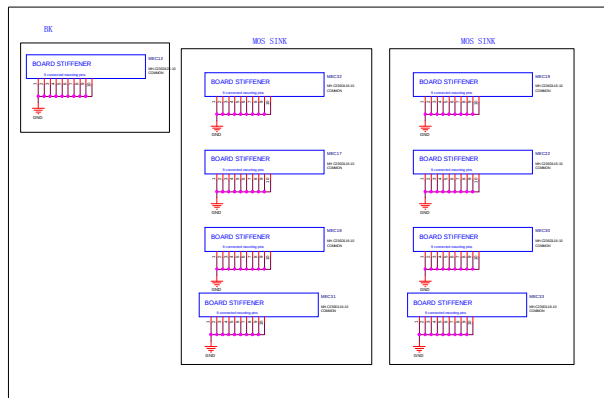
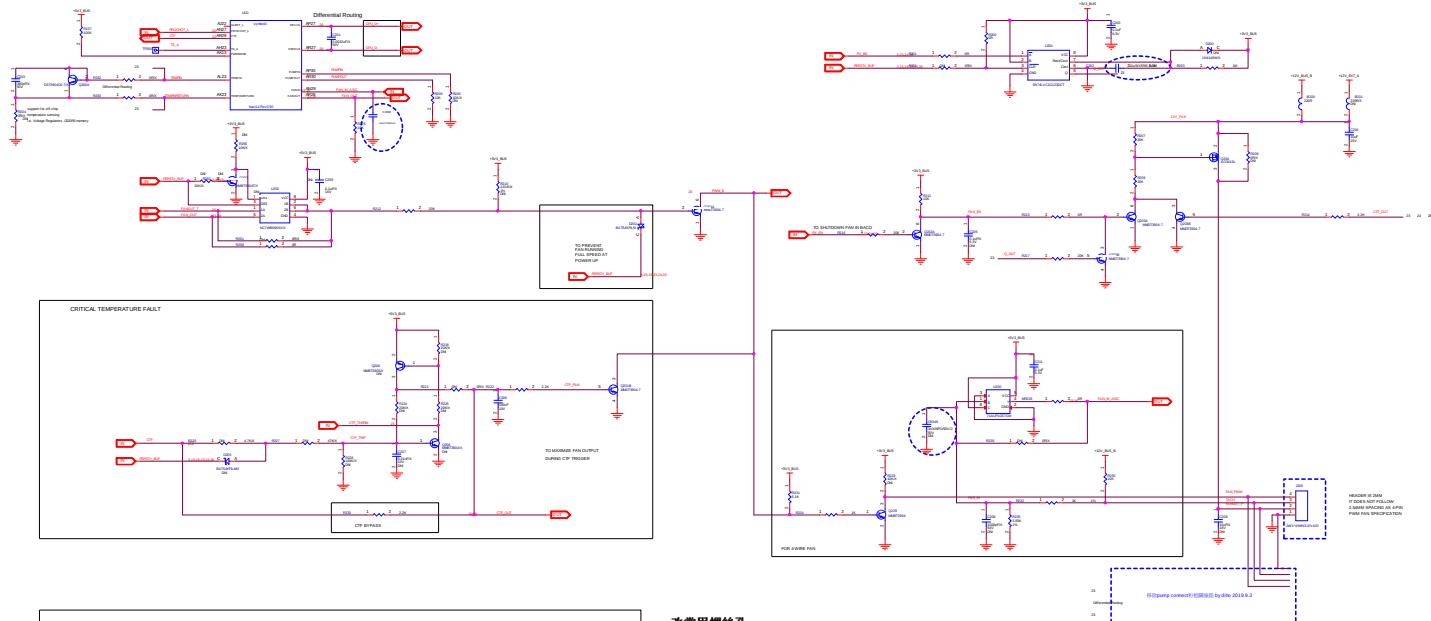




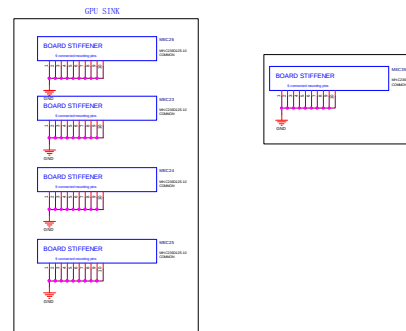
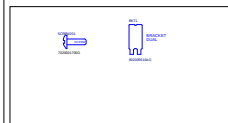


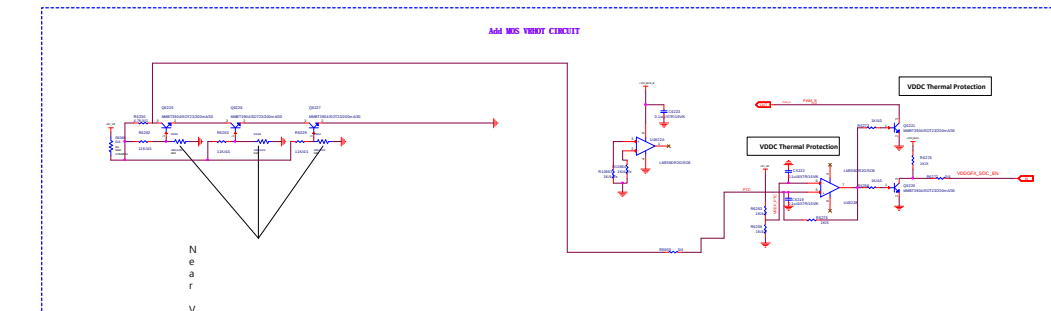
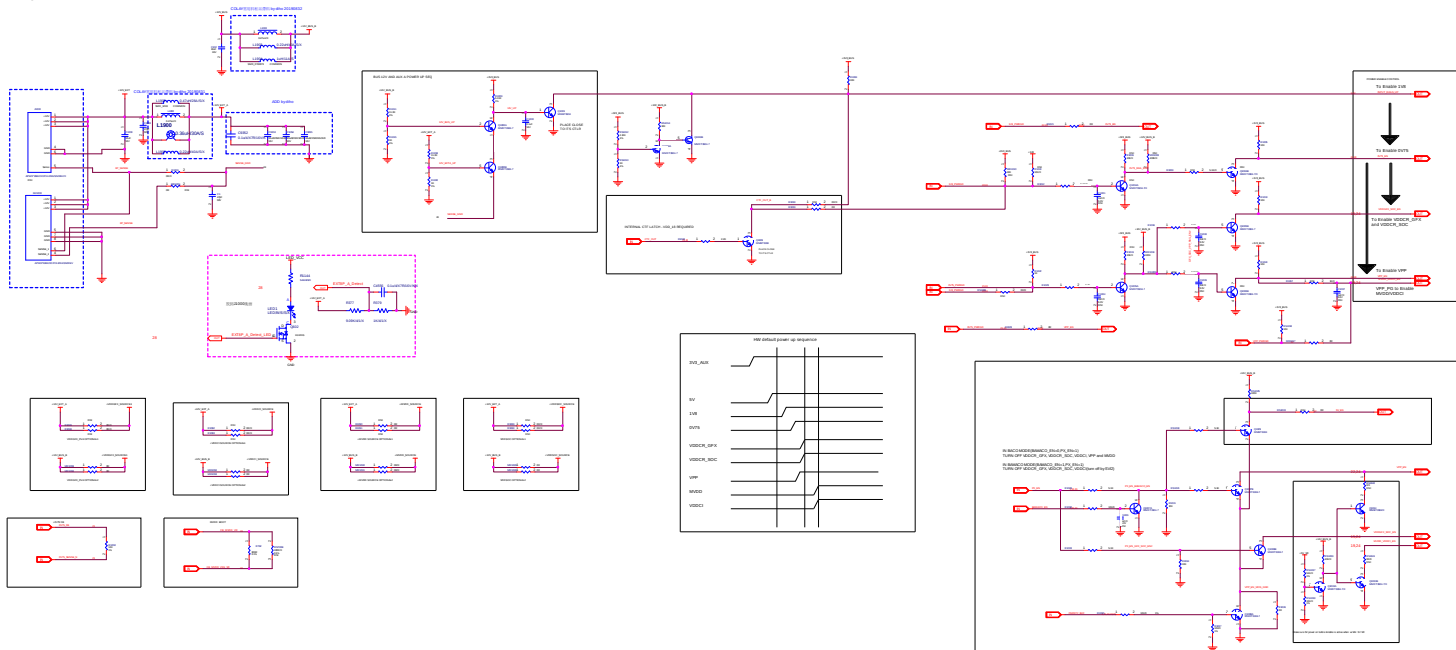






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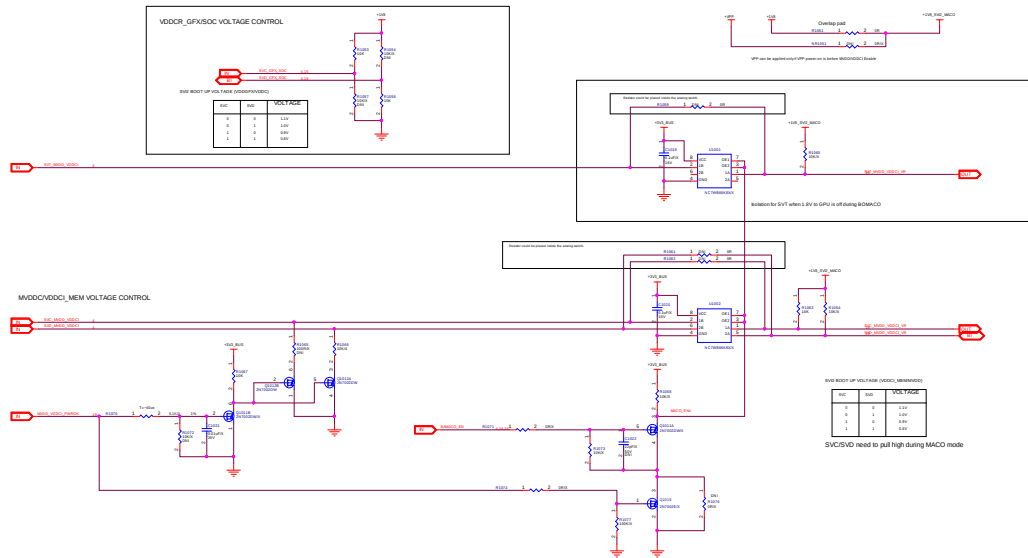
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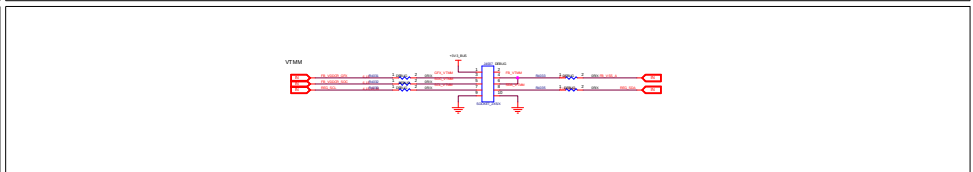
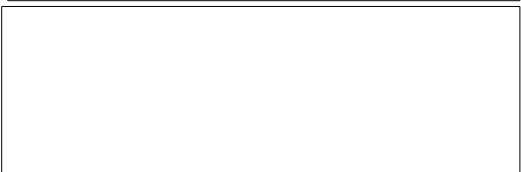
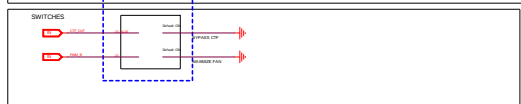
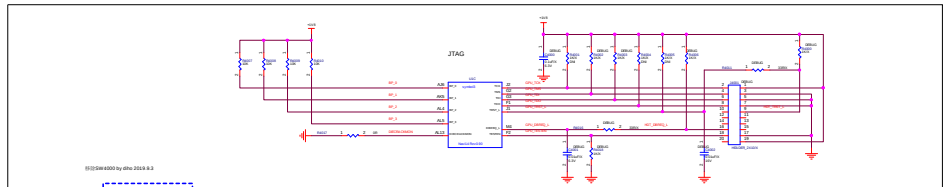
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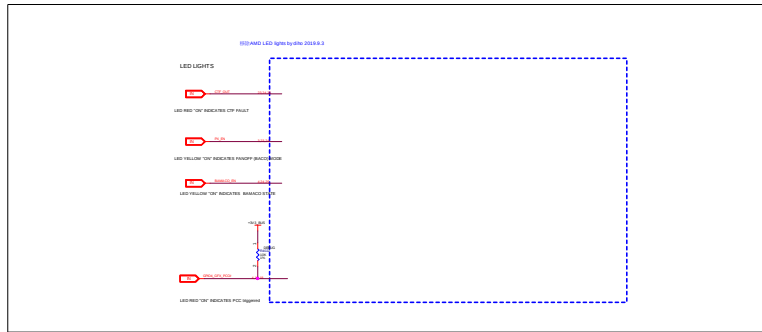
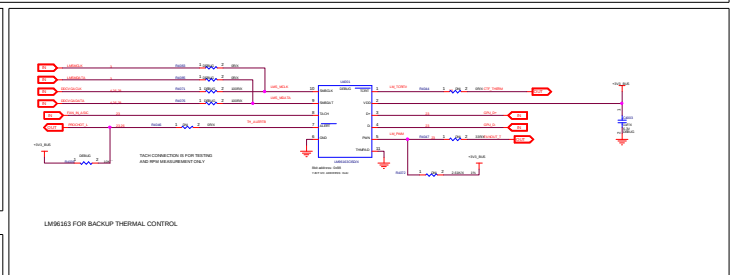
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(29) DEBUG CIRCUIT



DEVICE TWO-WIRE ADDRESS	AS PIN CONNECTION
180C0000	Ground
180C0001	VCC
180C0002	SDA
180C0003	SCL



GIGABYTE™	
File	
REVISION HISTORY	
Doc Number	Rev
GV-R55XTOC-4GD	1.0
Date: Tuesday, December 10, 2019	Sheet 27 of 28



GIGABYTE TM		
Title MCU_LED		
Size Custom	Document Number GV-R55XTOC-4GD	Rev 1.0