

PG165-A00

TU116 6GB GDDR5, 192b, X32
DVI-D + DP + HDMI

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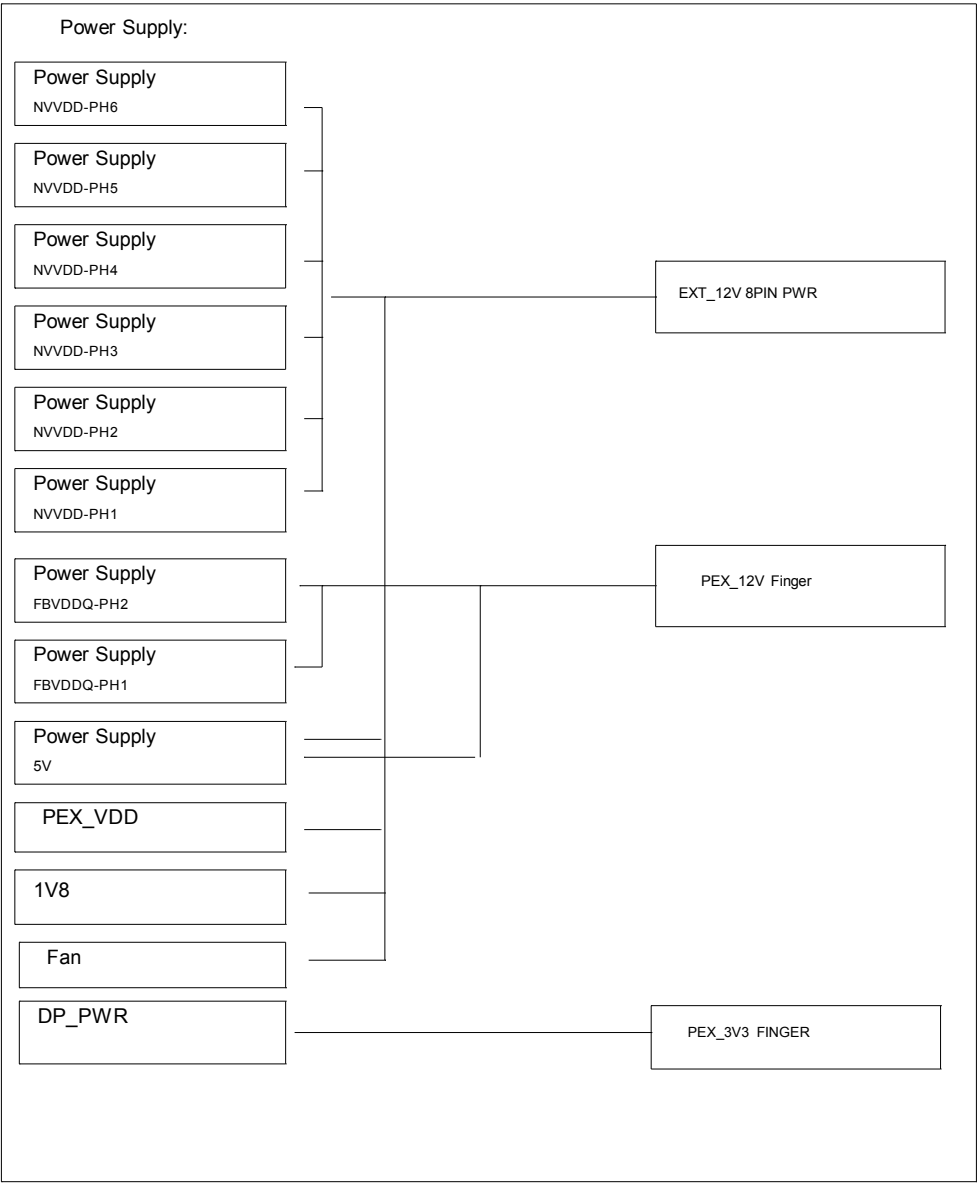
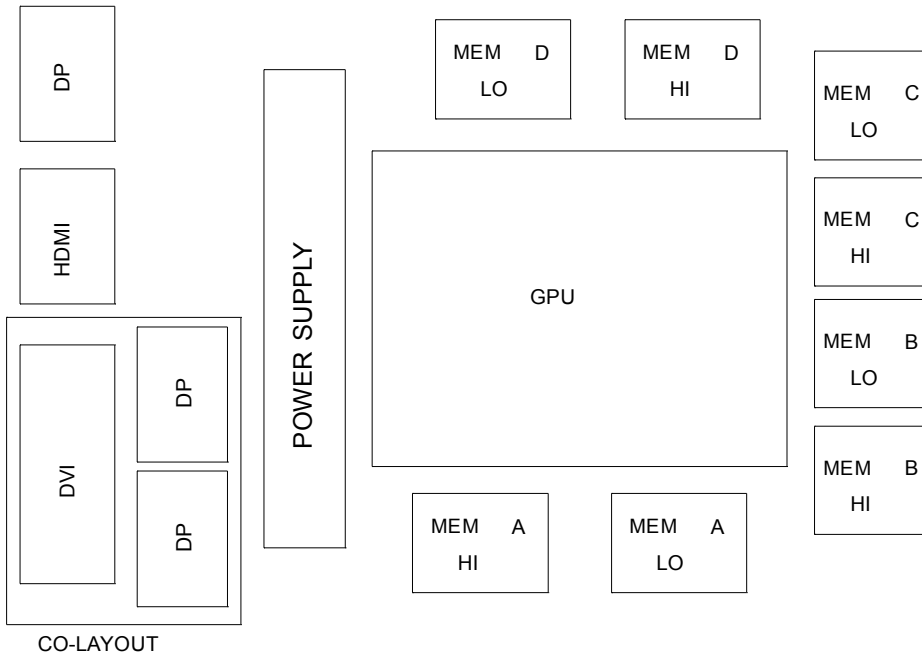
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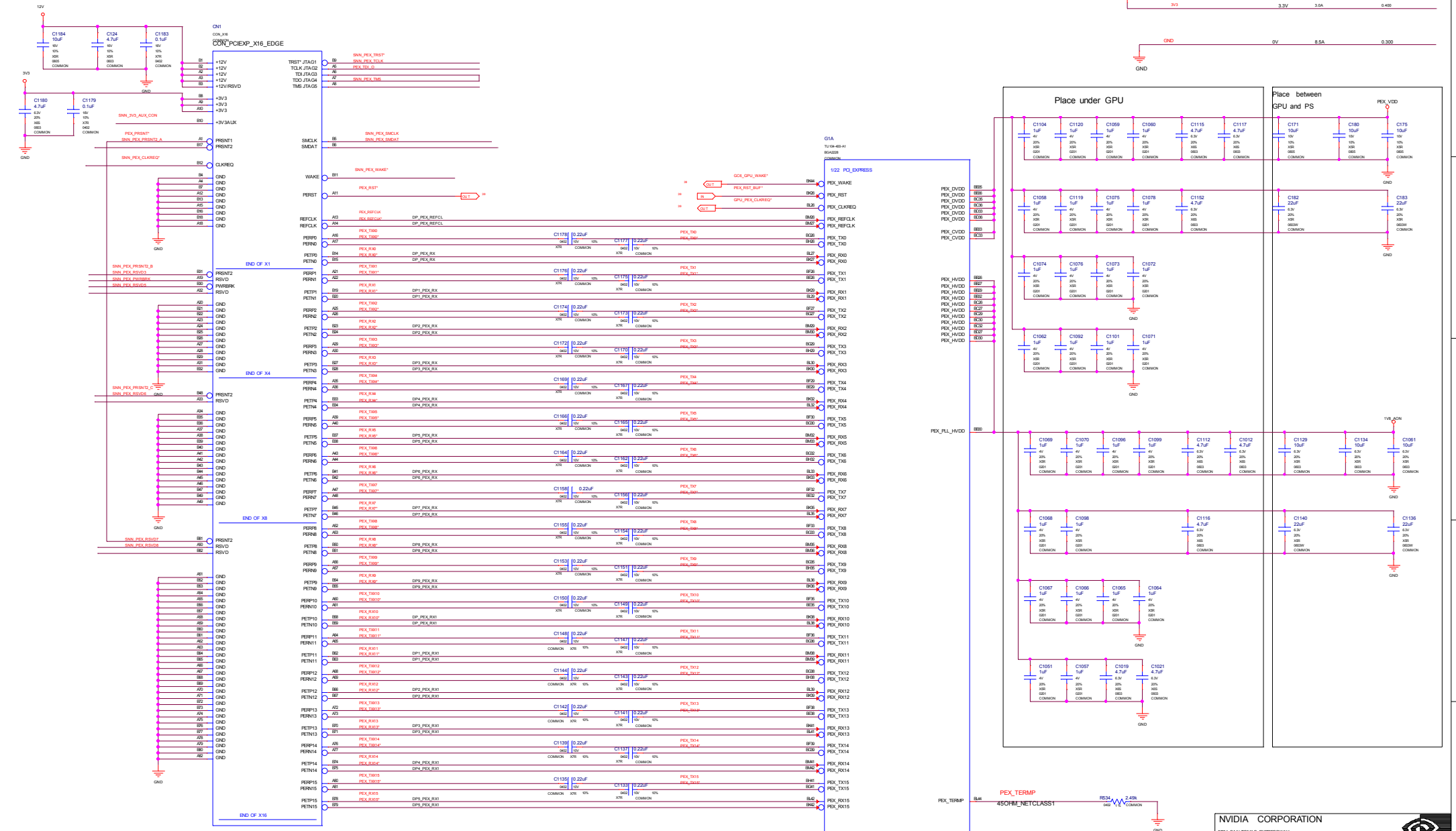
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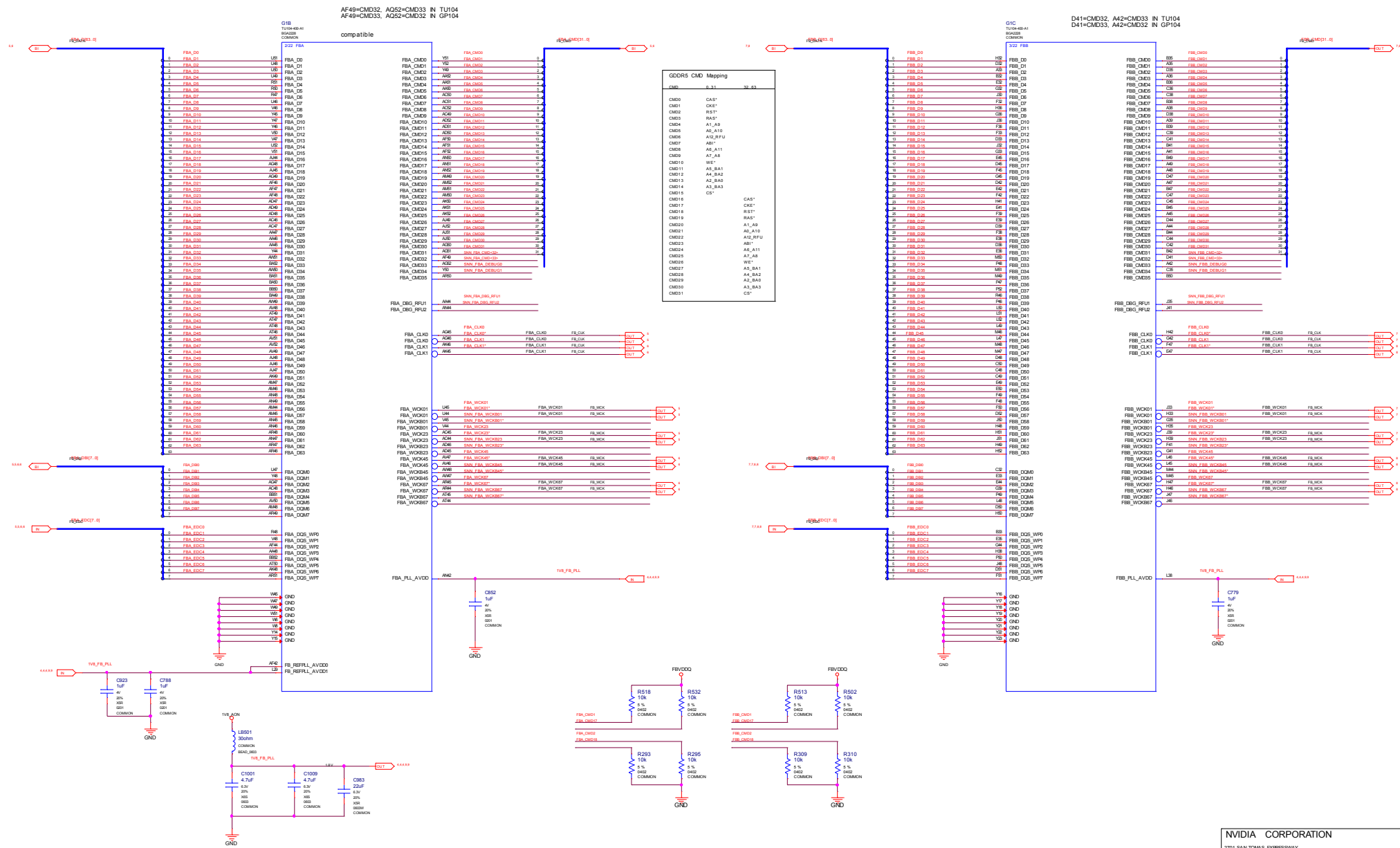
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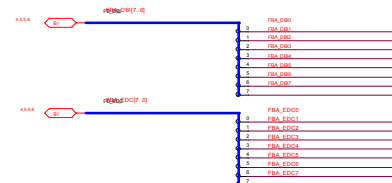
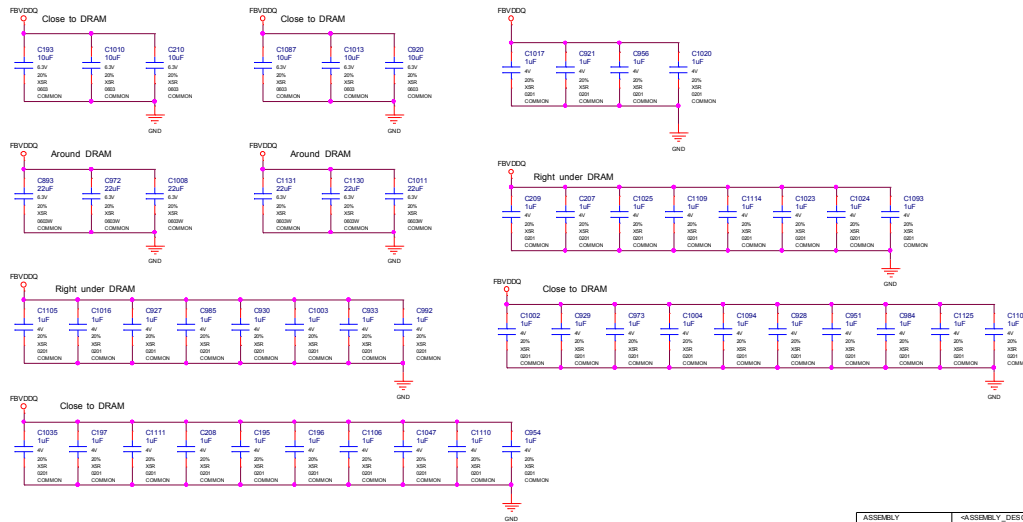
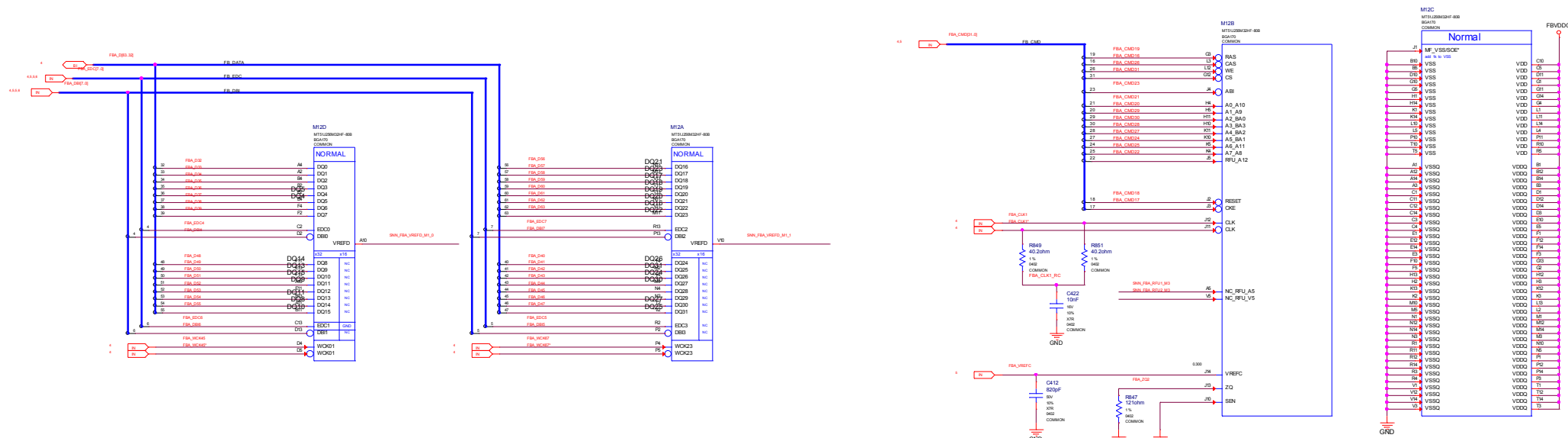
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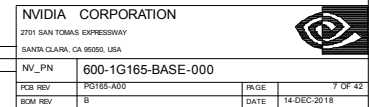
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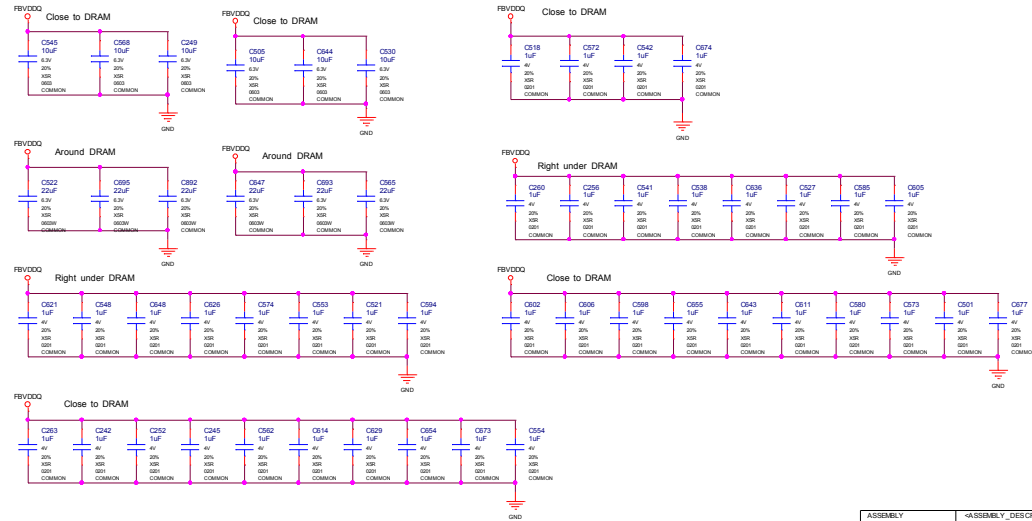
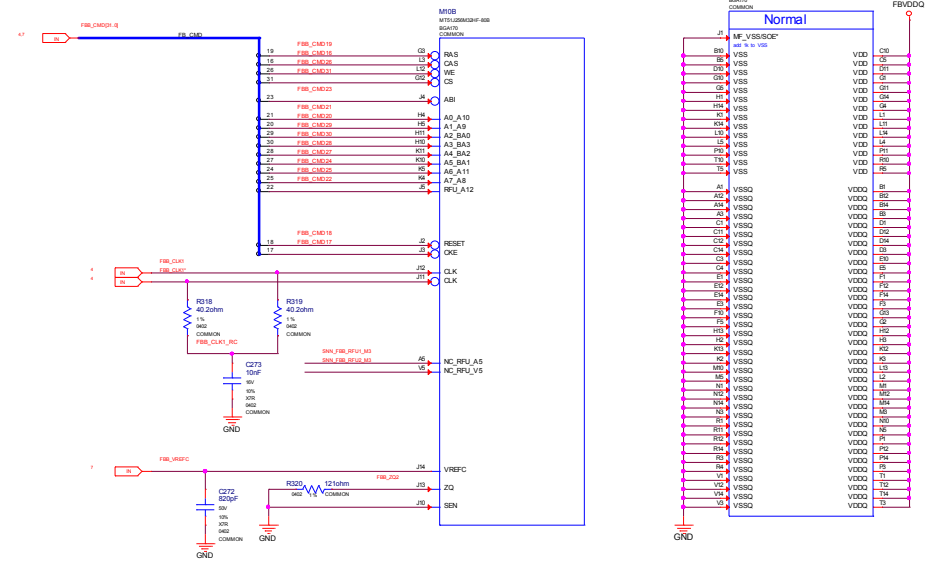
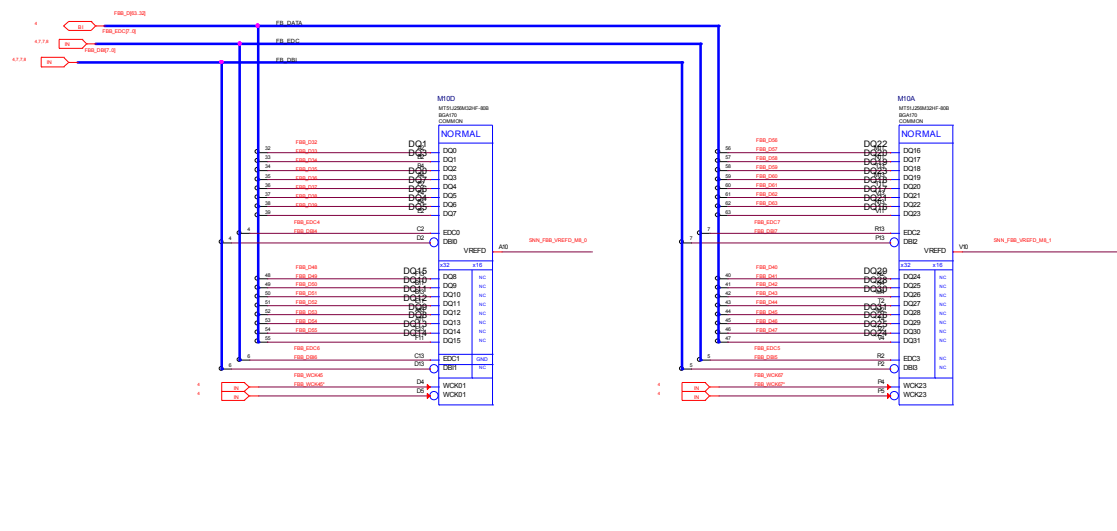
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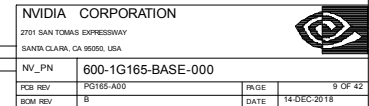
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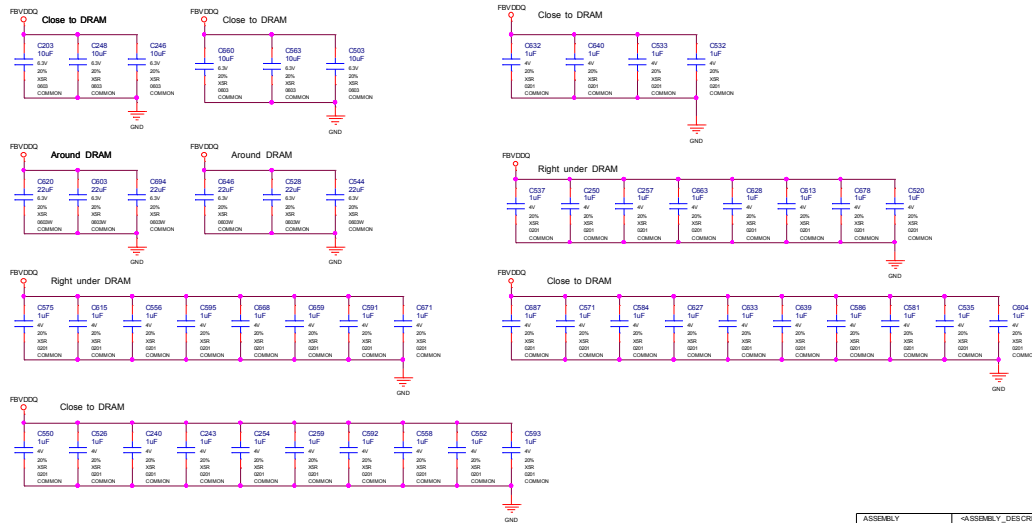
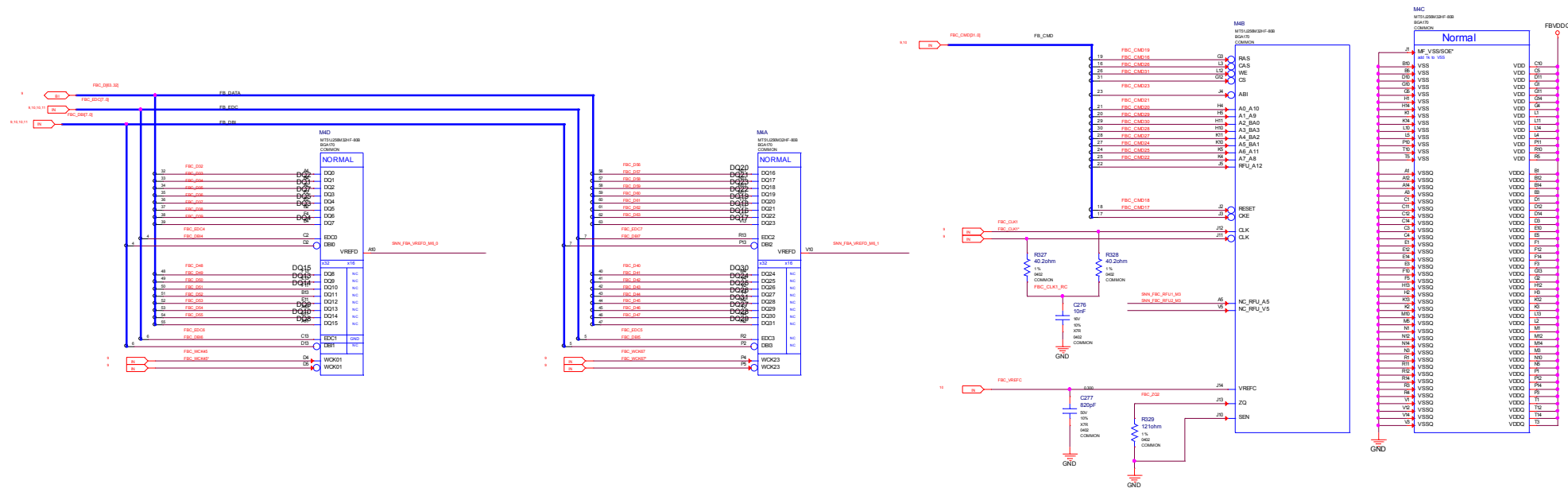


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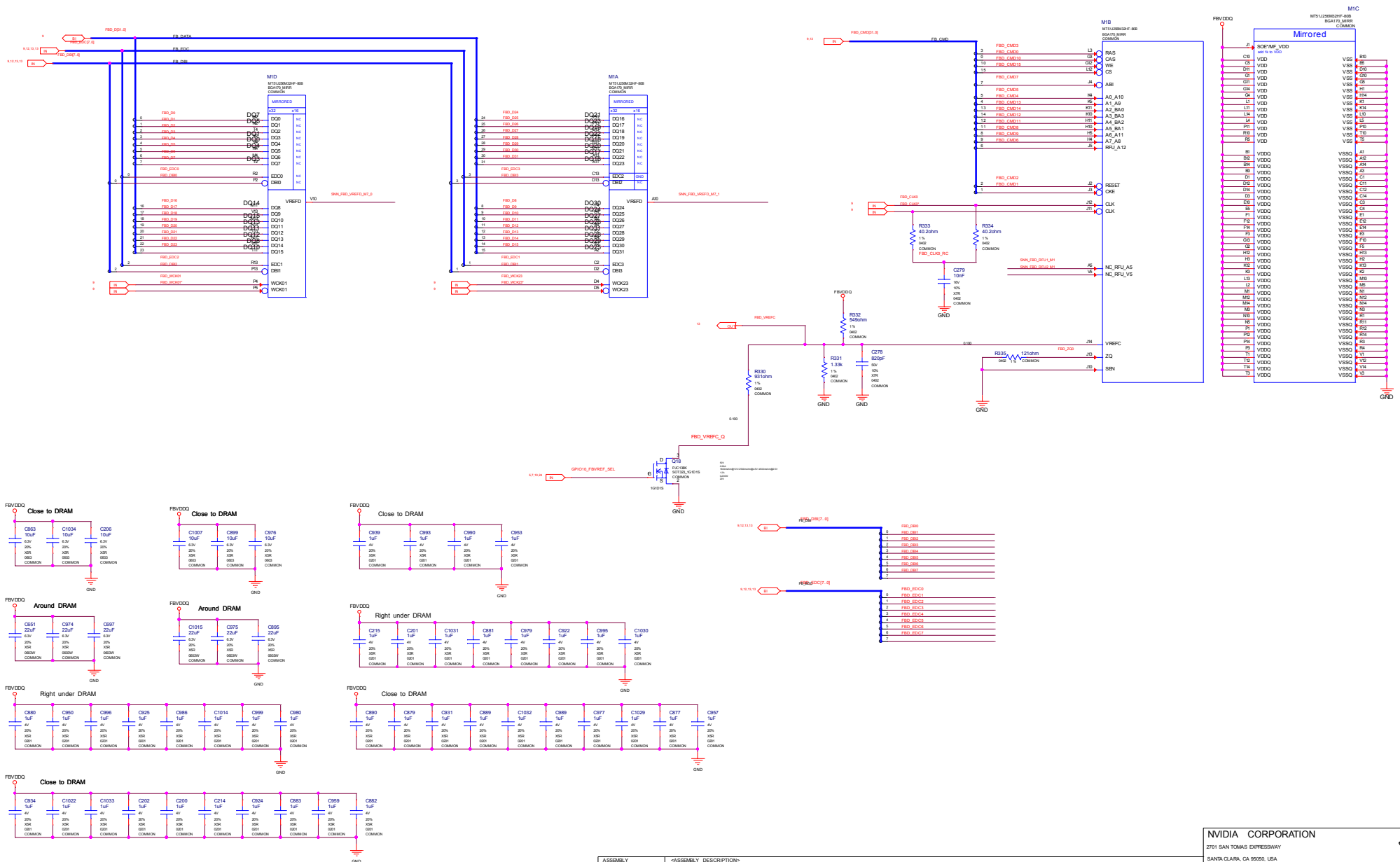
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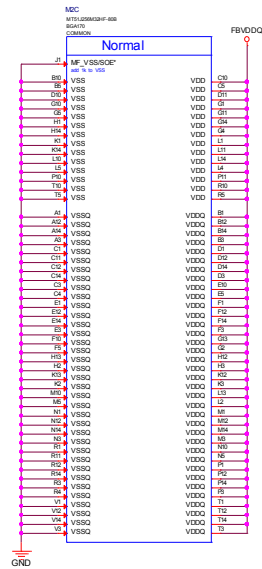
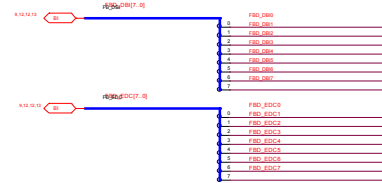
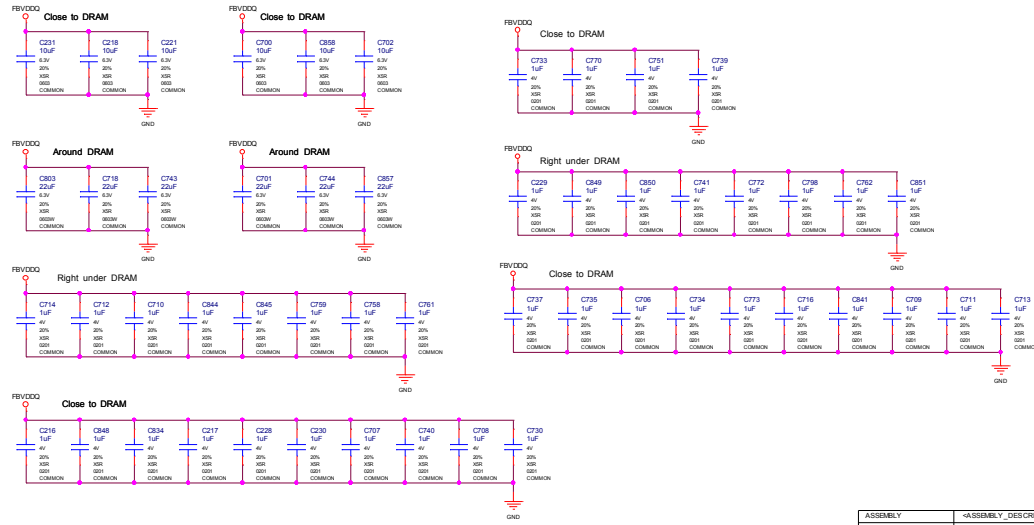
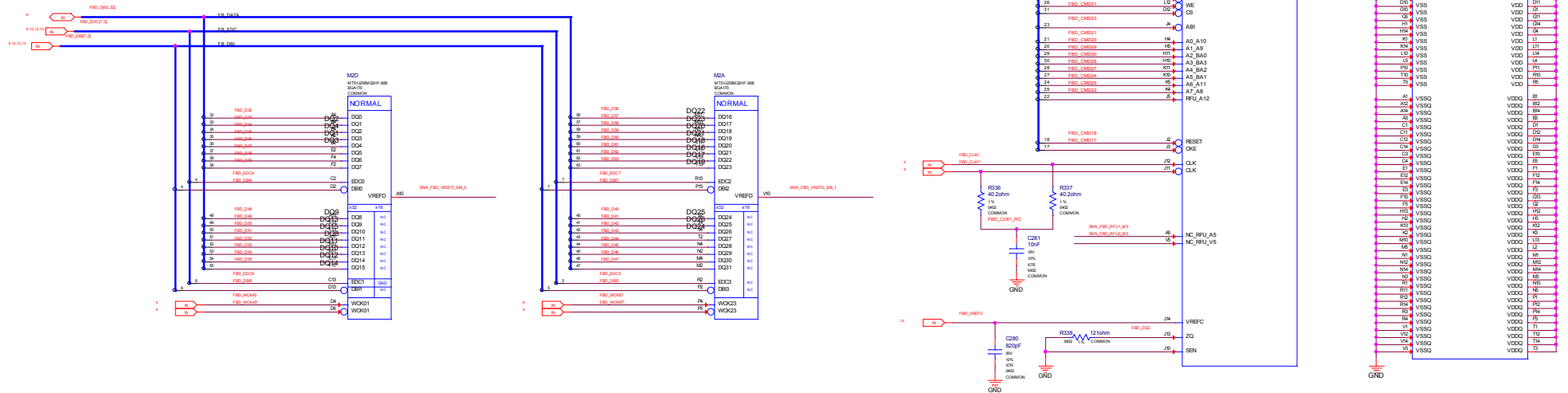
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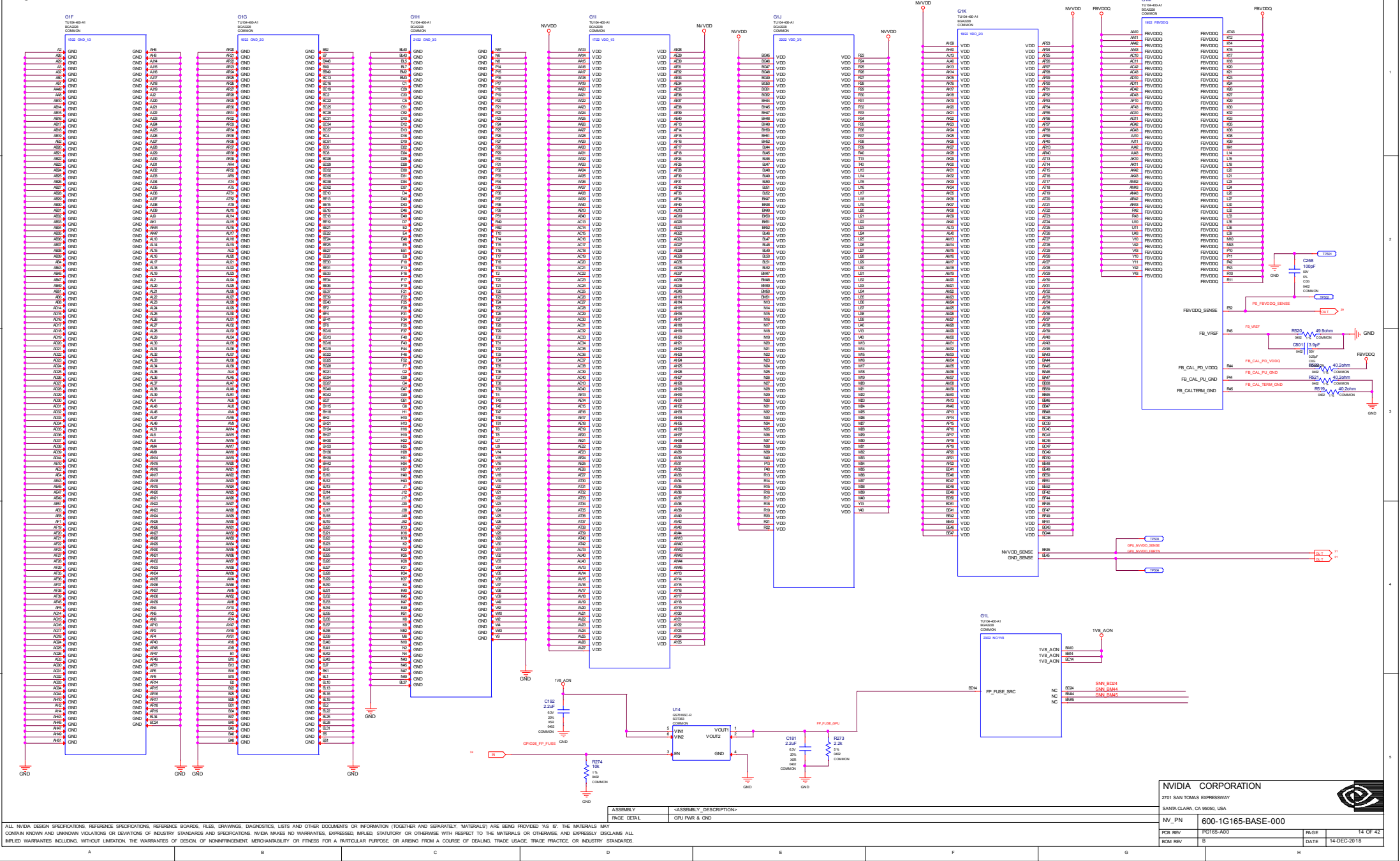
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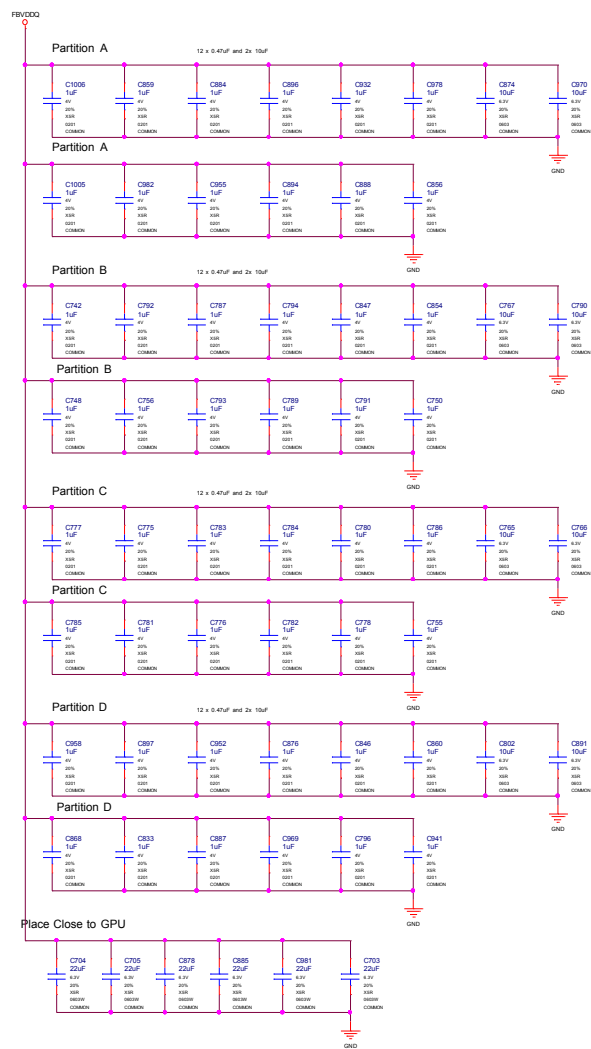


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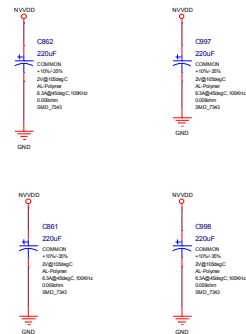
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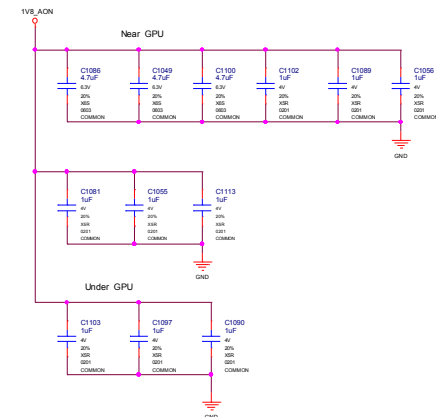
FBVDDQ



NVDD



1V8_AON



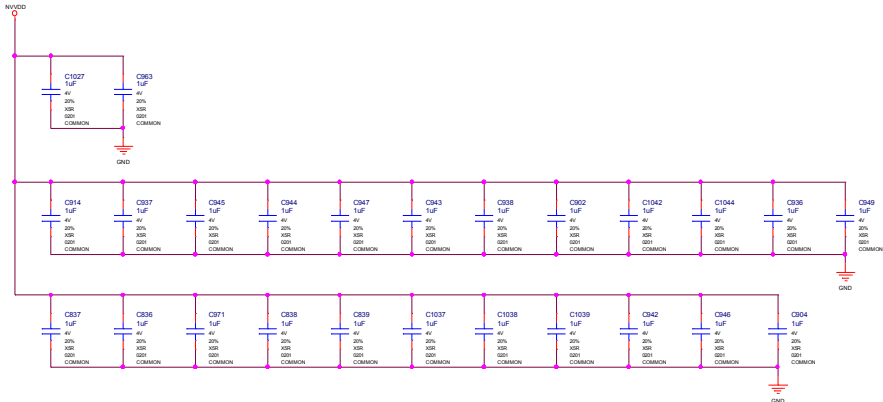
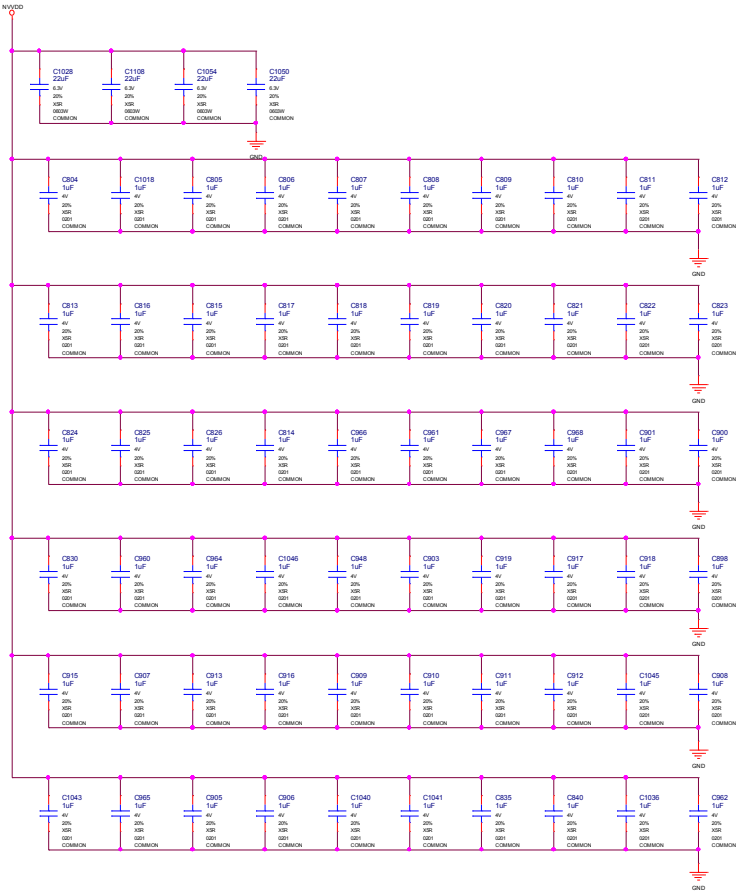
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FRIDGE DETAIL	GPU DECOUPLING 1

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NVDD

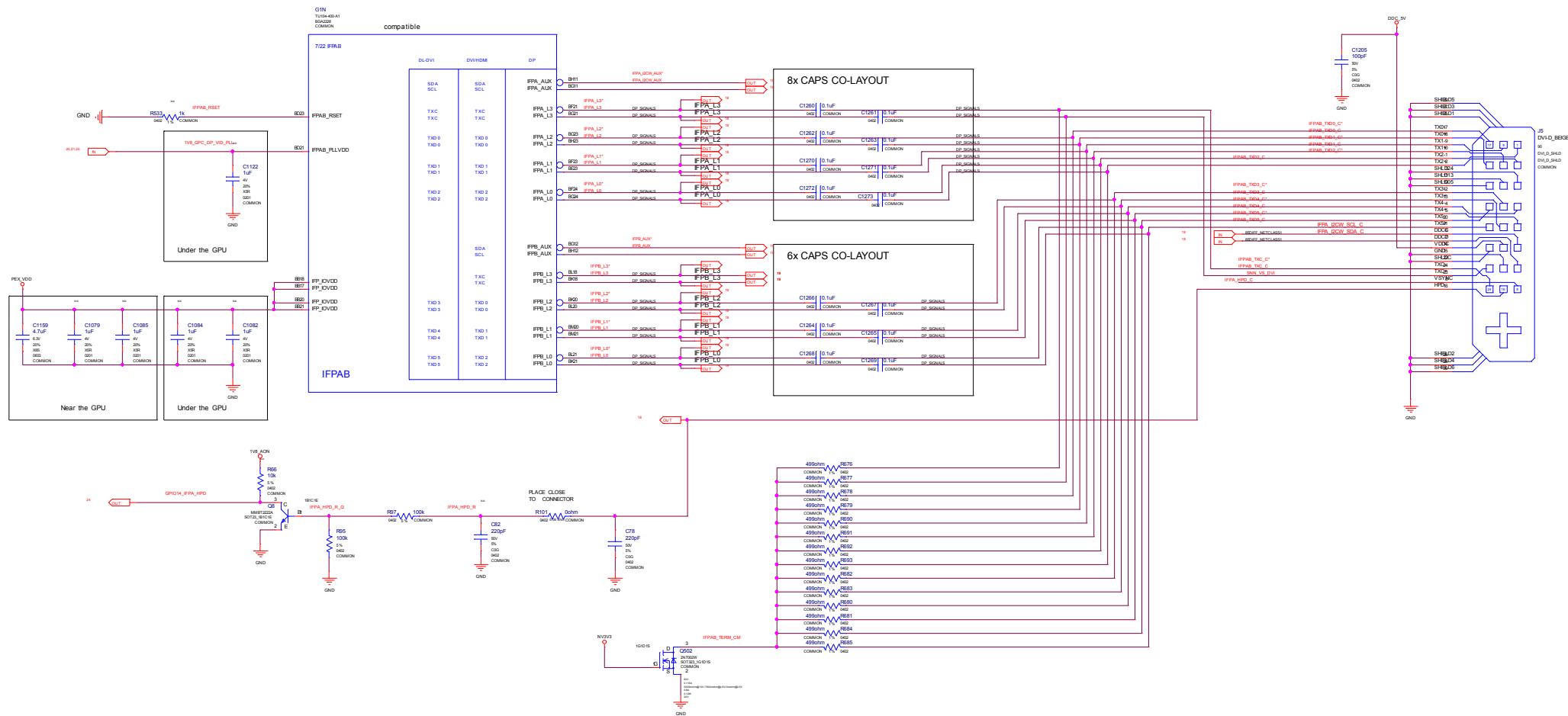


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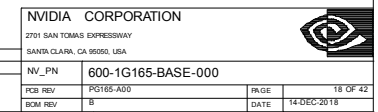
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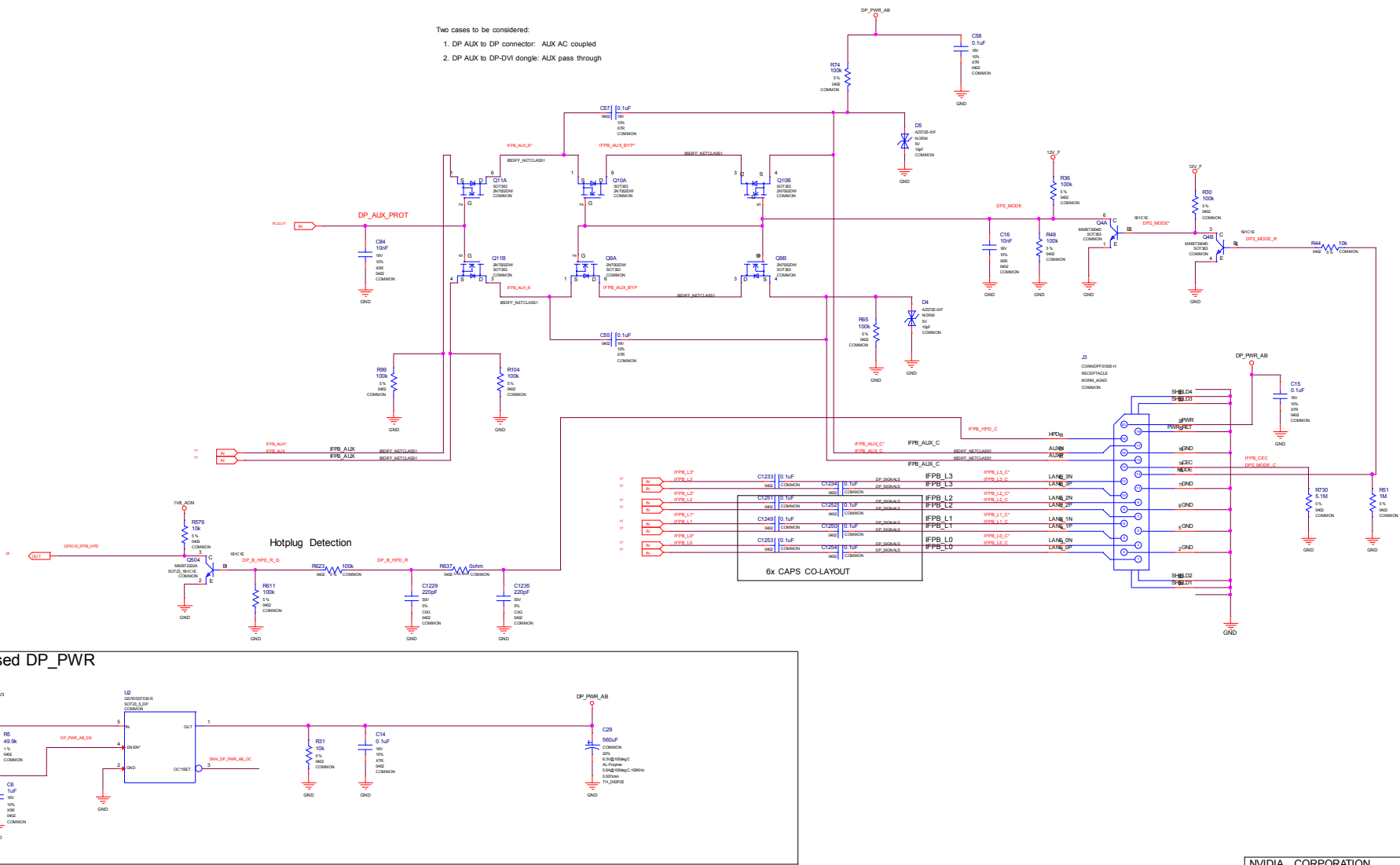
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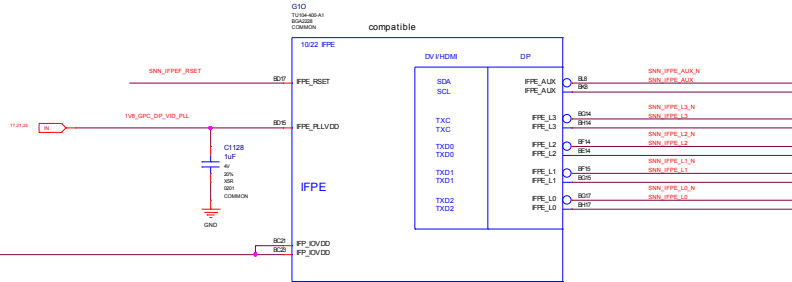
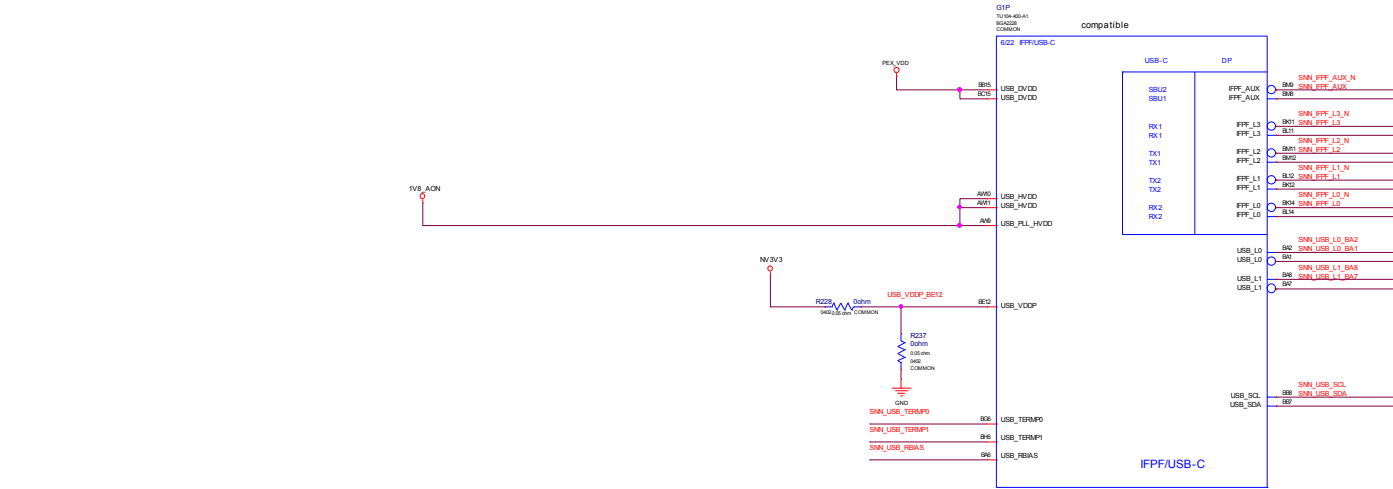




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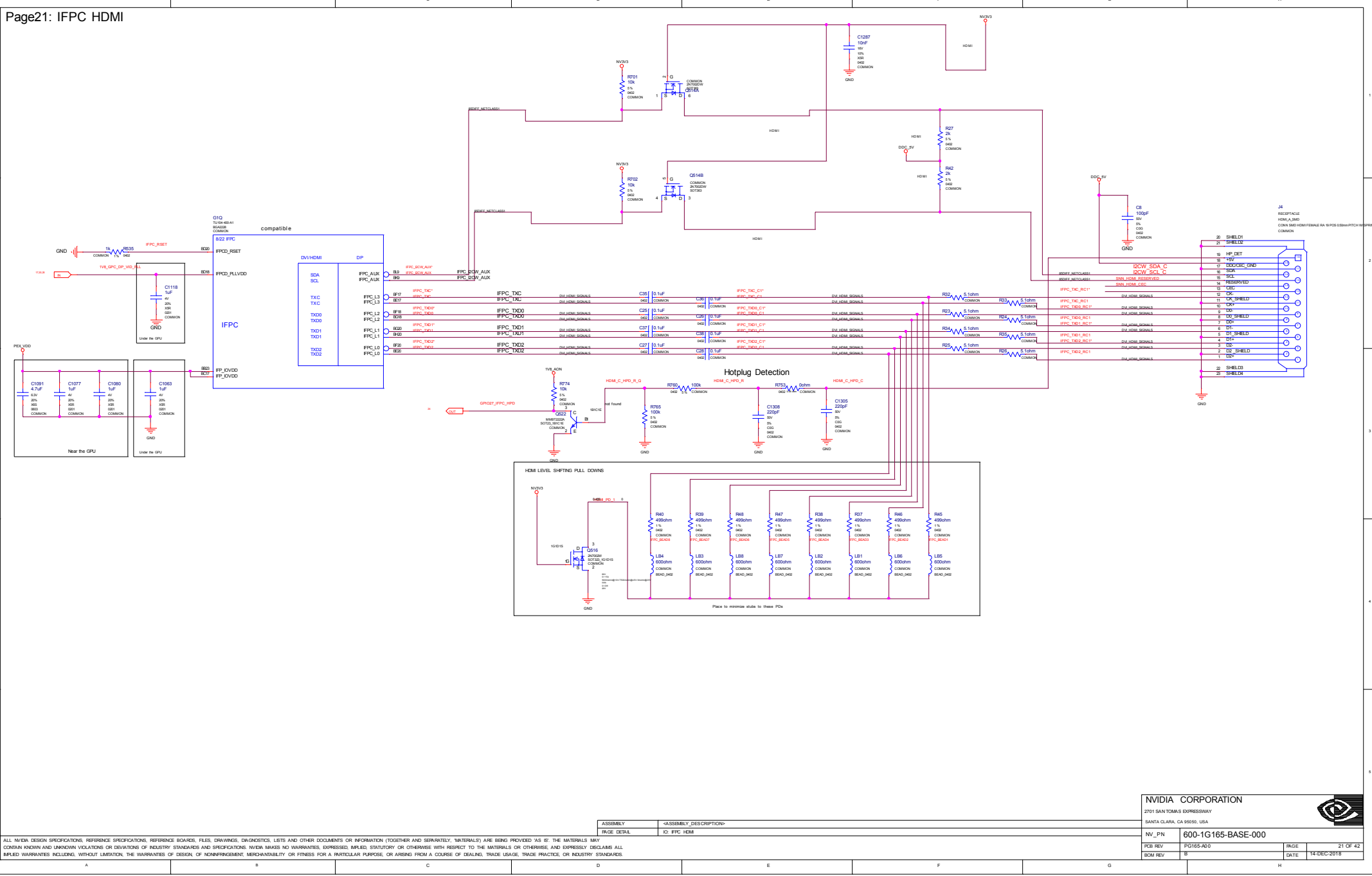


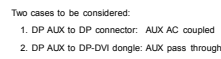
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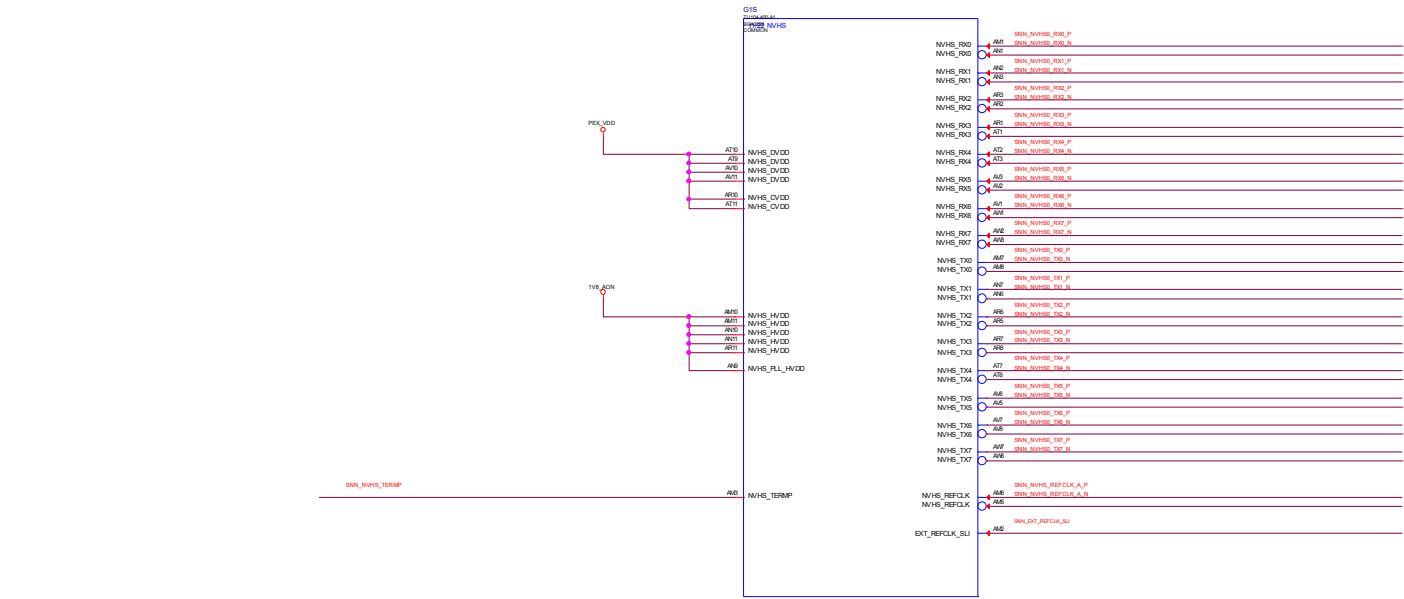
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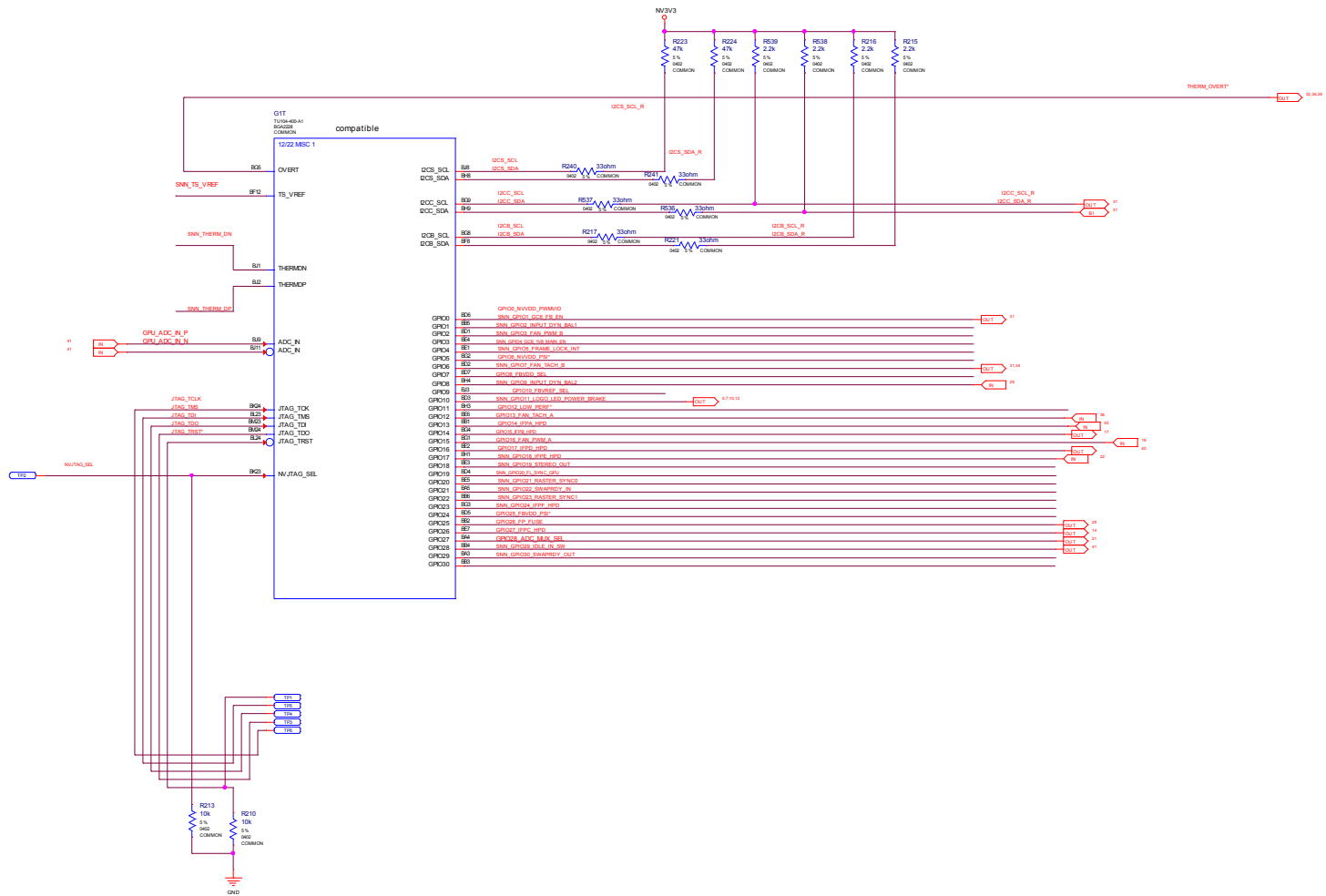
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STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
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L	L	H	00001
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
L	L	M	01000

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L	L	H	0001	Micron 8Gb 256Mx32 MT51J256M32HF-80:B
L	H	L	0010	Hynix 8Gb 256Mx32 H5GC8H24AJR-R2C
L	H	H	0011	Hynix 8Gb 256Mx32 H5GQ8H24MJR-R4C
H	H	L	0110	
H	H	H	0111	
L	L	M	1000	
L	M	L	1001	

ROM_SO	ROM_SI	ROM_SCLK	DUMMY[2:0]_FS_OVERT	1:ENABLE 0:DISABLE	
L	L	L	XXX1	FS_OVERT ENABLE	DEFAULT
L	L	M	XXX0	FS_OVERT DISABLE	

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

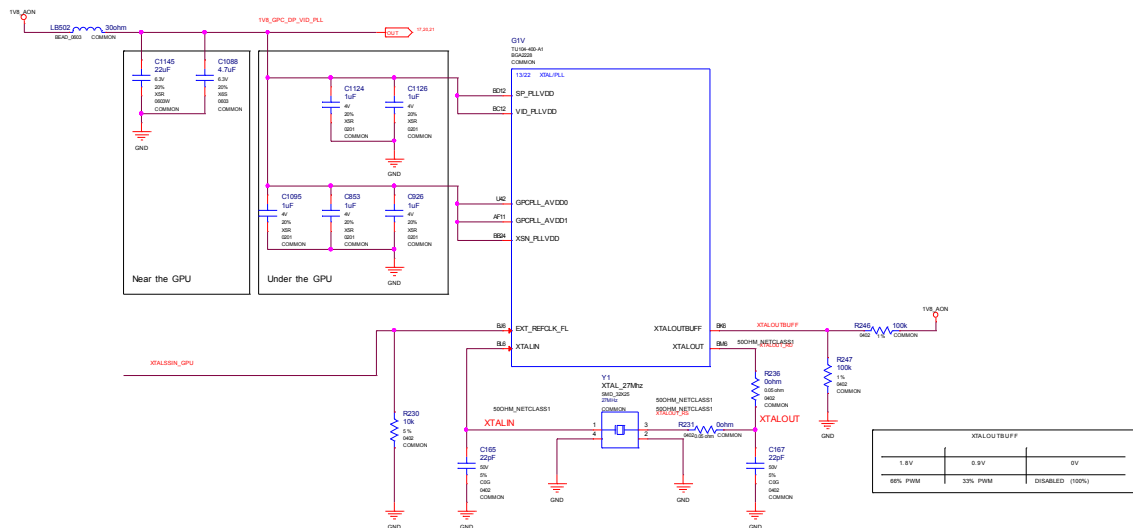
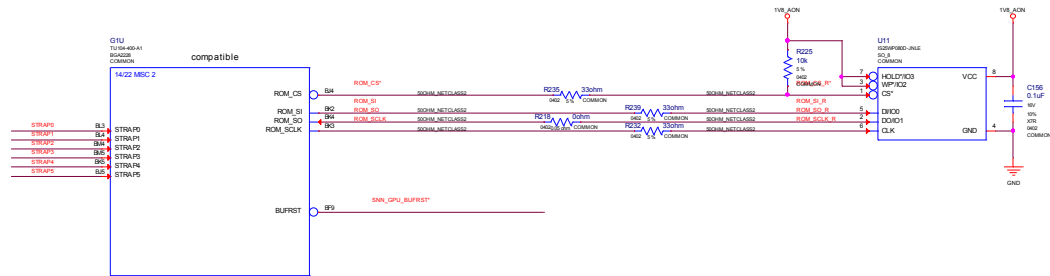
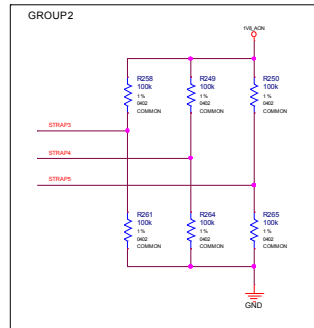
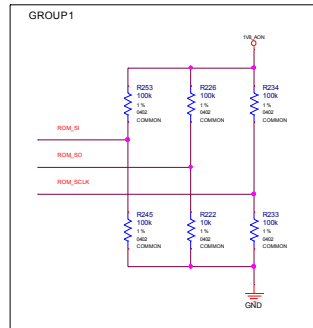
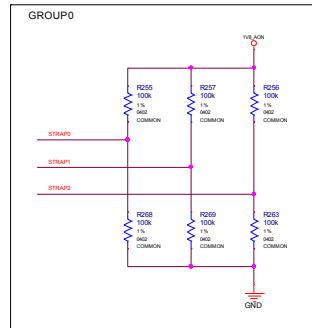
H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL

1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

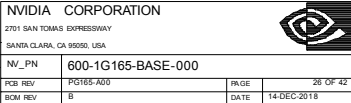
1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE

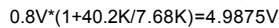


XTALOUTBUFF		
1.8V	0.9V	0V
80% PWM	30% PWM	DISABLED (100%)

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NV_PN	600-1G165-BASE-000				
PCB REV	PG165-ADD	PKG	25 OF 42		
ROM REV	B	DATE	14-DEC-2018		

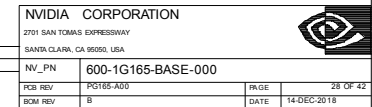
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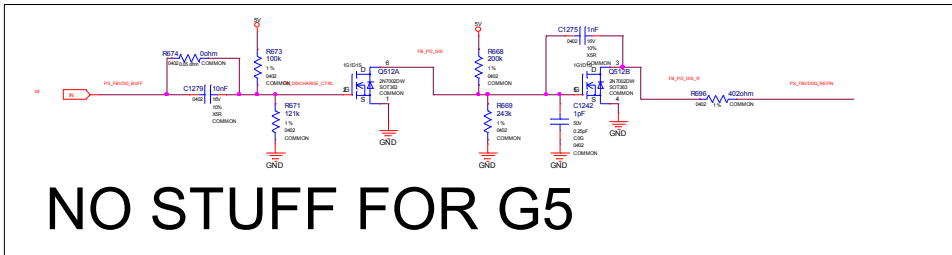




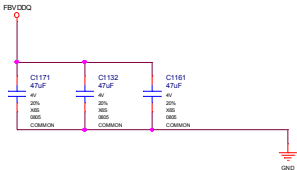
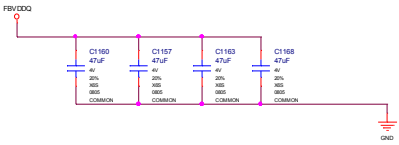
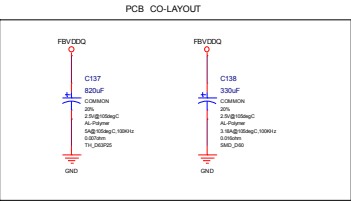
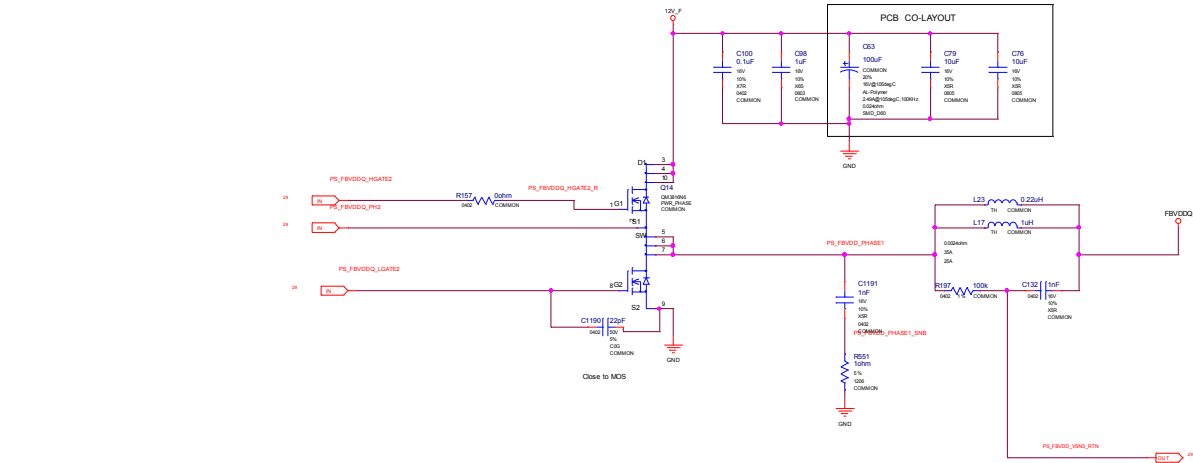
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PCB REV	PG165-A00	PAGE	27 OF 42	
BOM REV	B	DATE	14-DEC-2018	

СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!



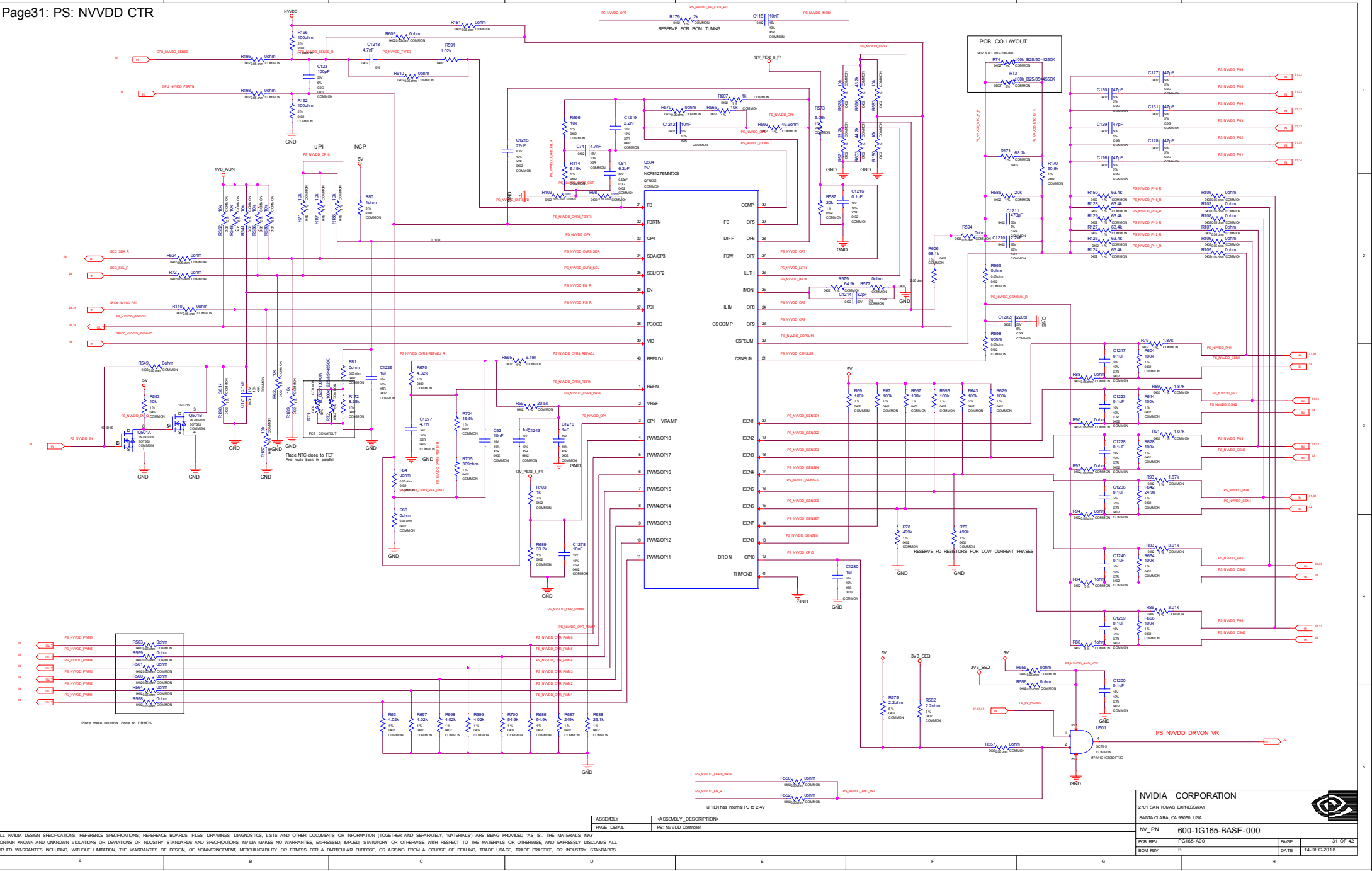


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PCB REV	PG165-A00	PRGE	29 OF 42
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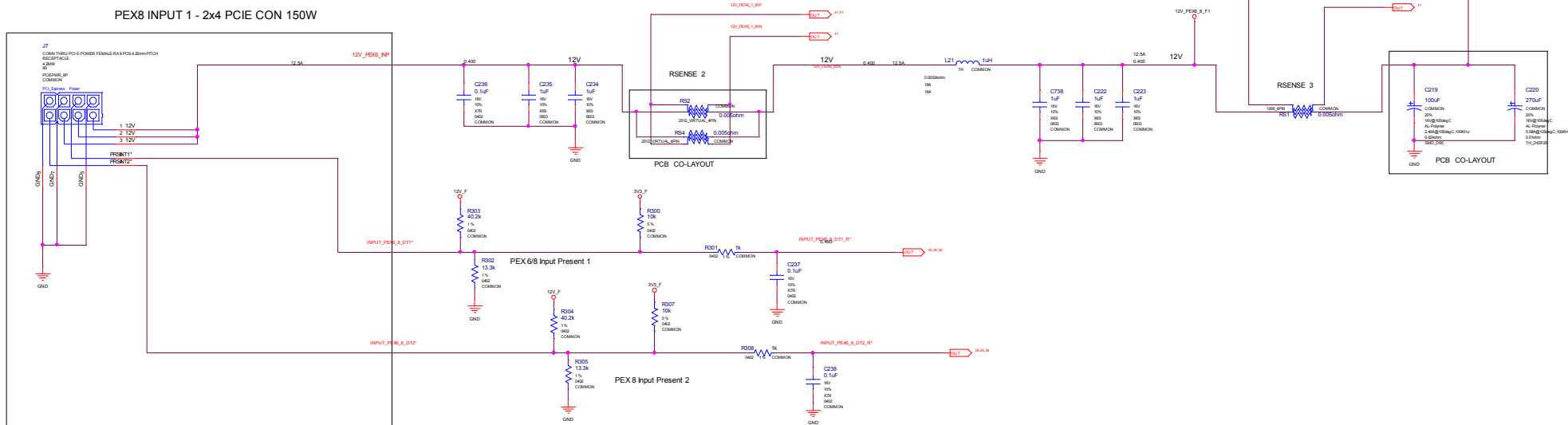
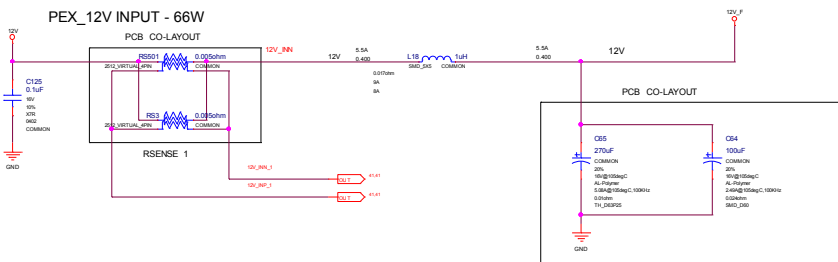
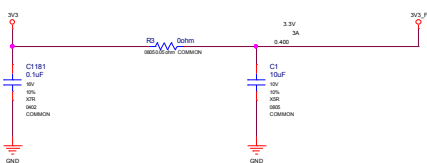
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NV_PN	600-1G165-BASE-000		
PCB REV	PG165-A00	PKG	30 OF 42
BOM REV	8	DATE	14-DEC-2018



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ASSEMBLY	DESCRIPTION
PS: NVVDD CTR <td>PS: NVVDD Controller</td>	PS: NVVDD Controller

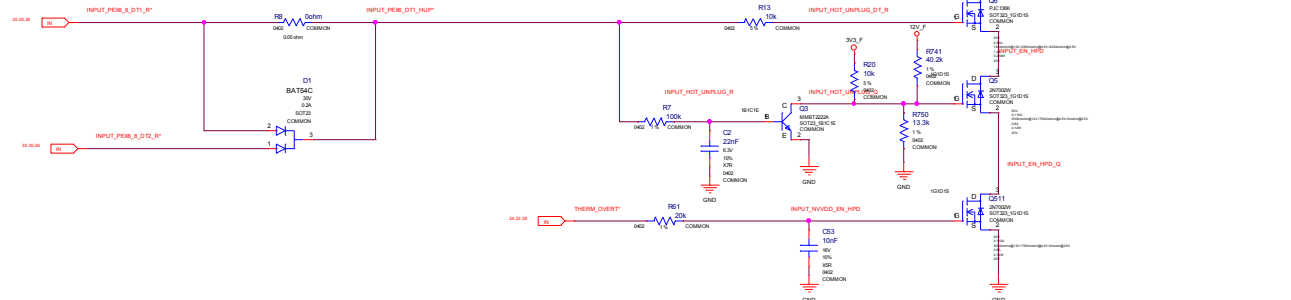
REV	DATE	BY	CHKD
1.0	14-DEC-2018		



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NV_PN		600-1G165-BASE-000	
PCB REV	PG165-A00	PRGE	35 OF 42
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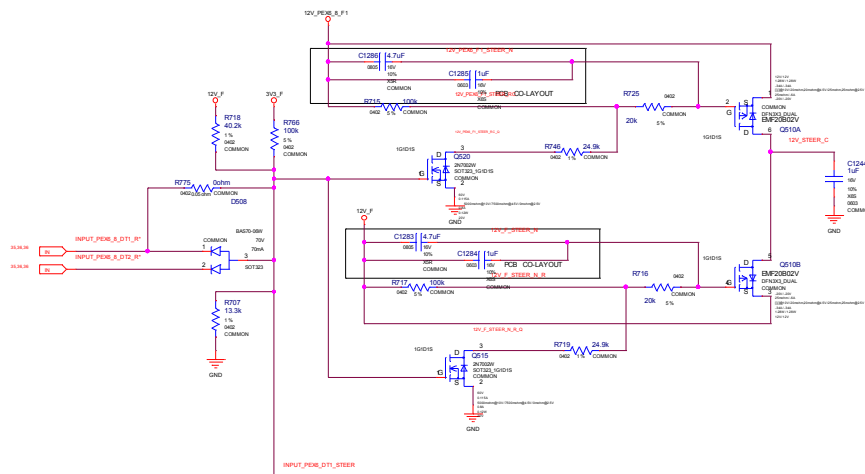
PEX Input Present 1

POWER CONNECTOR HOT-UNPLUG DETECT

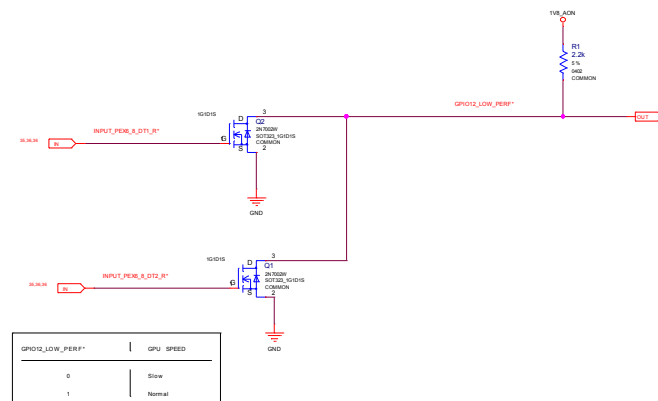


12V CURRENT STEERING (UNDER POWER BOOT):

GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA



PEX Input - Power Level/PSI* Control



ASSEMBLY

ASSEMBLY DESCRIPTION

PS: CURRENT STEERING UNDER POWER BOOT, HOT-UNPLUG

600-1G165-BASE-000

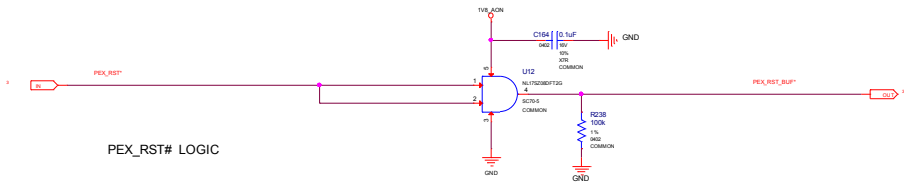
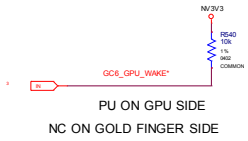
PG165-A00

DATE 14-DEC-2018





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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	SEQ MISC

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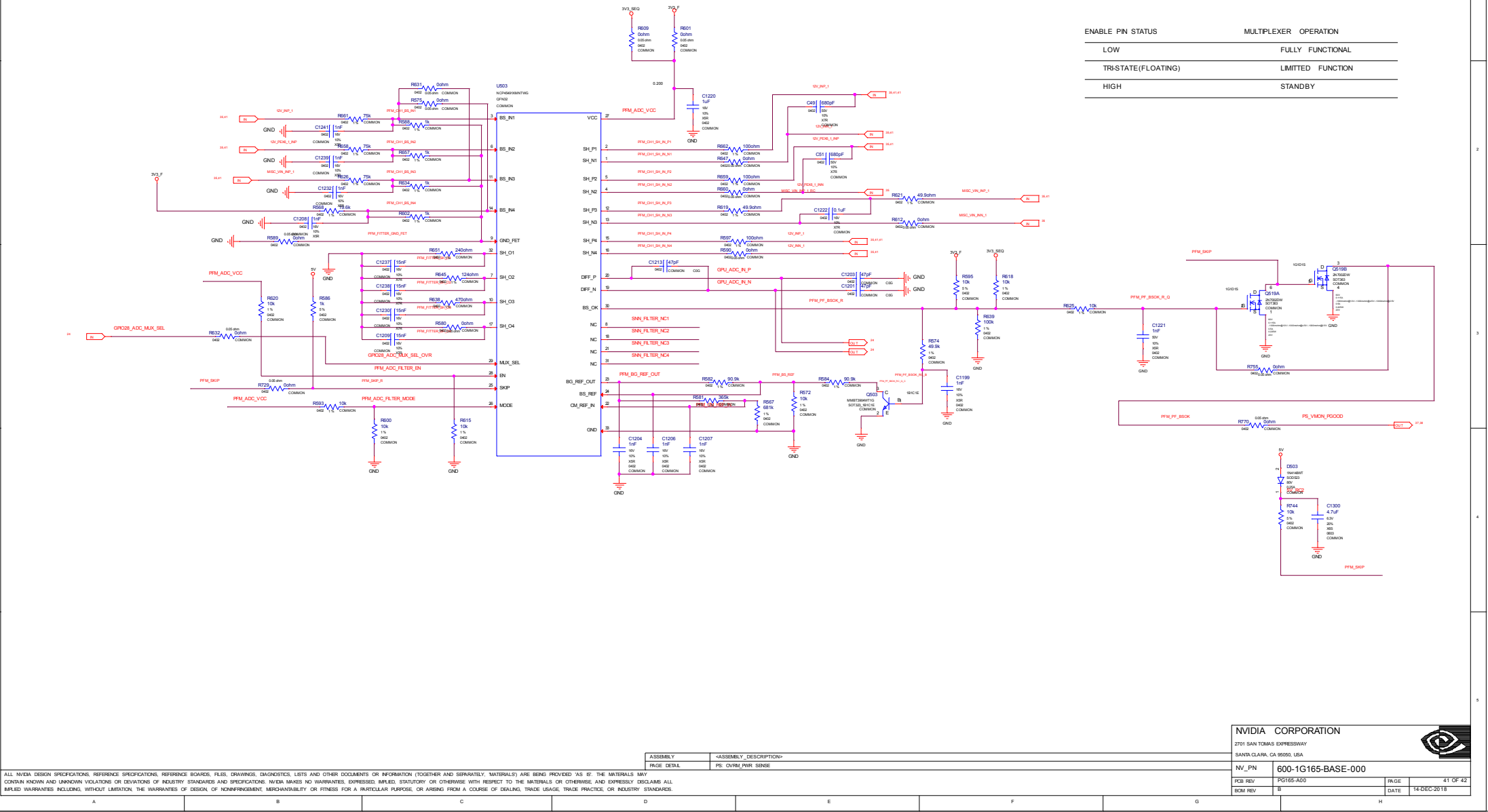


NV_PN	600-1G165-BASE-000		
PCB REV	PG165-A00	PRGE	39 OF 42
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[illegible]

H

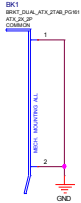
DATE	14-DEC-2018
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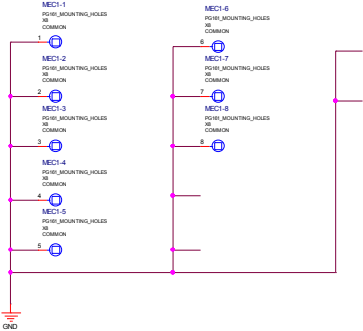
MODE PN STATUS	MULTIPLEXER	OPERATION
LOW	DEVICE A	
TRI-STATE(FLOATING)	STAND-ALONE	
HIGH	DEVICE B	

ENABLE PN STATUS	MULTIPLEXER	OPERATION
LOW	FULLY FUNCTIONAL	
TRI-STATE(FLOATING)	LIMITED FUNCTION	
HIGH	STANDBY	

Bracket:



MOUNTING HOLES FOR HS:



BACK STIFFENER:



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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PCB DETAIL	MECH

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PCB REV	PG165-A00	PRGE	42 OF 42
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