

PG161-A01

TU106 6GB GDDR6, 192b, X16
DVI-D/DP + DP + HDMI

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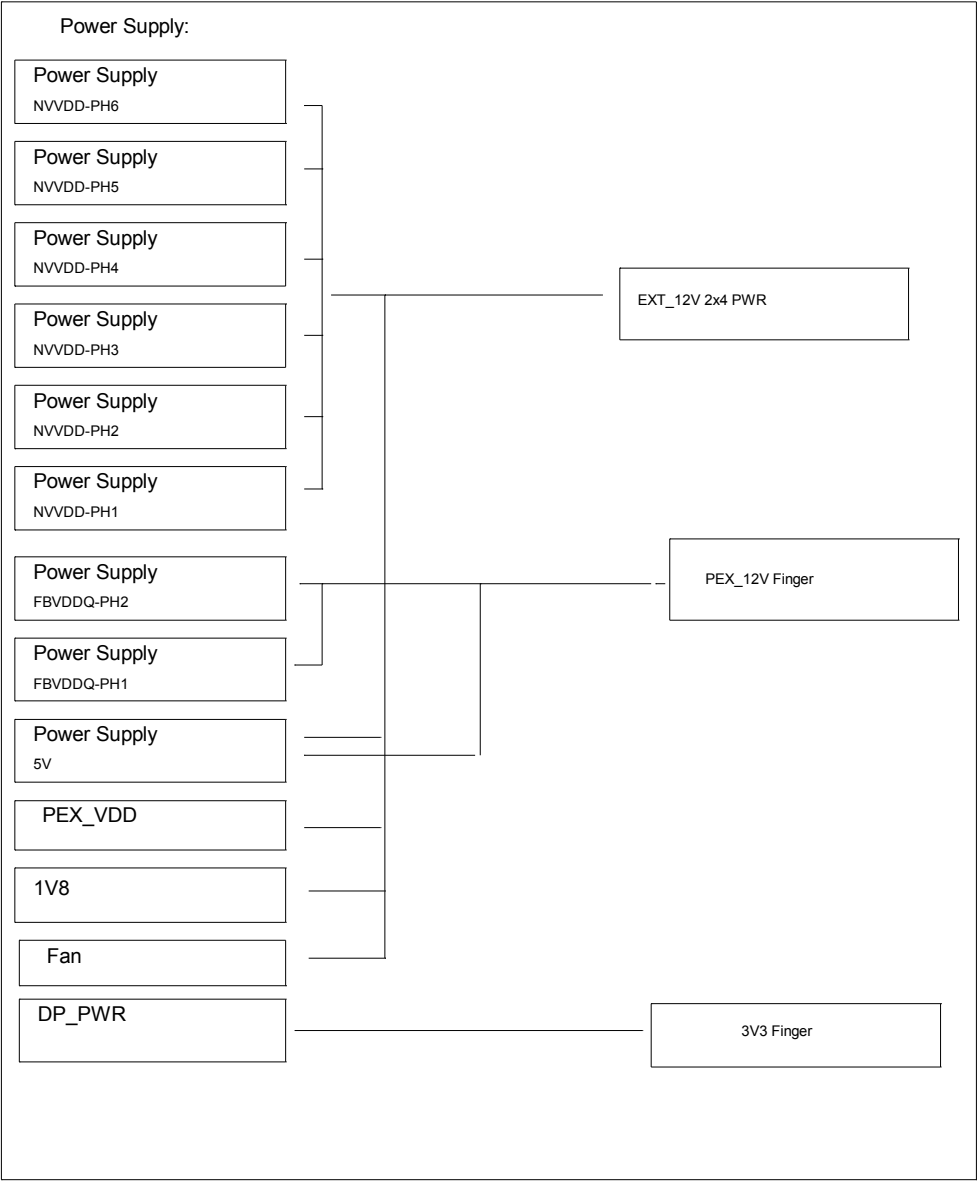
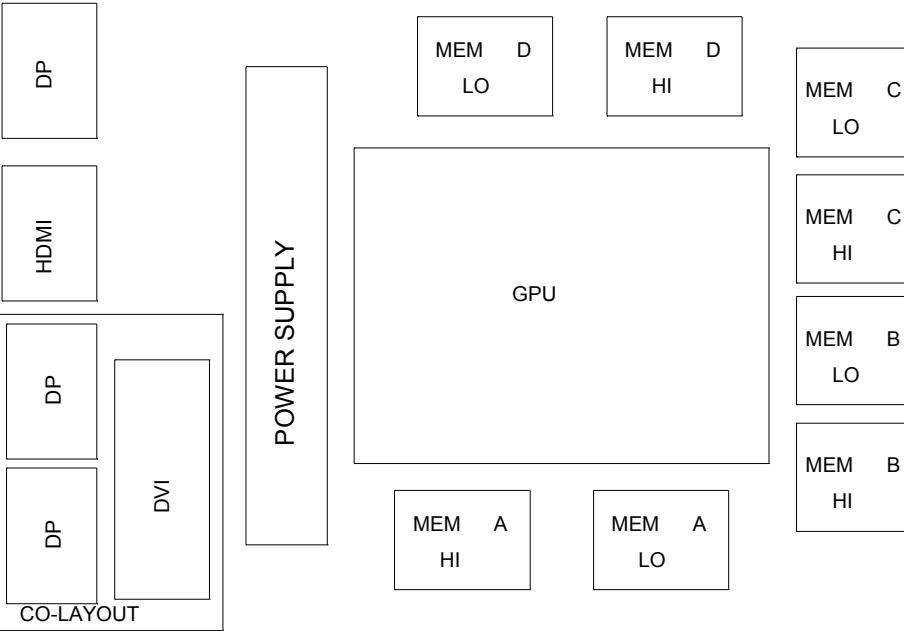
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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE DETAIL	Block Diagram

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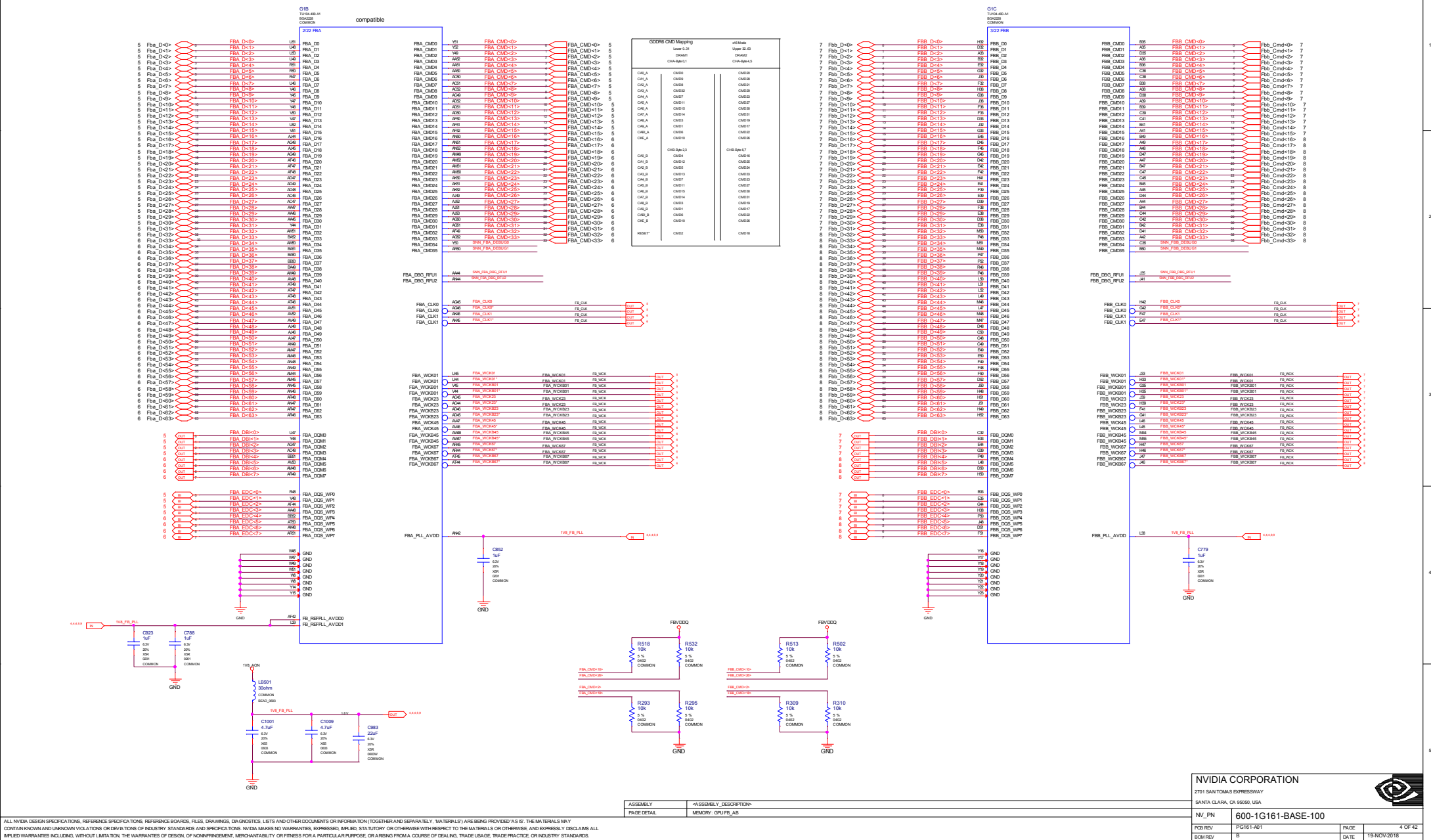




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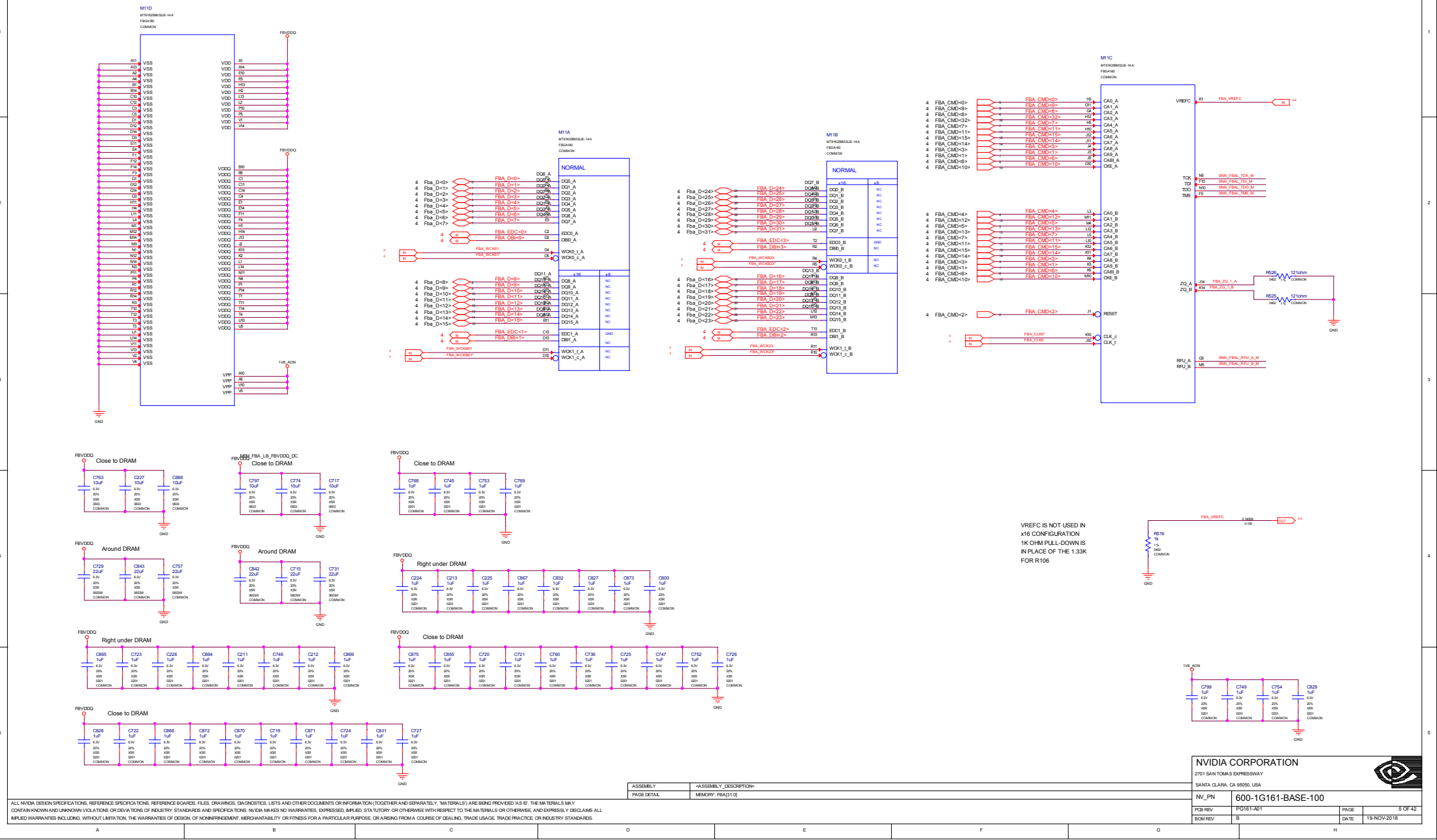
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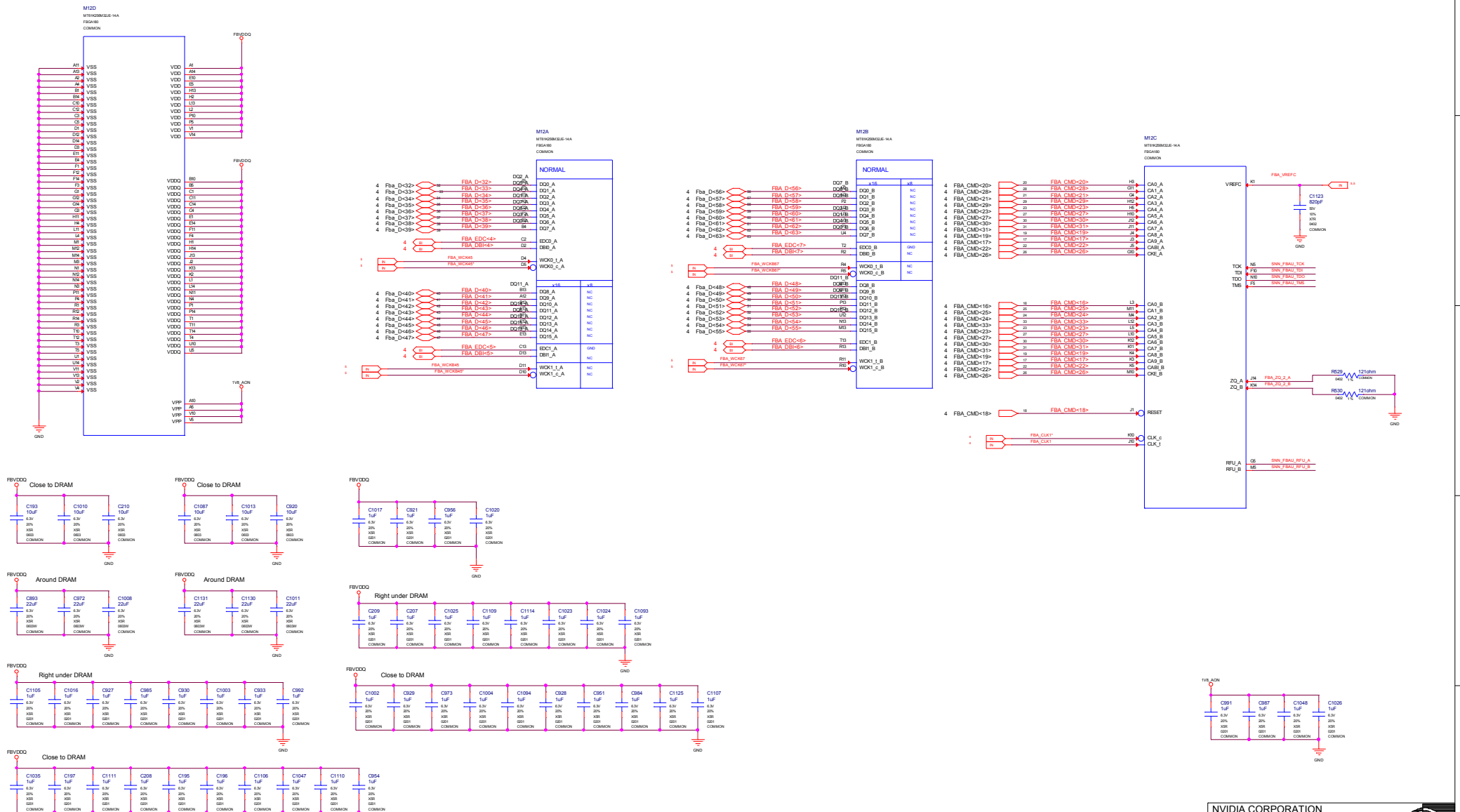


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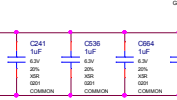
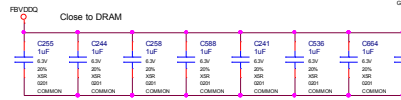
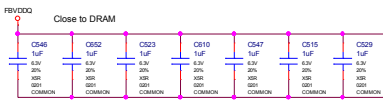
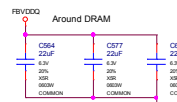
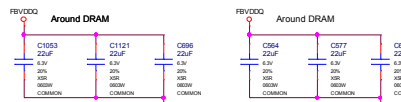
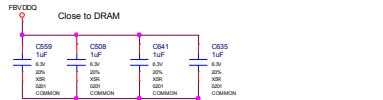
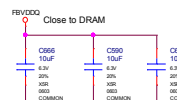
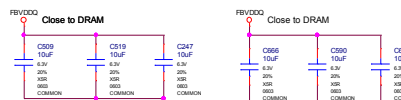
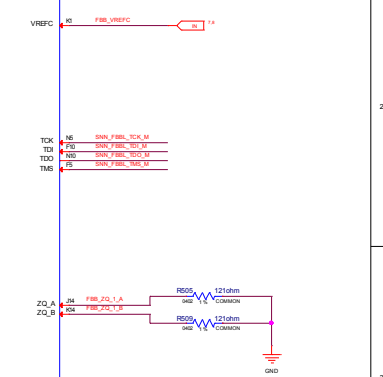
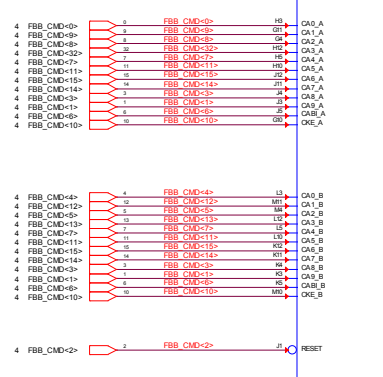
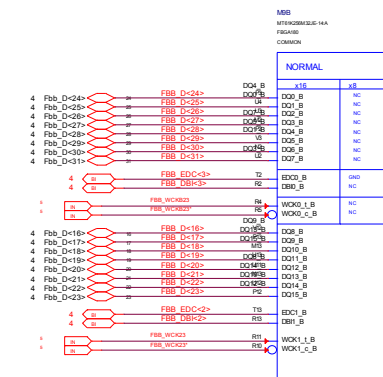
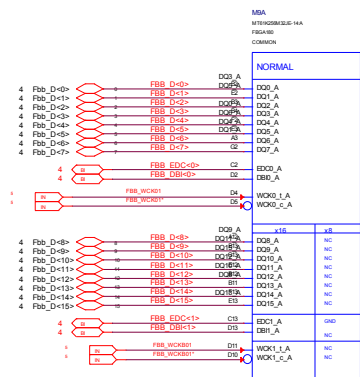
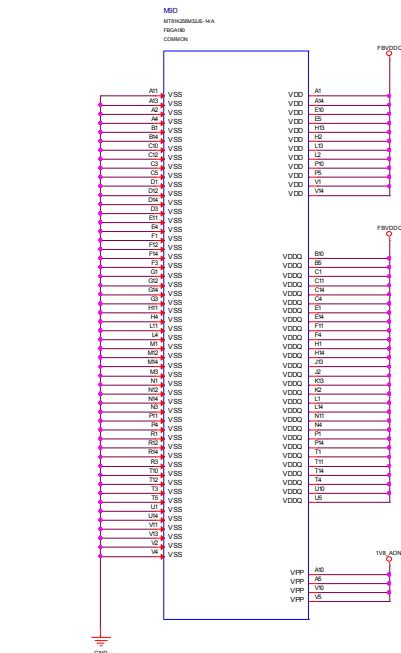
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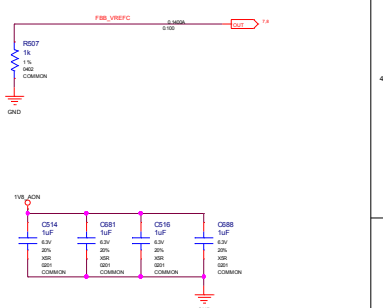
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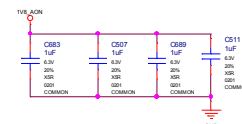
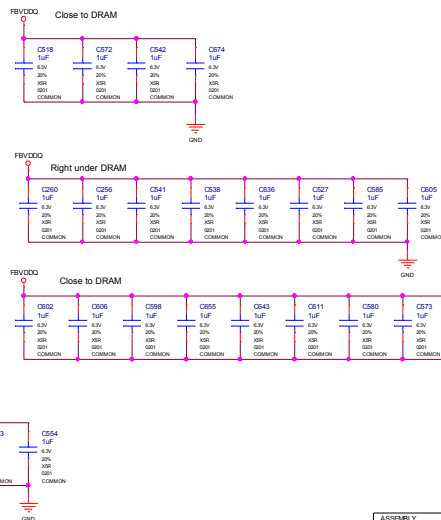
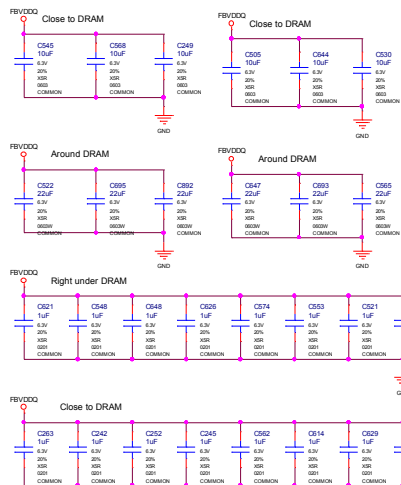
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1K OHM PULL-DOWN IS
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FOR R104



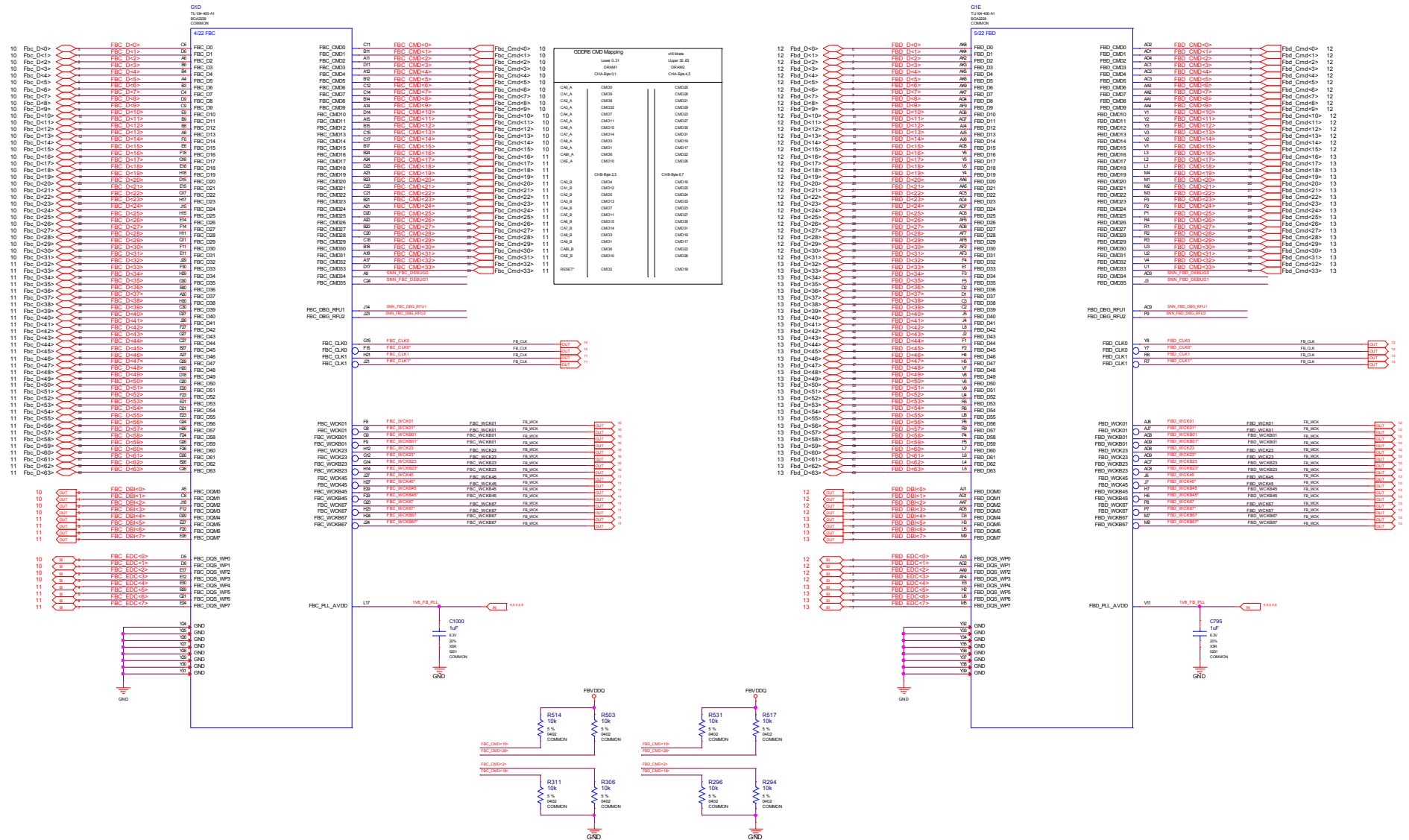
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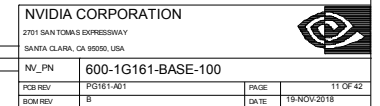


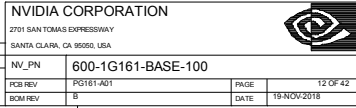
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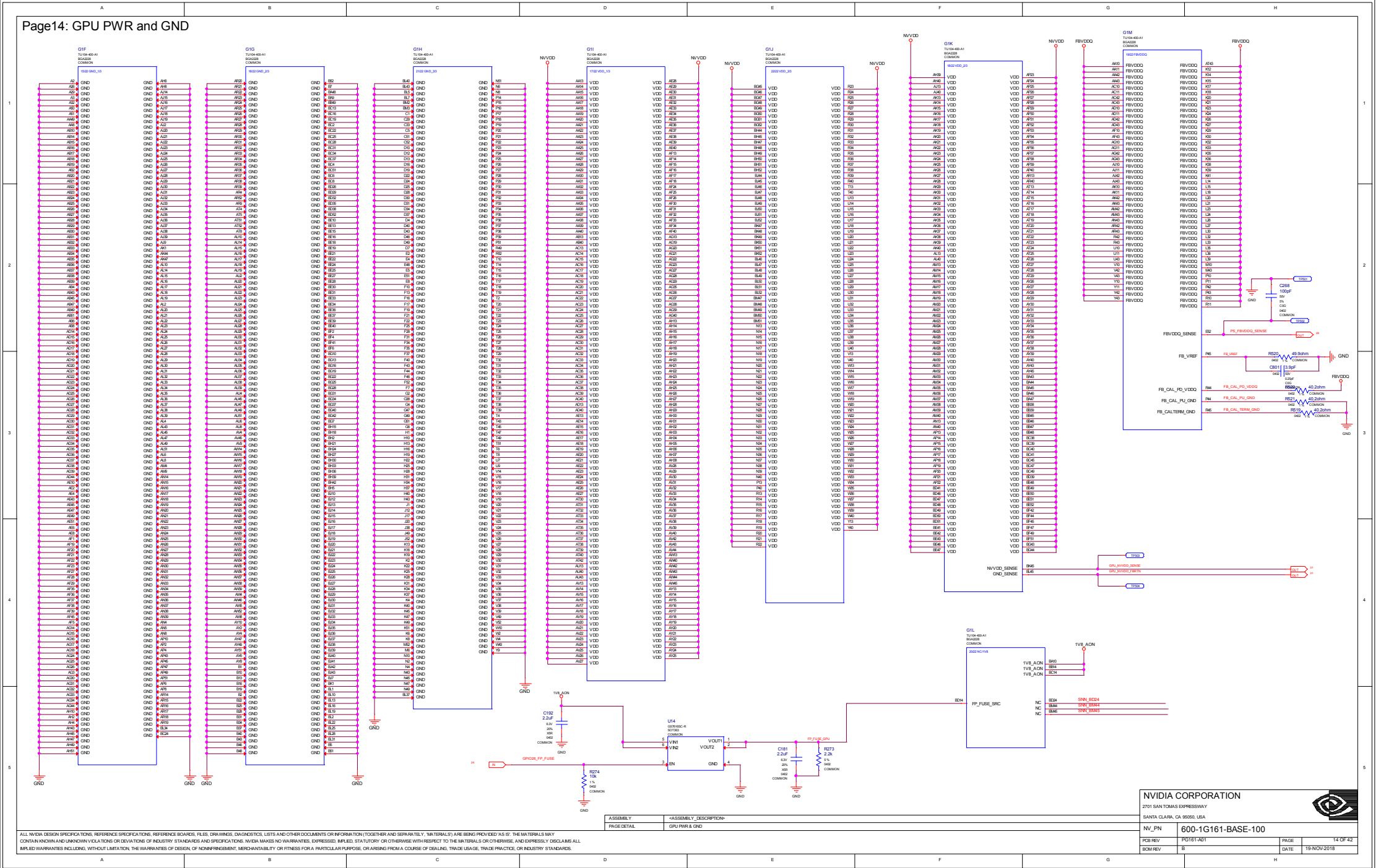
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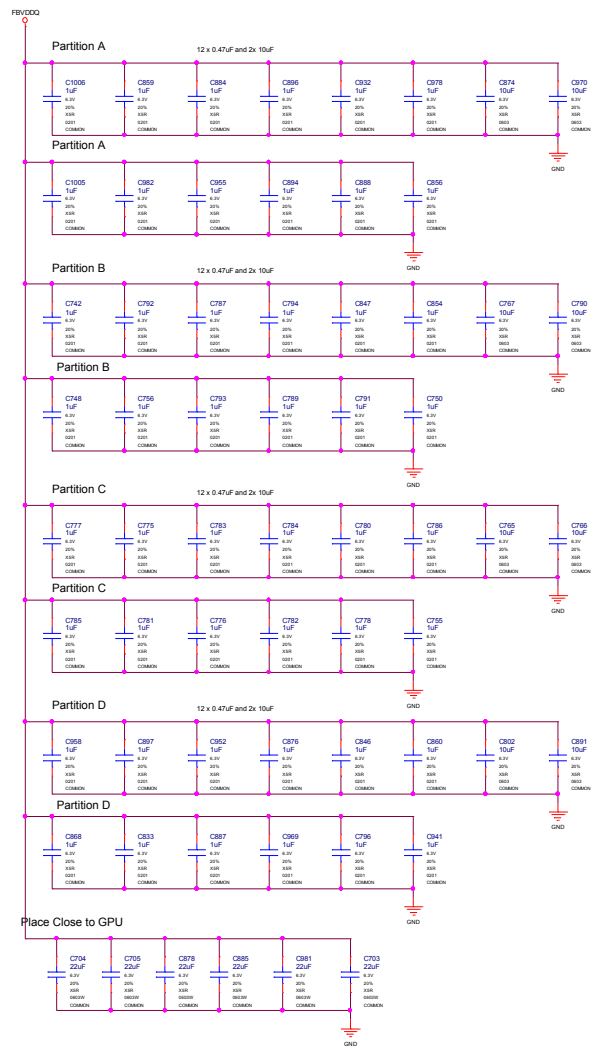


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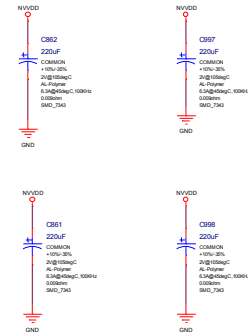
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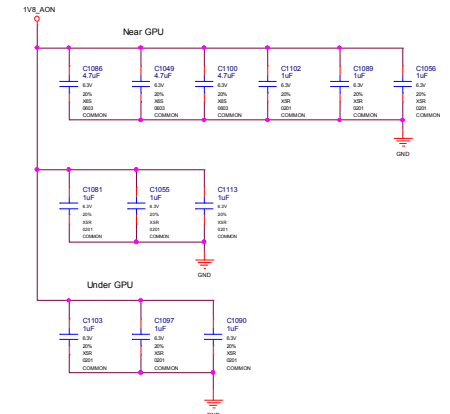
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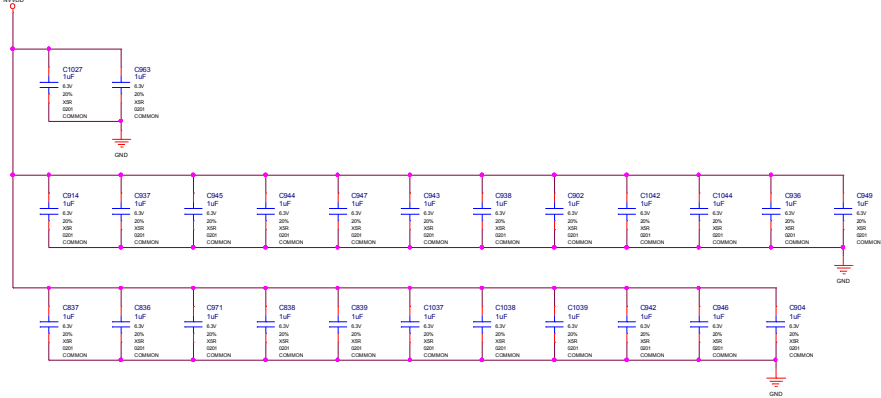
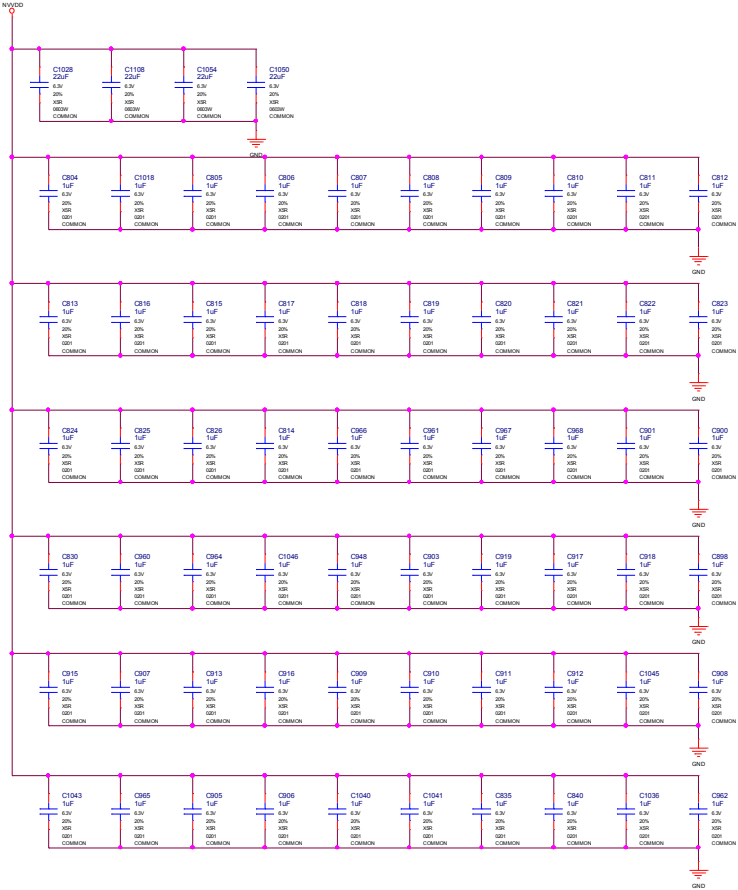


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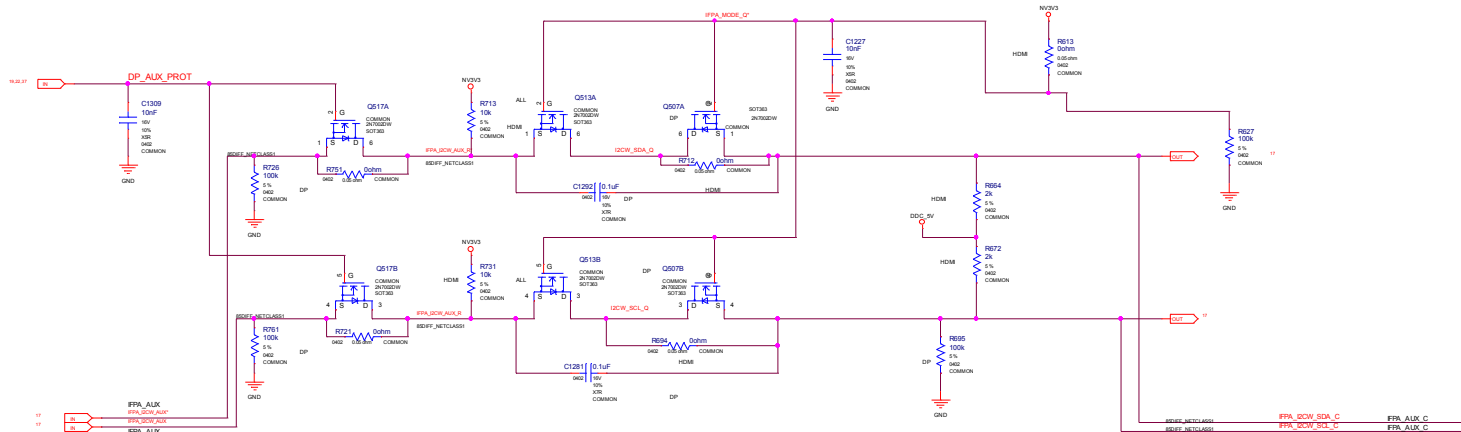
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IFPA DP CO-LAYOUT WITH IFPAB DVI-D-DL

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IFPB DP CO-LAYOUT WITH IFPAB DVI-D-DL

Two cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through

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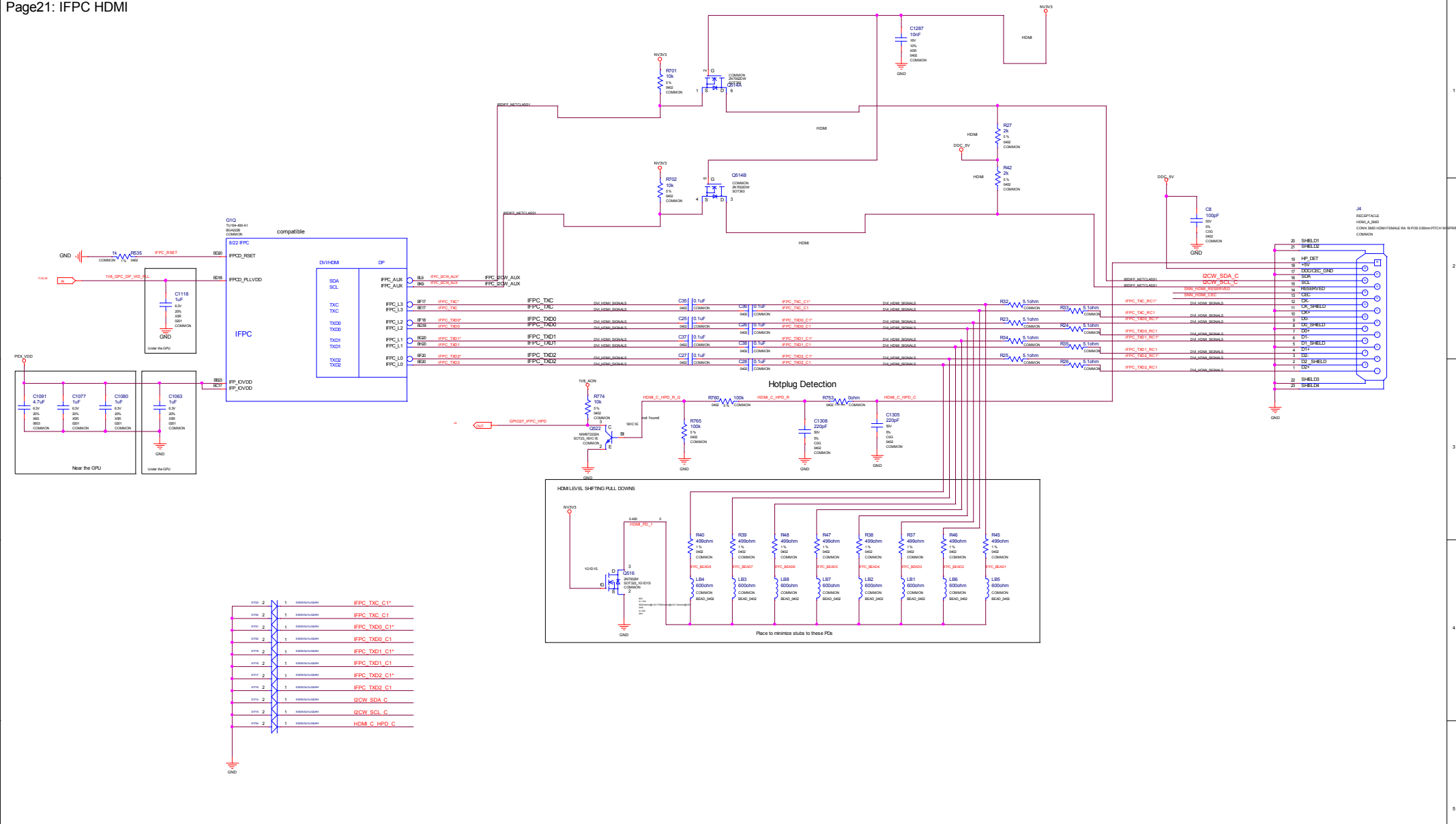
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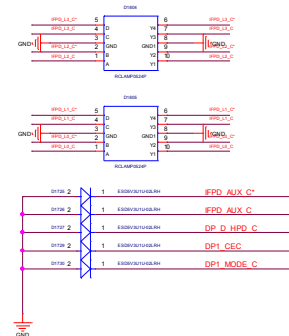
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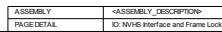
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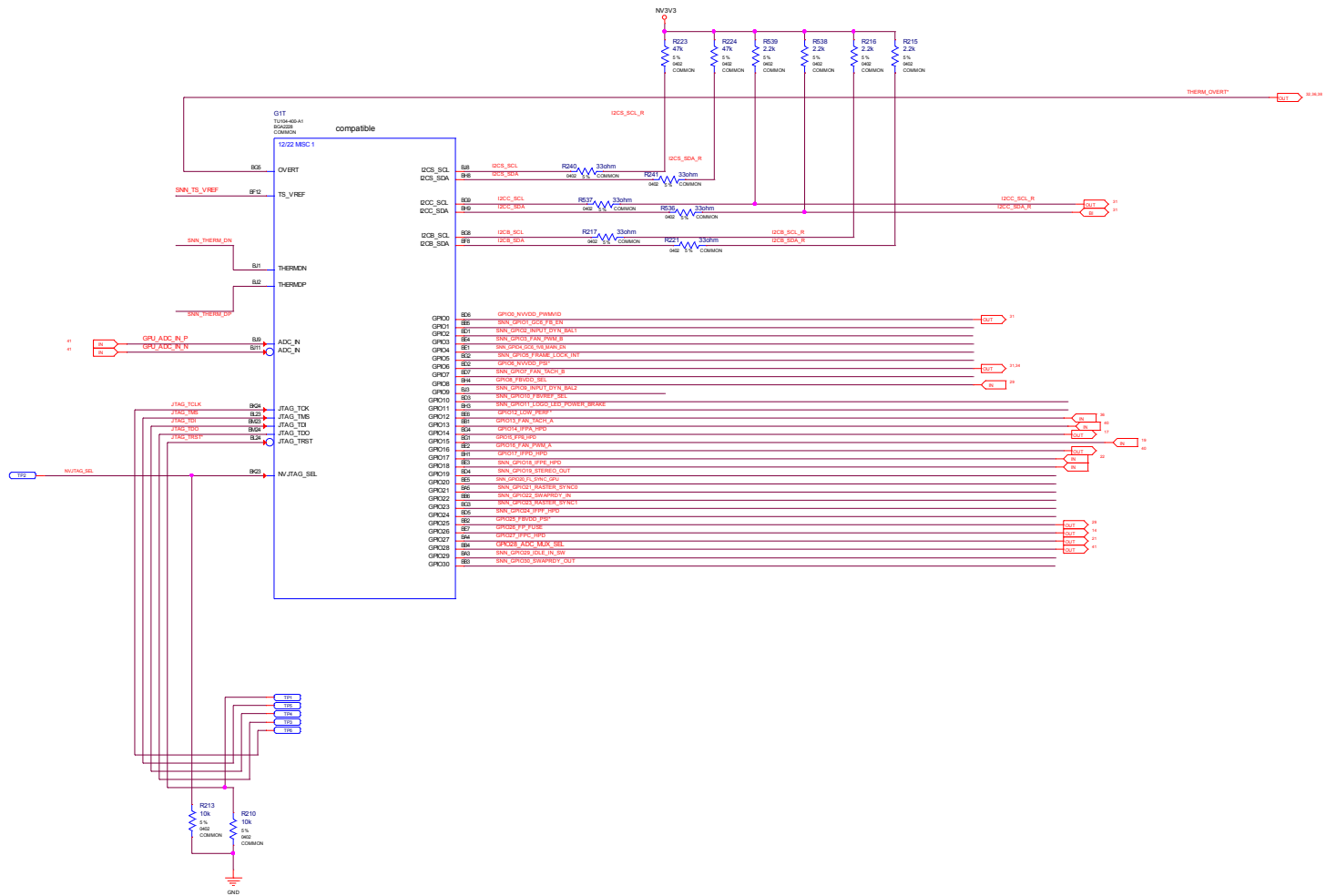


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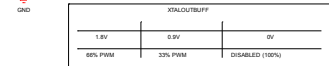
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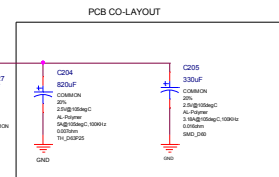
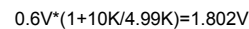
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00011	8Gb	256-bit	Samsung	C
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00101	8Gb	256-bit	Hynix	M
00110	16Gb	256-bit	Samsung	M
00111				

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
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L	H	M	1	0	1	1
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L	M	L	1	0	0	1
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H	H	H	0	1	1	1
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L	H	L	0	0	1	0
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1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE

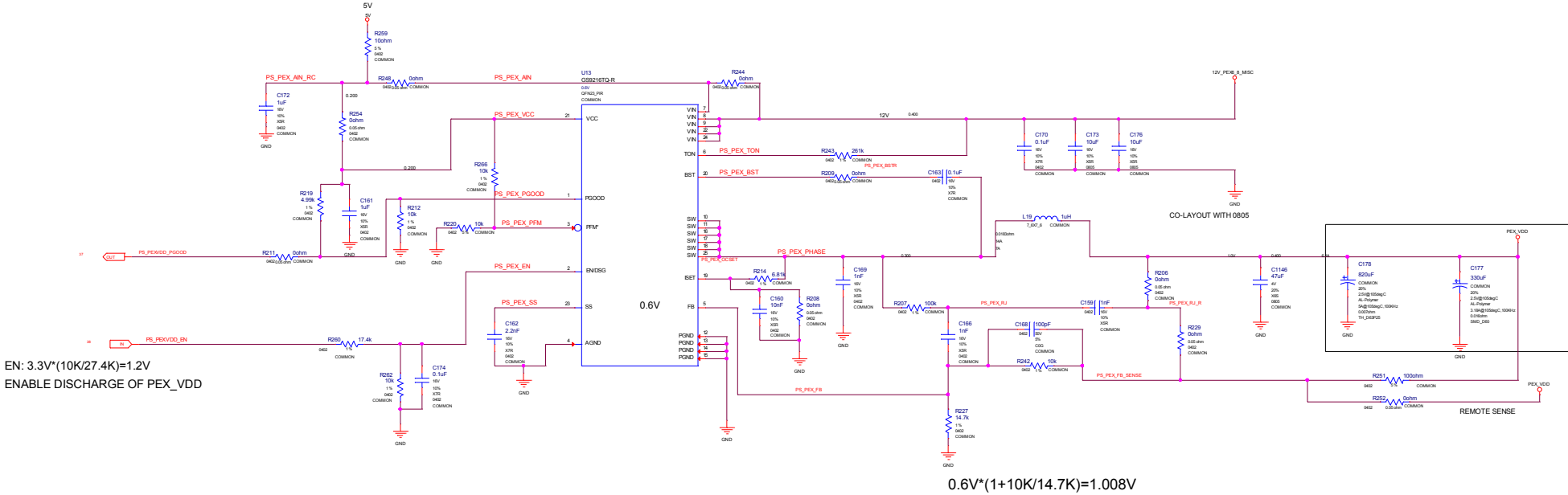


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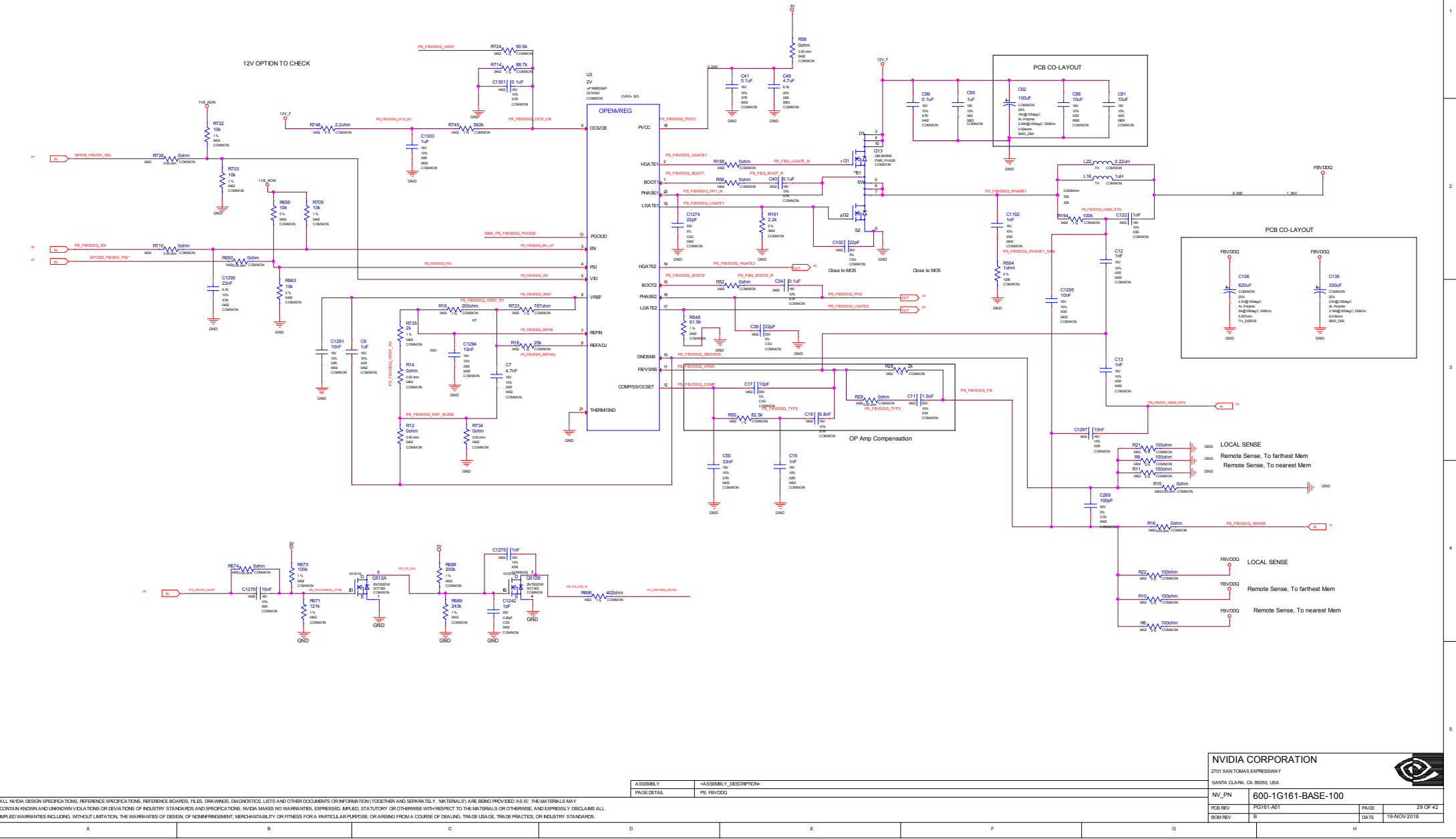
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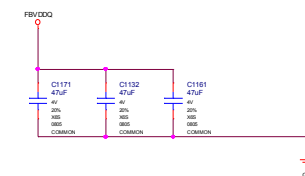
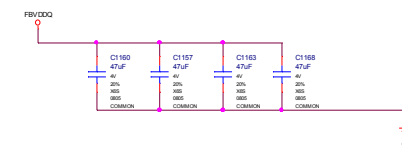
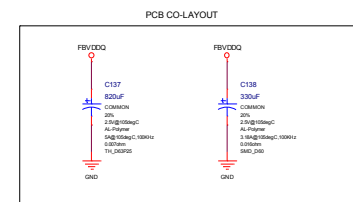
PEX Switcher



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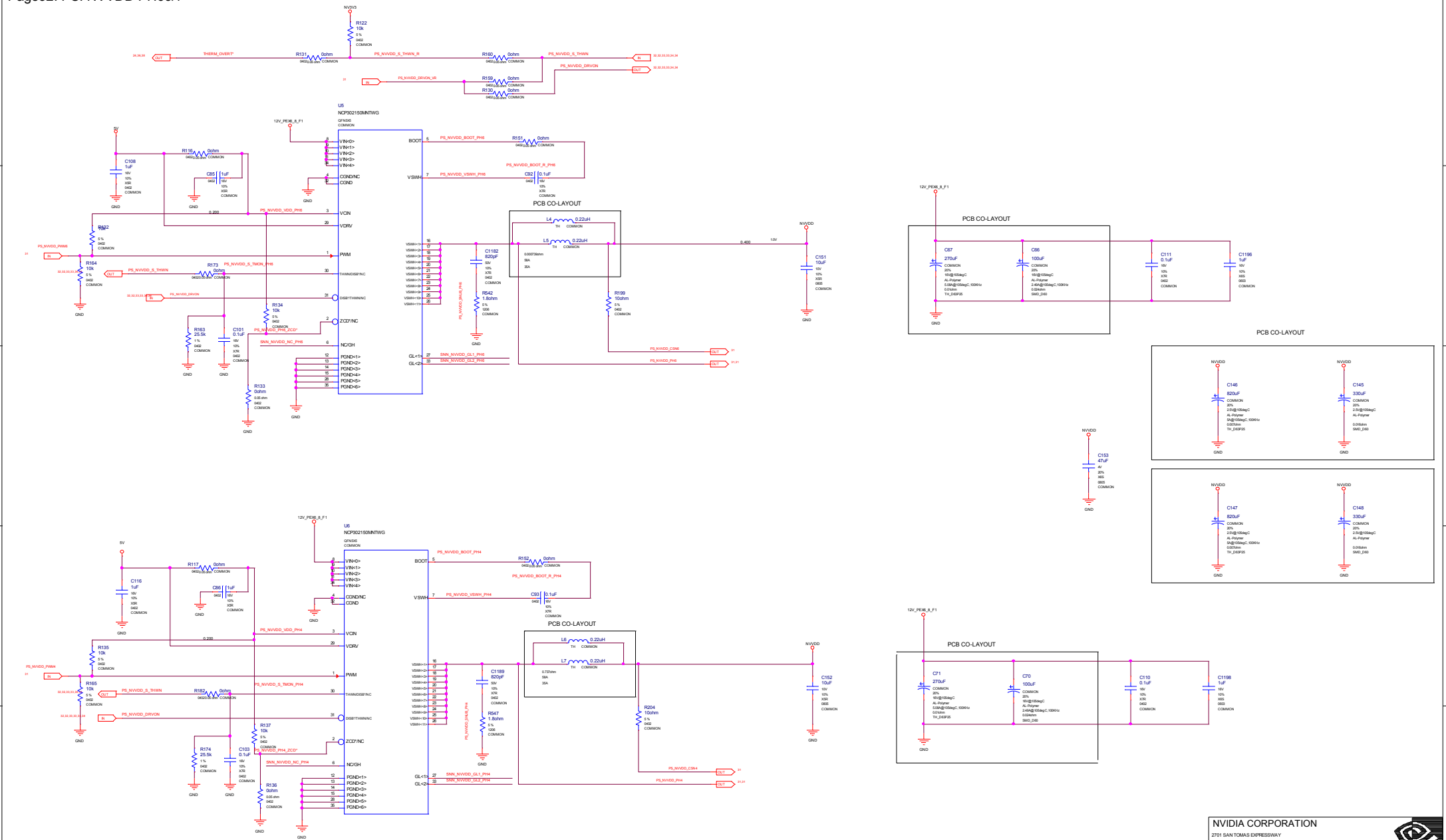


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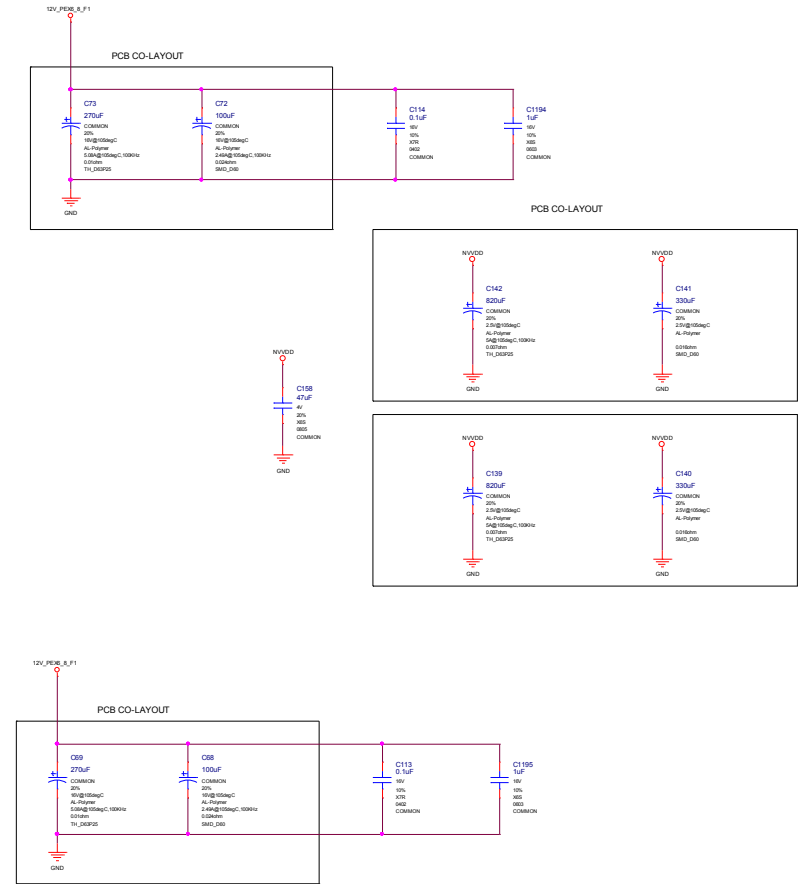
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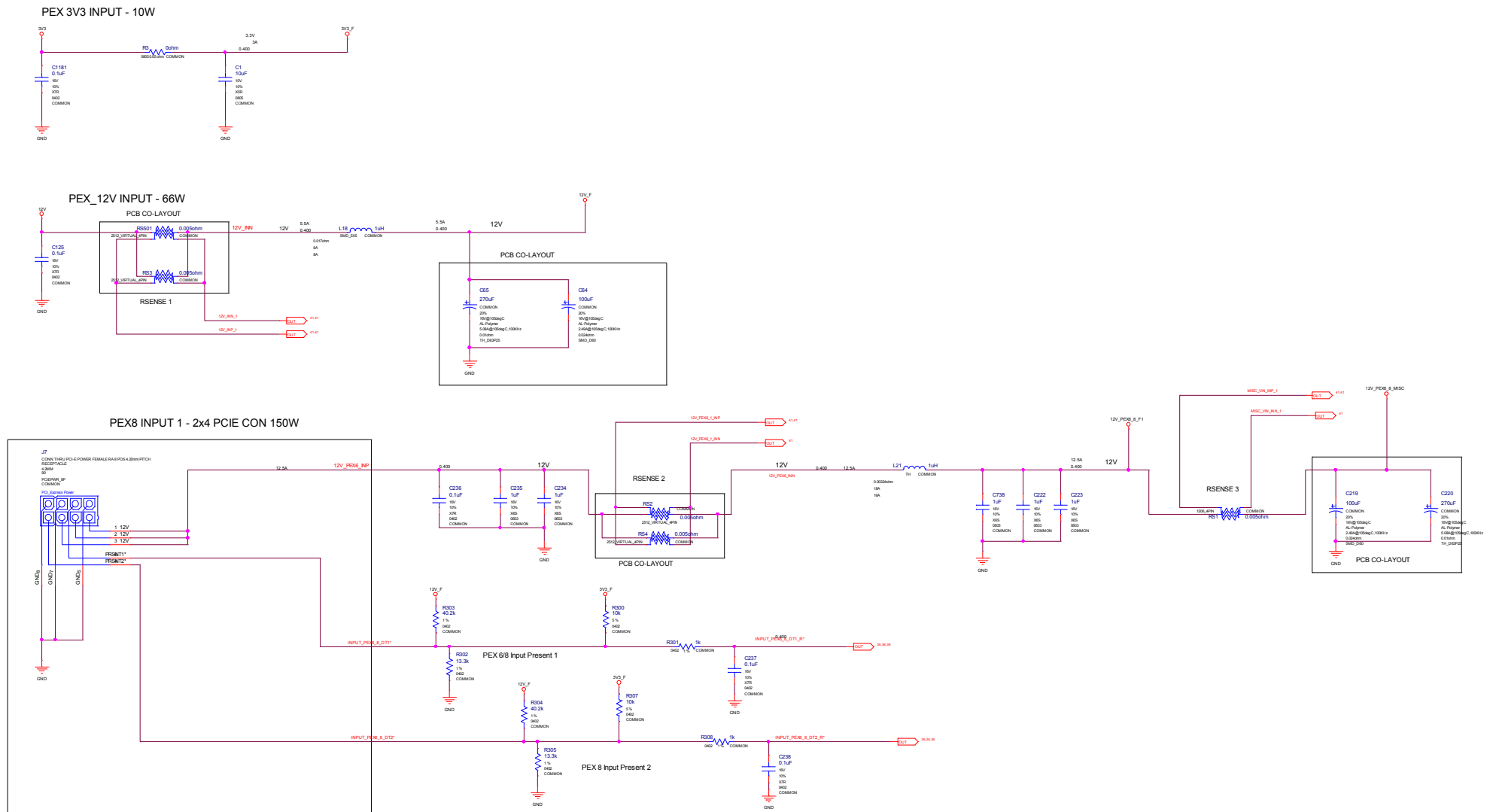
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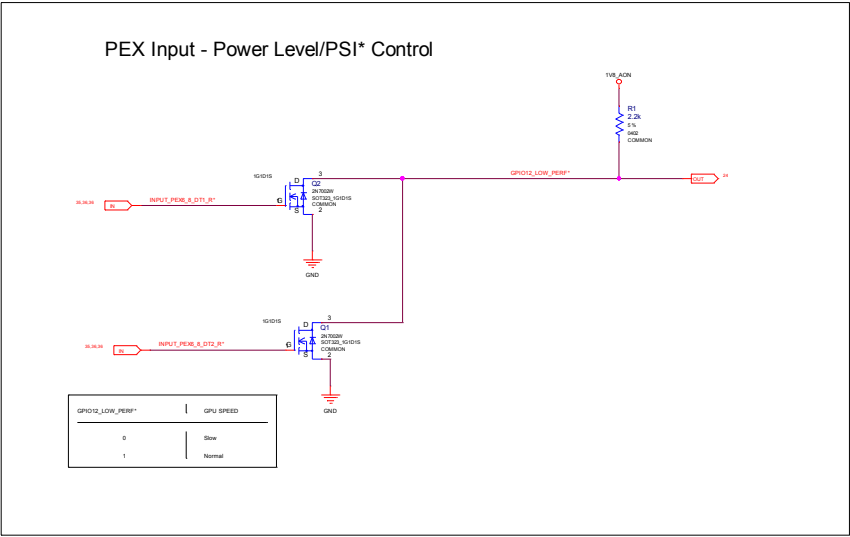
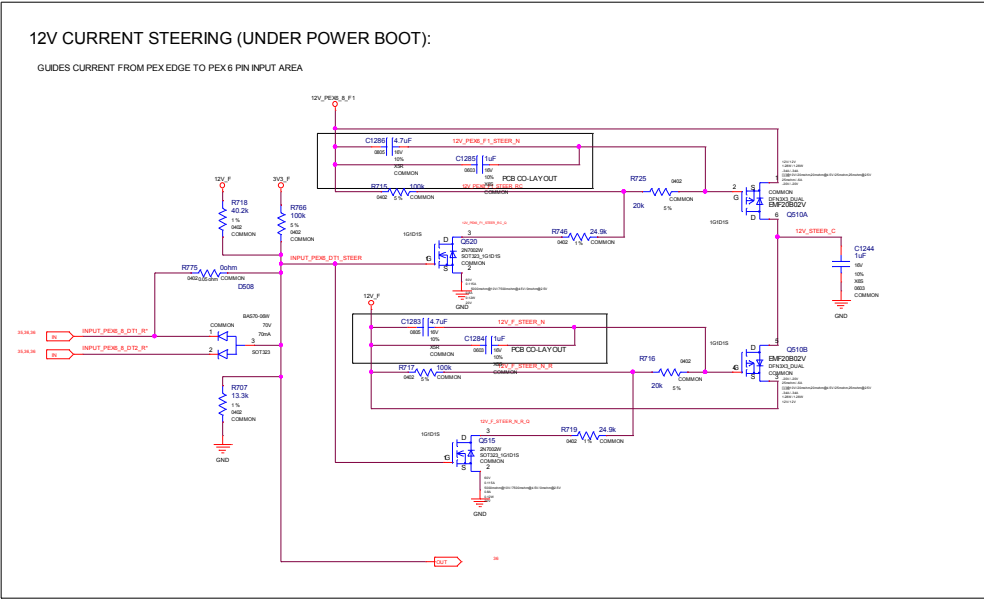
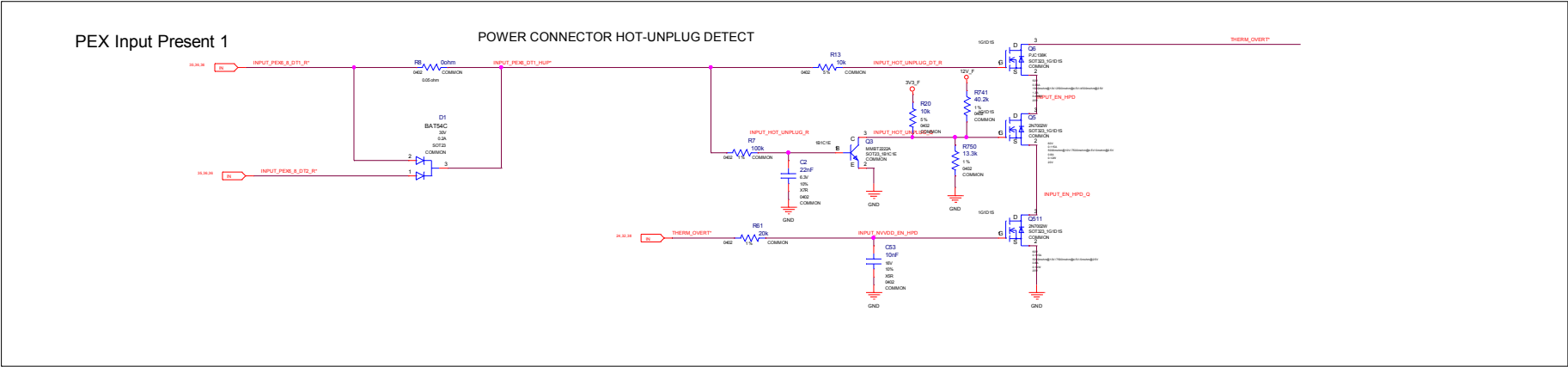
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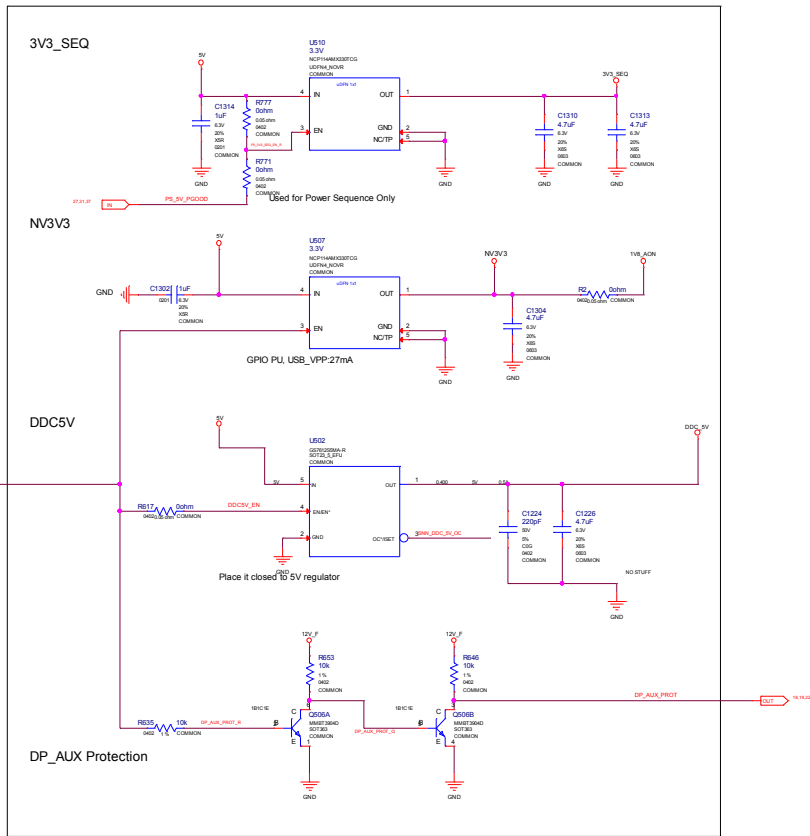
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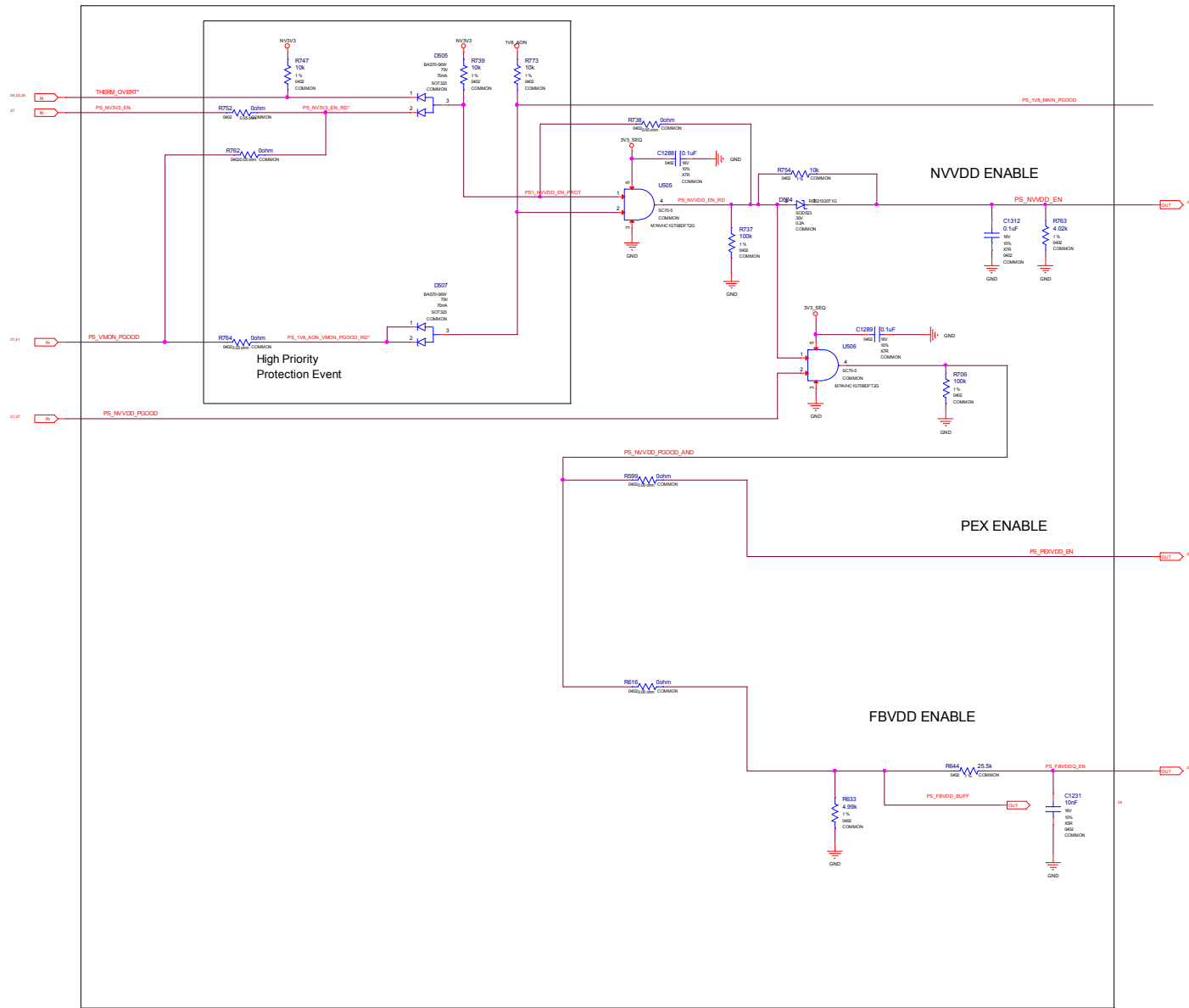
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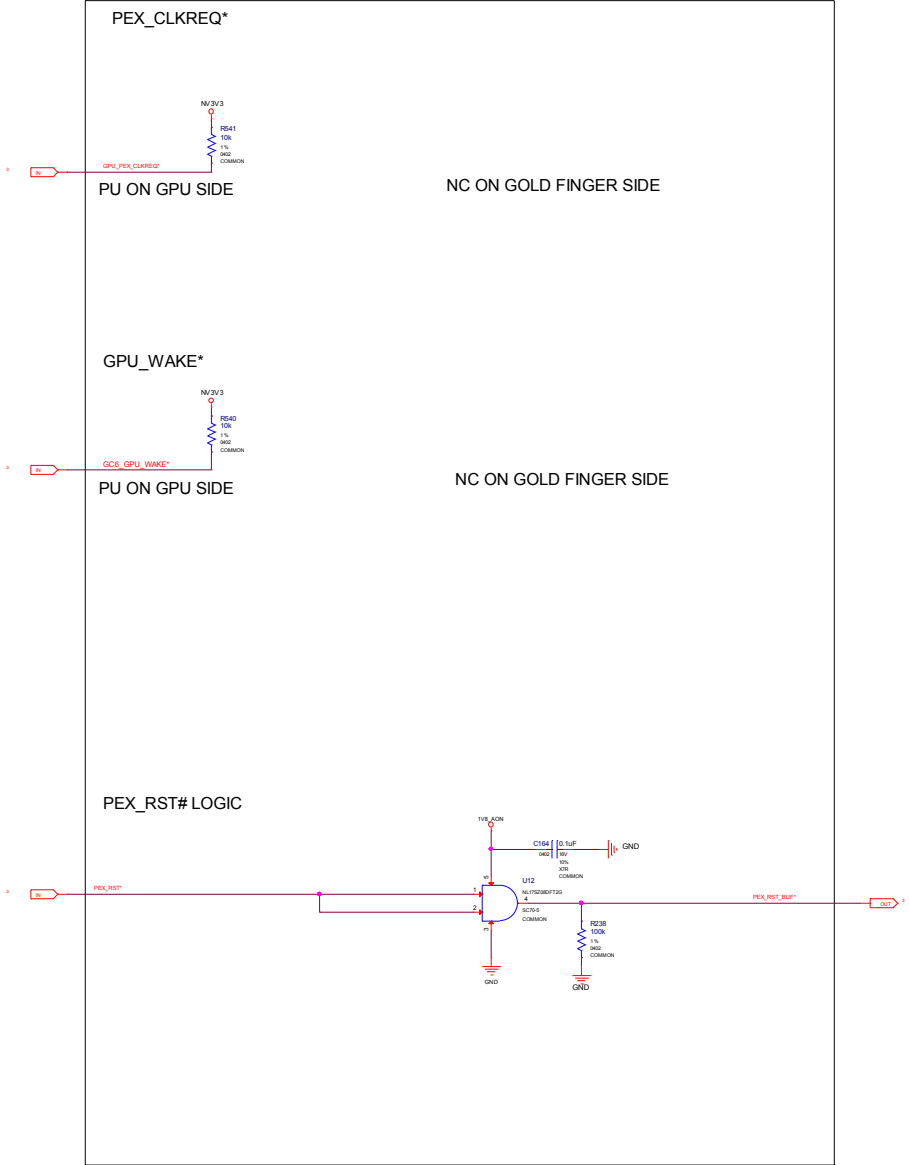
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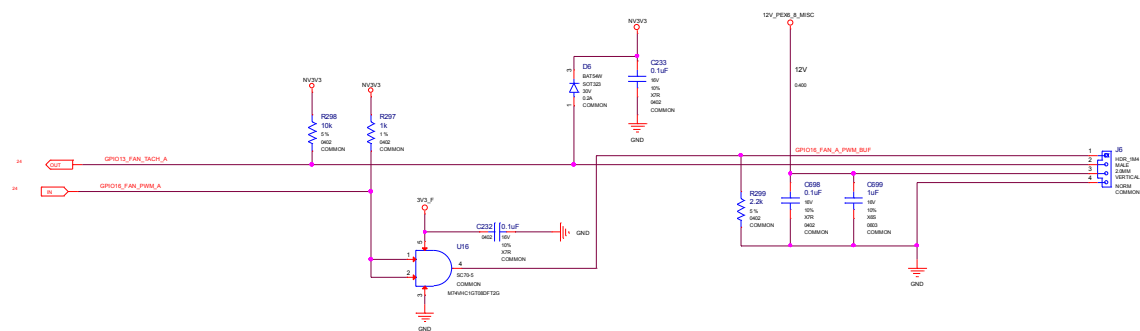
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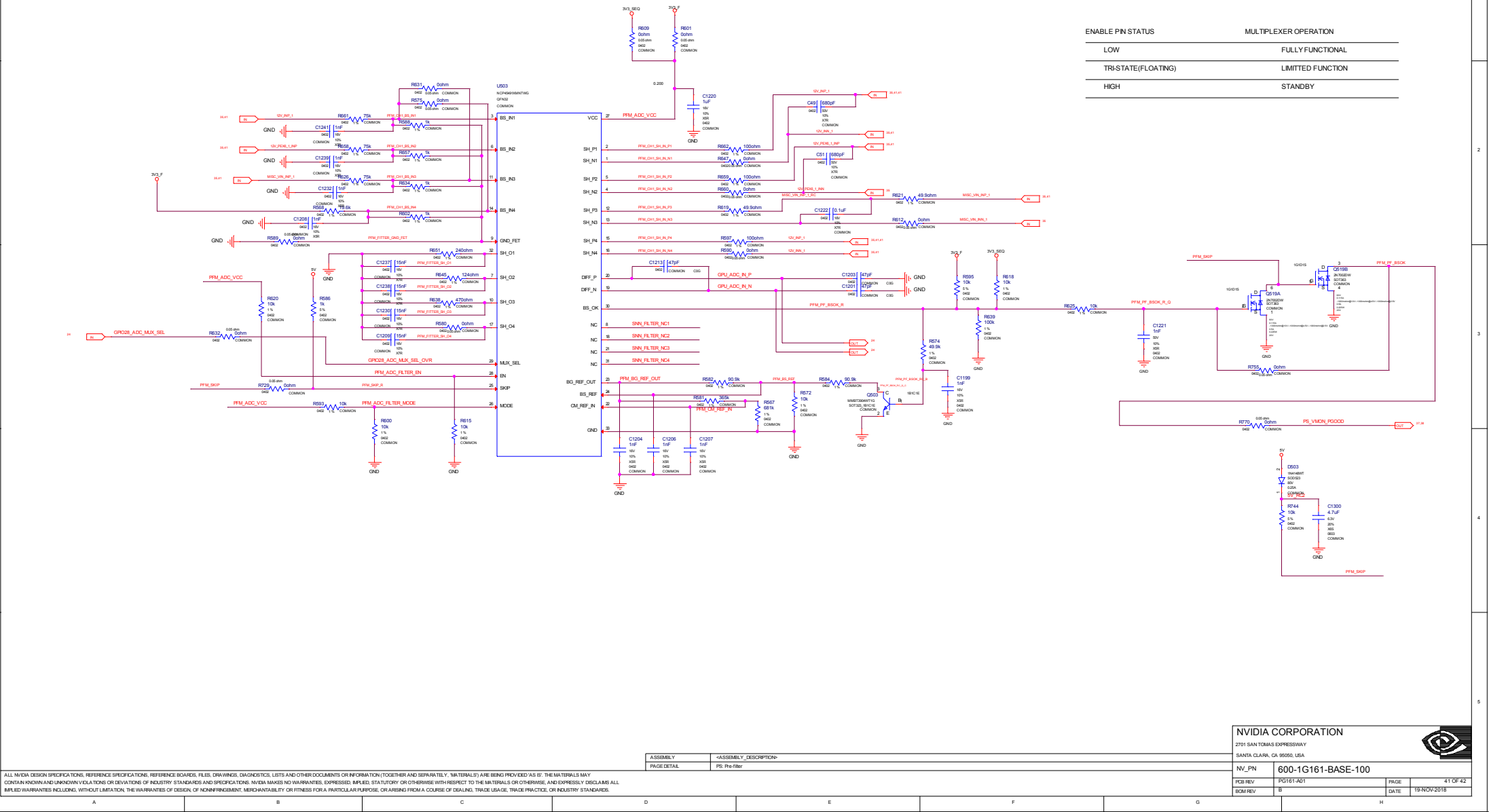
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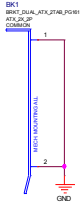
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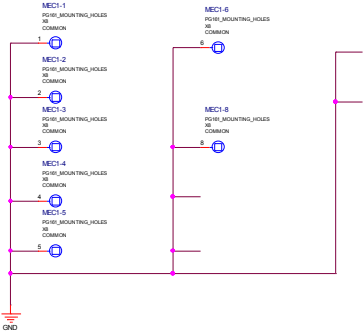




Bracket:



MOUNTING HOLES FOR HS:



BACK STIFFENER:



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