

PG161-A00

TU106 6GB GDDR6, 192b, X16  
DVI-D/DP + DP + HDMI

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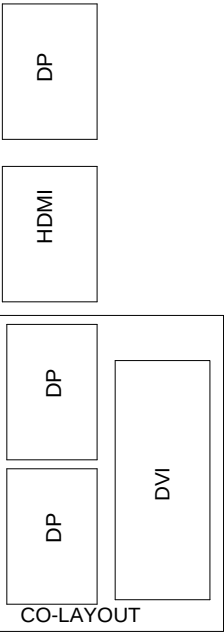
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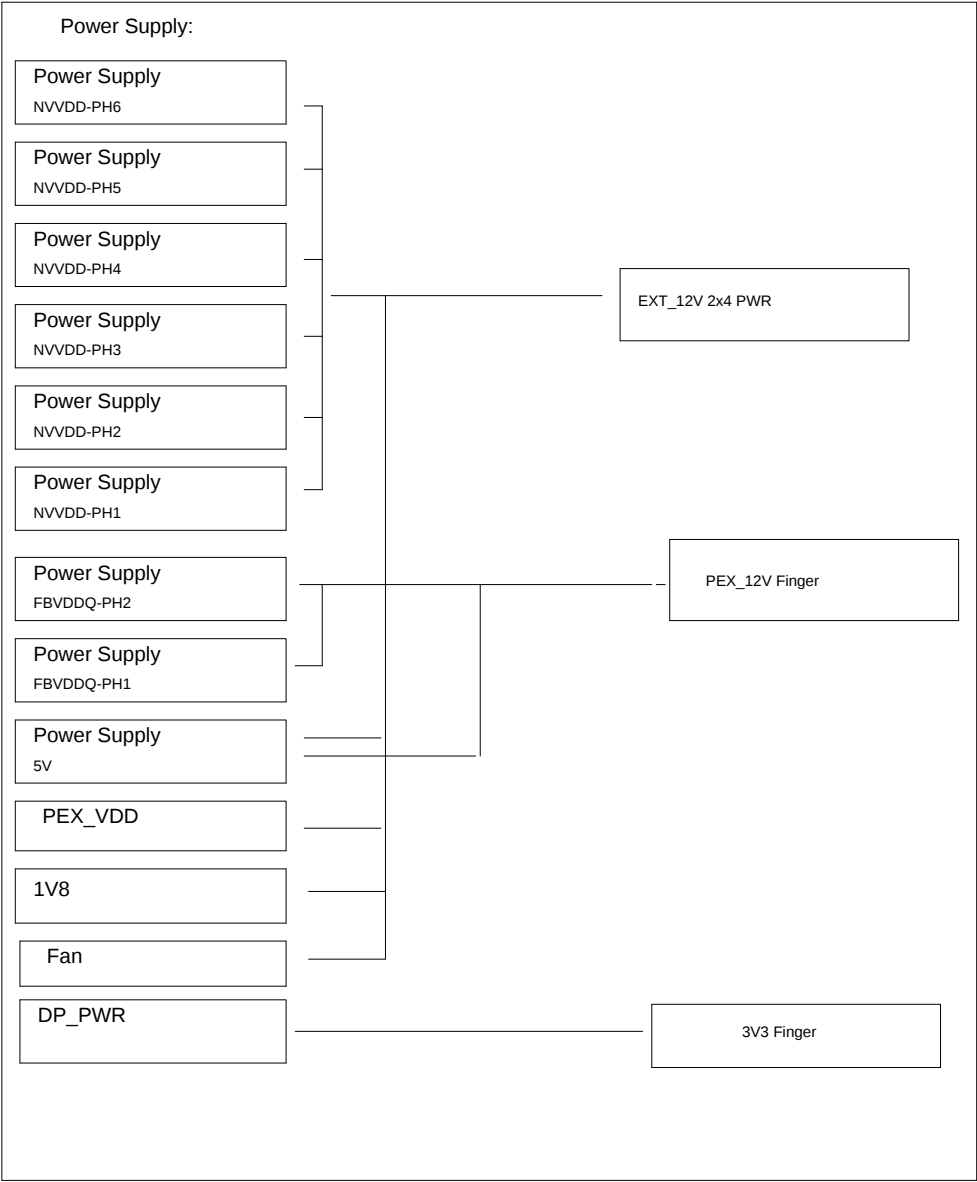
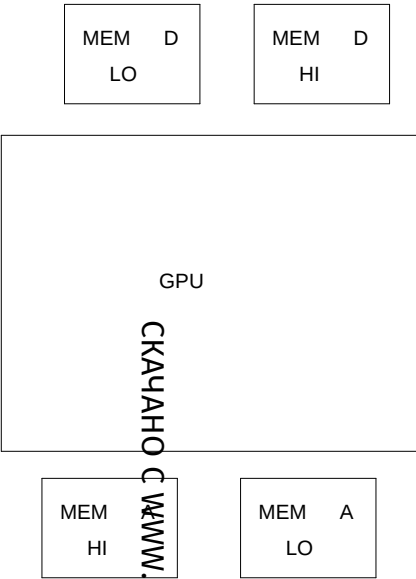
V375-20  
1. 移除 CO-LAY  
2. Core Power: uP9512SQKI (QFN 4x4) + (NCP302045 x 4)  
Memory Power: I32-S725602-N03 + (UBIQ 3103+3107\*2)  
3. P.34 add R779  
4. P.30 & 44 移除PS\_FBVDD\_BUFF換頁符號  
換 FBVDD controller · 換成1PHASE 上1下2  
5. P.52 add HDMI & DVI 螺絲  
6. IO 換常用料 · footprint 不變

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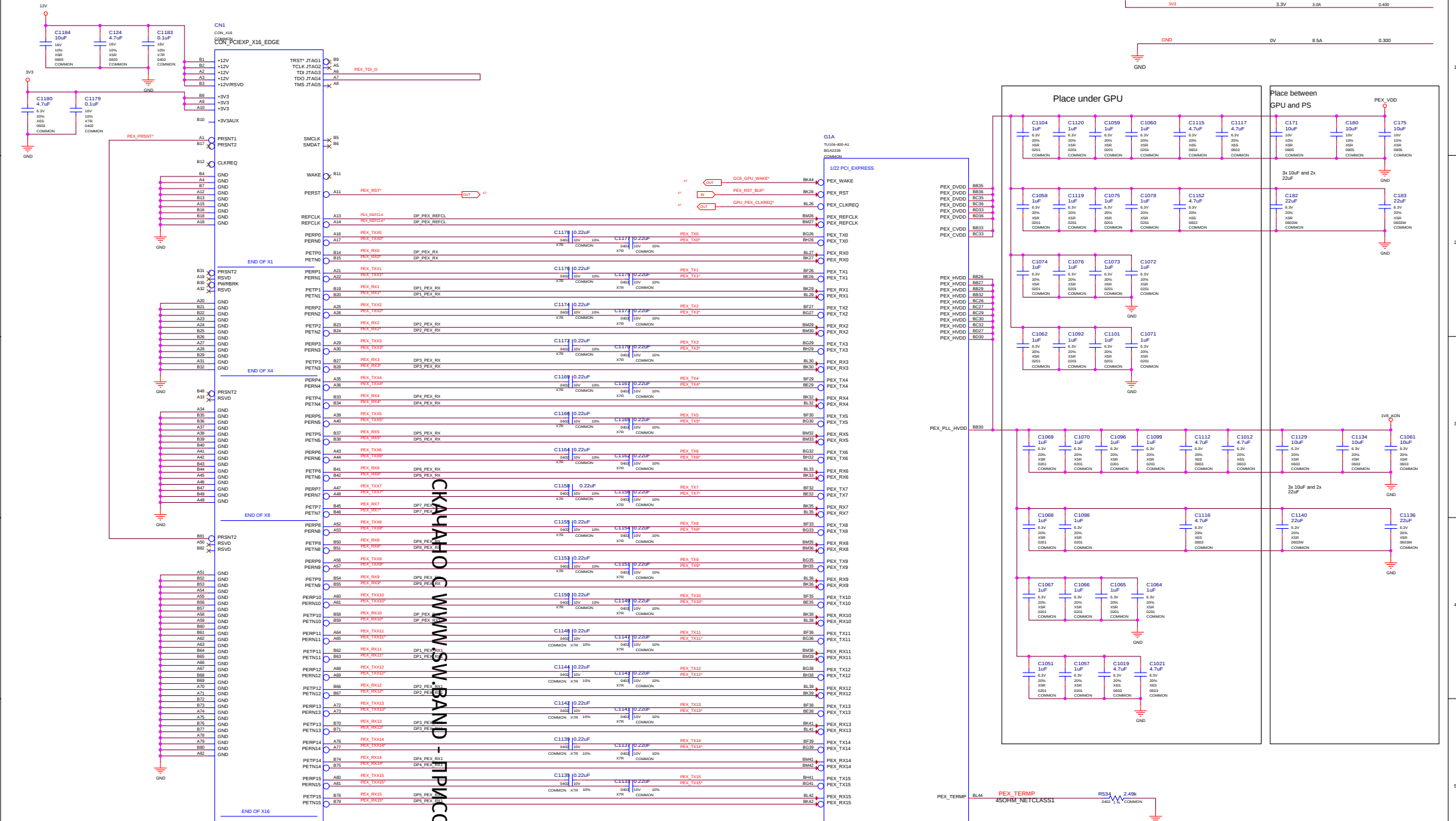
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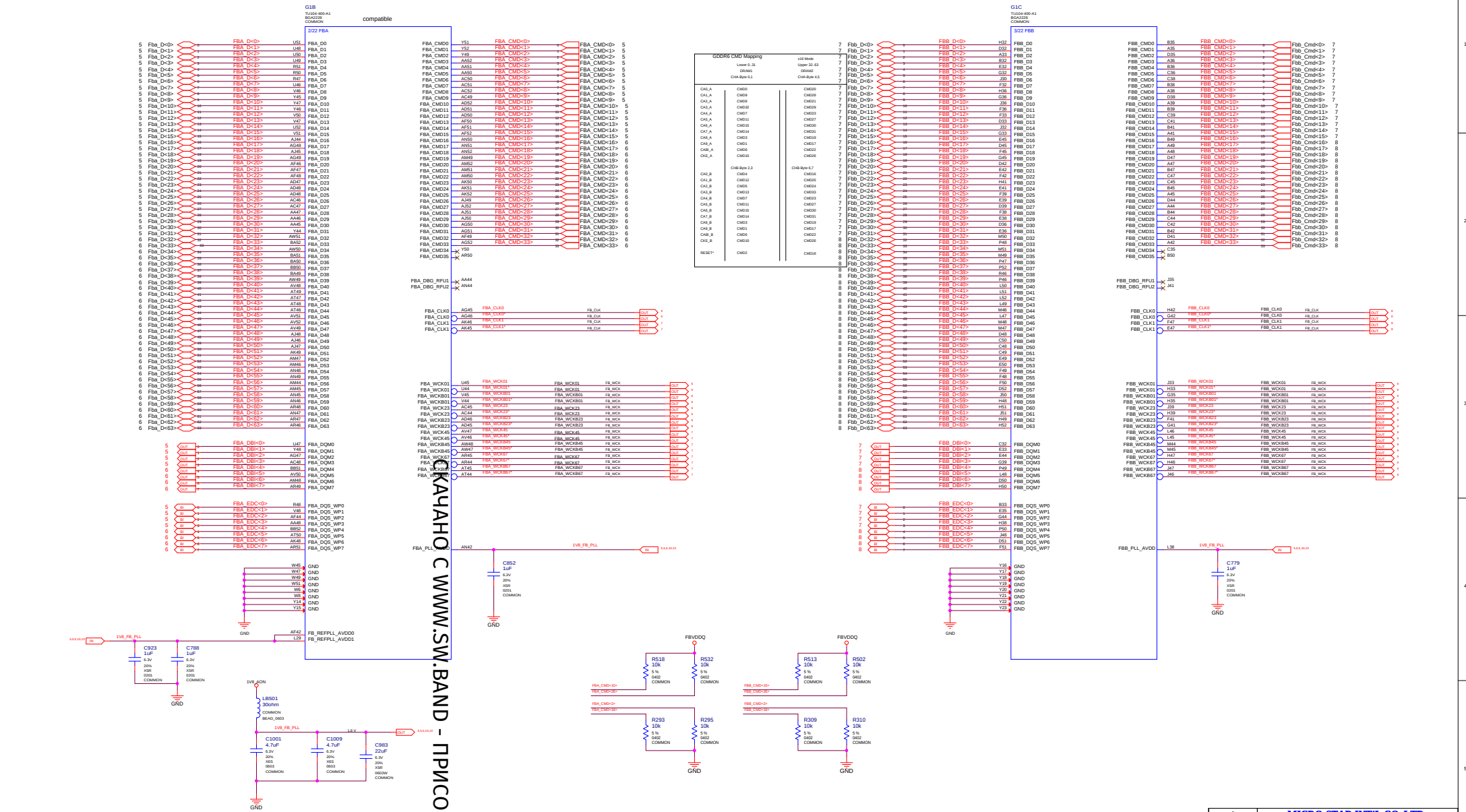
# NO PEX GEN4 SUPPORT DELETE RC TERMINATIONS

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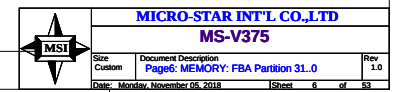
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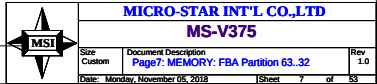


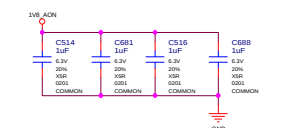
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| MS-V375                         |                      |               |
| Size                            | Document Description | Rev           |
| Custom                          | Page4: PCIE RC TERM  | 1.0           |
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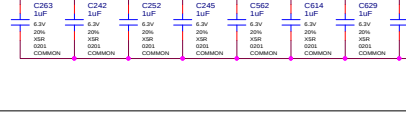
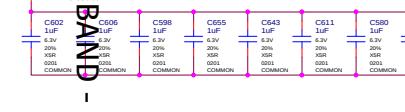
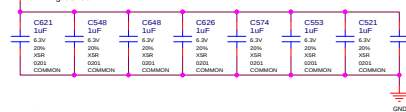
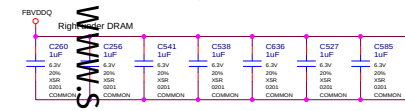
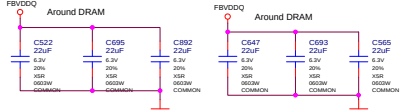
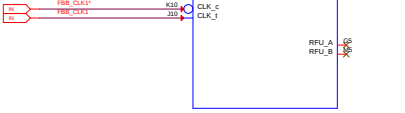
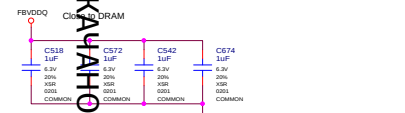
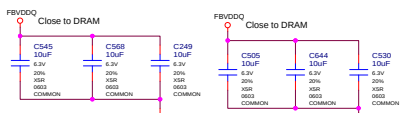
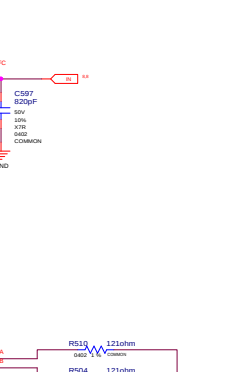
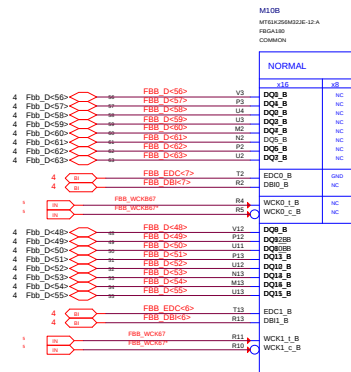
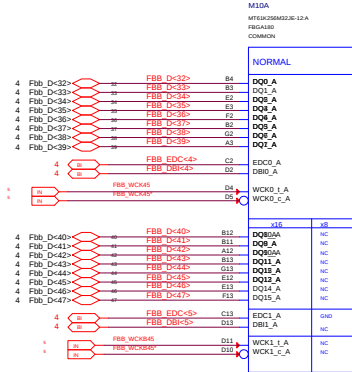
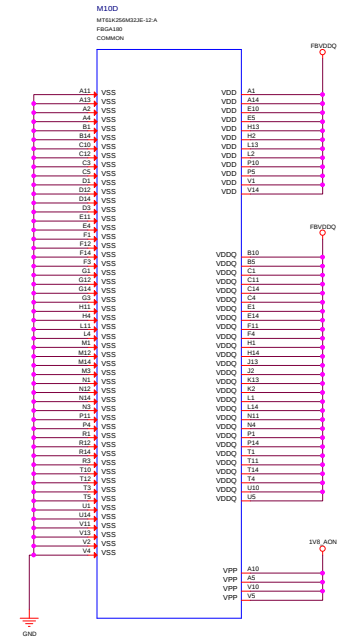


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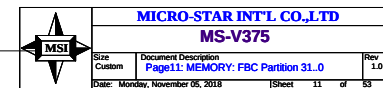


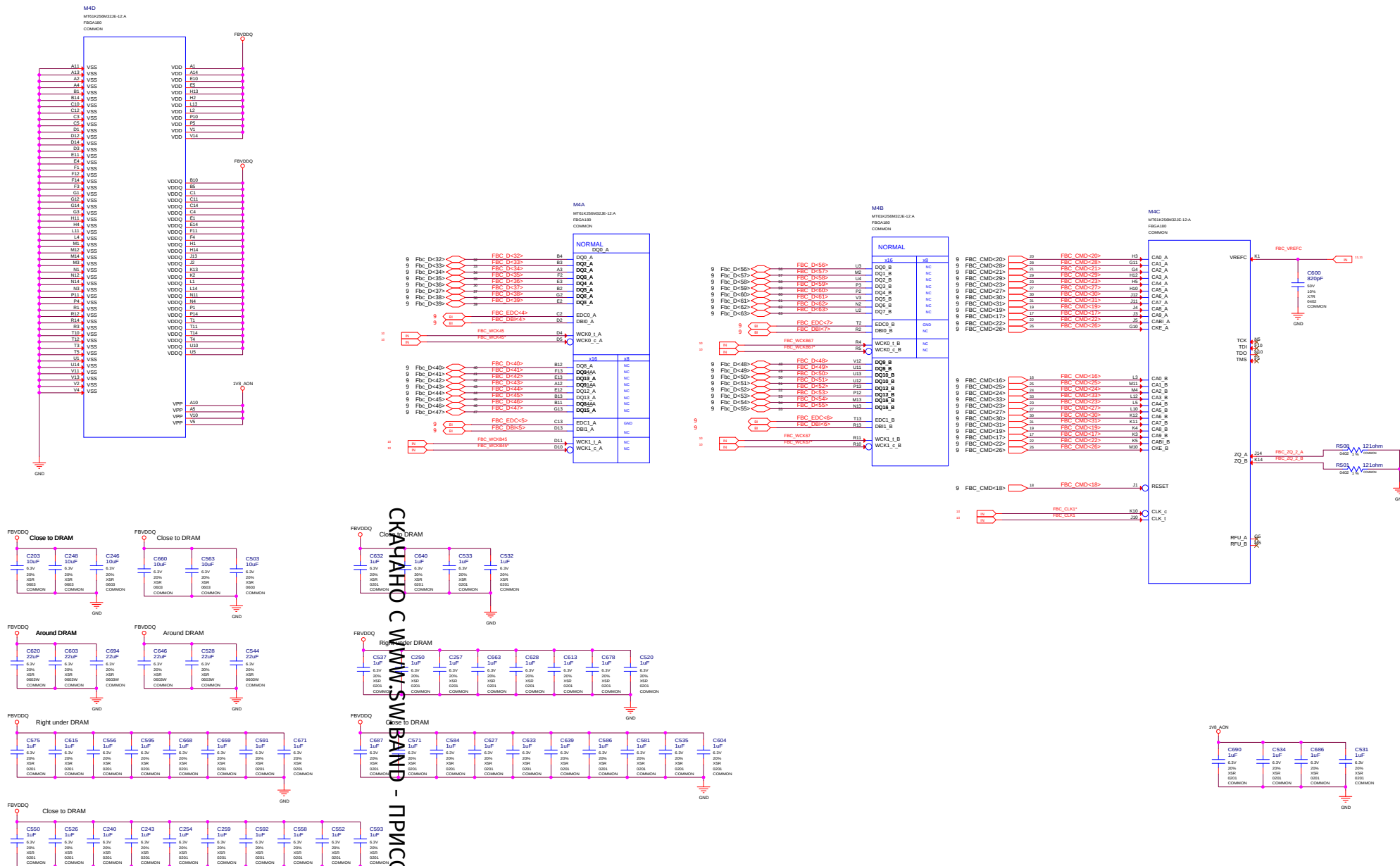




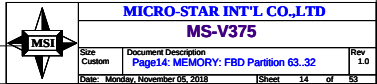
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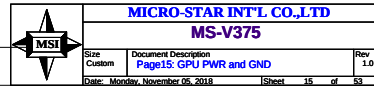




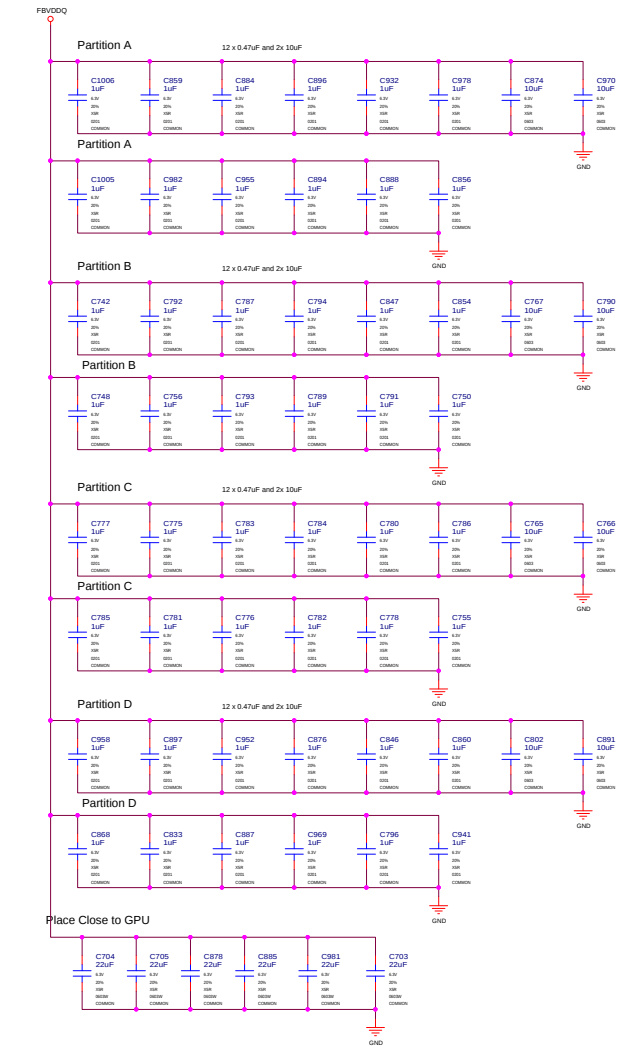




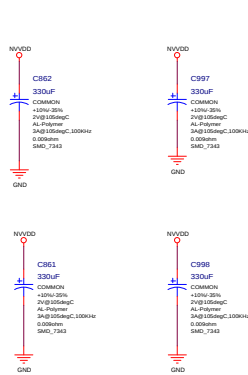




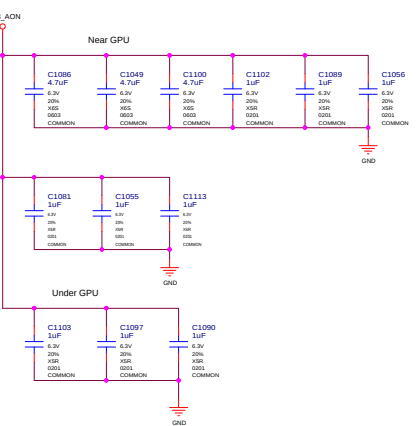
FBVDDQ



NVVD0



1V8\_AON

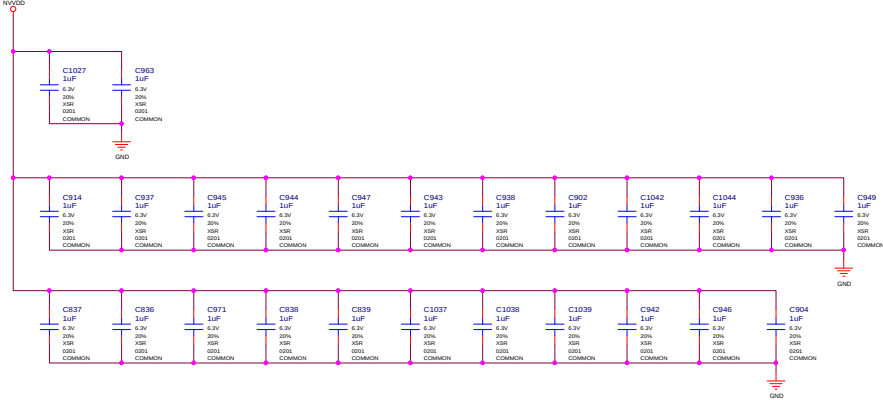
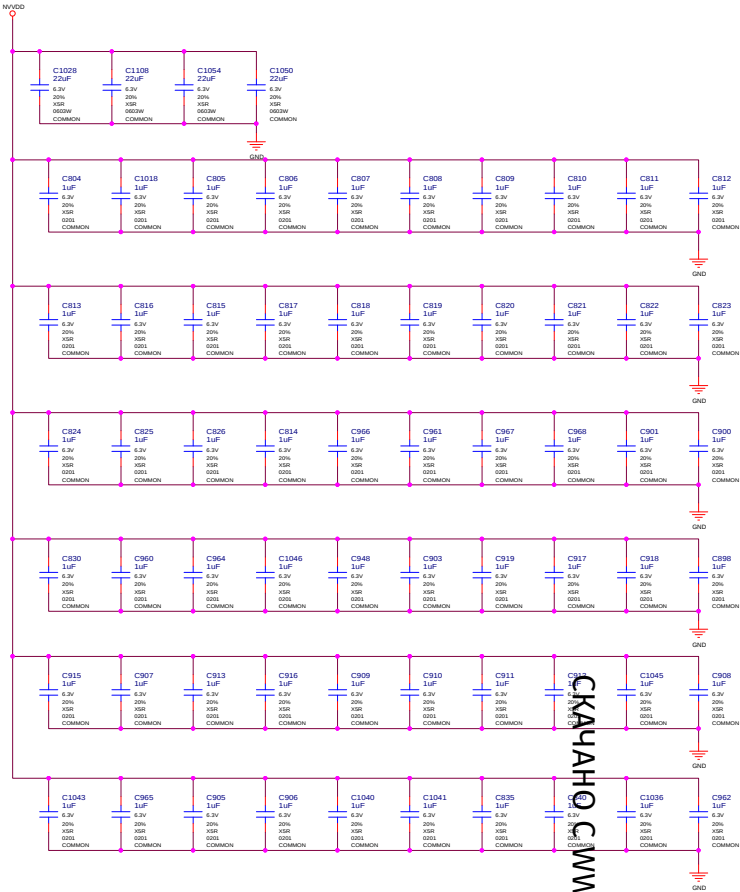


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|                                 | Custom                   | Page16: GPU Decoupling 1 | 1.0 |
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NVDD



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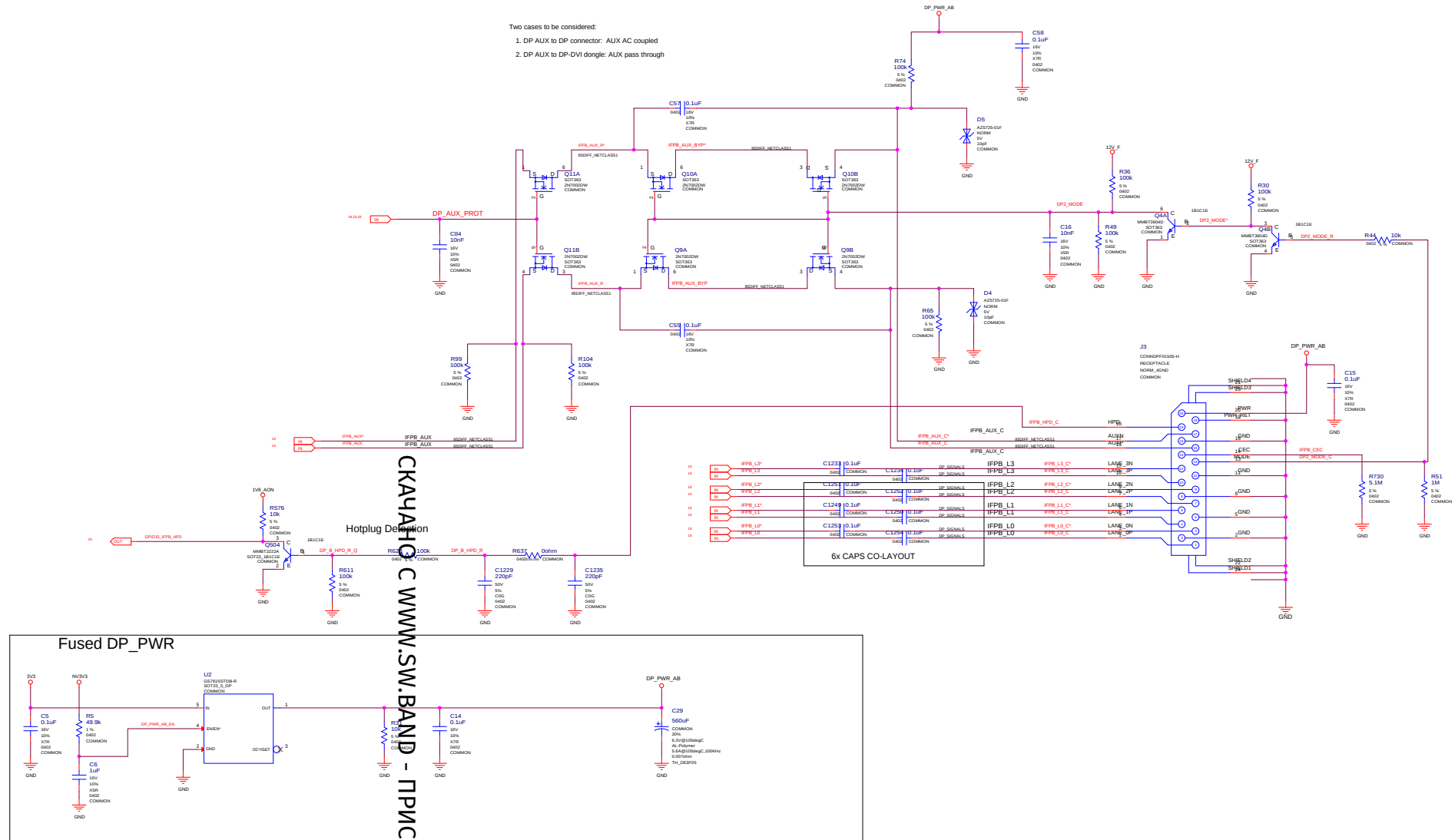
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## IFPB DP CO-LAYOUT WITH IFPAB DVI-D-DL

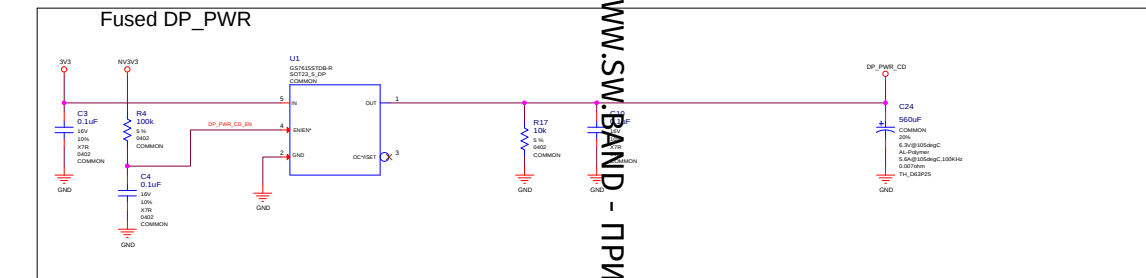
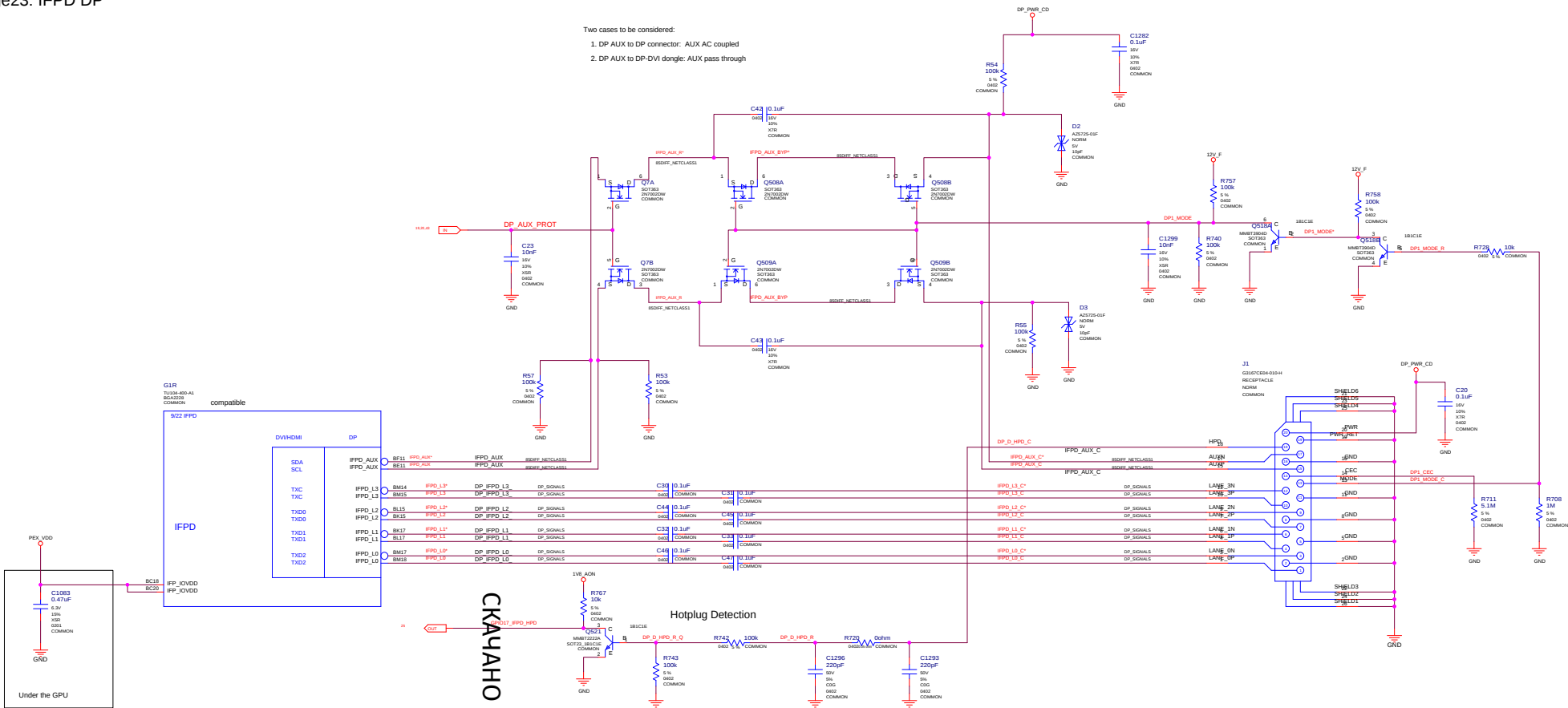


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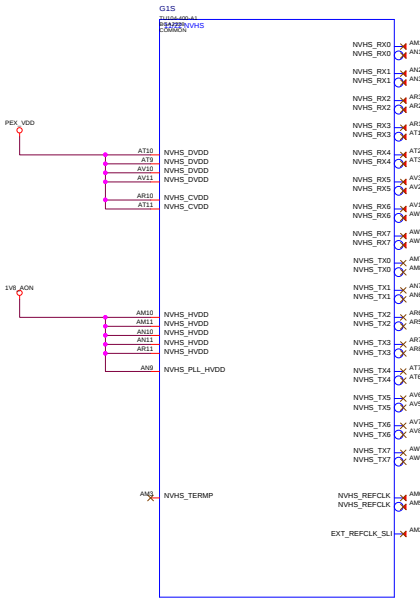




Two cases to be considered:  
1. DP AUX to DP connector: AUX AC coupled  
2. DP AUX to DP-DVI dongle: AUX pass through



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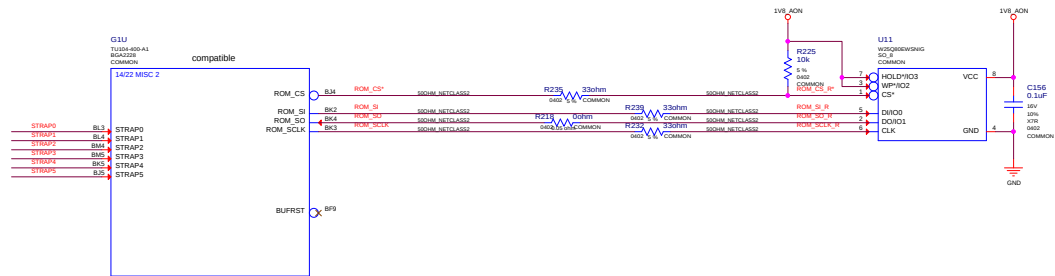
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| Size<br>Custom                  | Document Description<br><b>Page25: MISC1: Fan, Thermal, JTAG, GPIO</b> | Rev<br>1.0 |
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| RAMCFG[4:0] | DENSITY | WIDTH   | VENDOR  | DIE |
|-------------|---------|---------|---------|-----|
| 00000       | 8Gb     | 256-bit | Samsung | C   |
| 00001       | 8Gb     | 256-bit | Micron  | A   |
| 00010       | 8Gb     | 256-bit | Hynix   | M   |
| 00011       | 8Gb     | 256-bit | Samsung | C   |
| 00100       | 8Gb     | 256-bit | Micron  | A   |
| 00101       | 8Gb     | 256-bit | Hynix   | M   |
| 00110       | 16Gb    | 256-bit | Samsung | M   |
| 00111       |         |         |         |     |

| STRAP5 | STRAP4 | STRAP3 | SMB_ALT_ADDR | DEVID_SEL              | PCIE_CFG | VGA_DEVICE |
|--------|--------|--------|--------------|------------------------|----------|------------|
| M      | H      | H      | 1            | 1                      | 1        | 1          |
| M      | H      | L      | 1            | ЧКАУАНО°C WWWW.SW.BAND | 1        | 0          |
| M      | L      | H      | 1            |                        | 0        | 1          |
| M      | L      | L      | 1            |                        | 0        | 0          |
| L      | H      | M      | 1            |                        | 1        | 1          |
| L      | M      | H      | 1            |                        | 1        | 0          |
| L      | M      | L      | 1            |                        | 0        | 1          |
| L      | L      | M      | 1            |                        | 0        | 0          |
| H      | H      | H      | 0            |                        | 1        | 1          |
| H      | H      | L      | 0            |                        | 1        | 0          |
| H      | L      | H      | 0            |                        | 0        | 1          |
| H      | L      | L      | 0            |                        | 0        | 0          |
| L      | H      | H      | 0            |                        | 1        | 1          |
| L      | H      | L      | 0            |                        | 1        | 0          |
| L      | L      | H      | 0            |                        | 0        | 1 DEFAULT  |
| L      | L      | L      | 0            |                        | 0        | 0          |

```
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

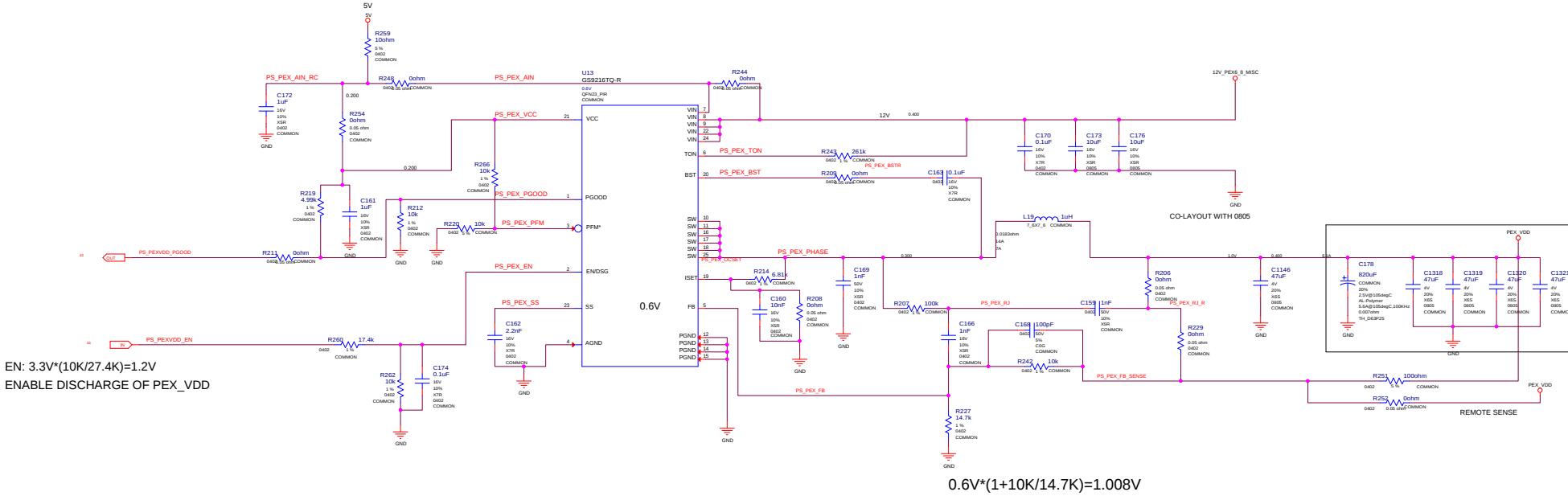
1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE
```







PEX Switcher

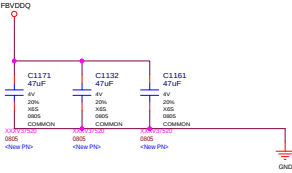
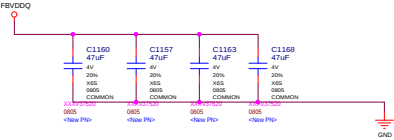
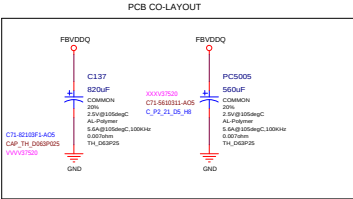


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| Custom                          | Page31: PS: FBVDDQ PH2 | 1.0            |  |
| Date: Monday, November 05, 2018 |                        | Sheet 31 of 53 |  |



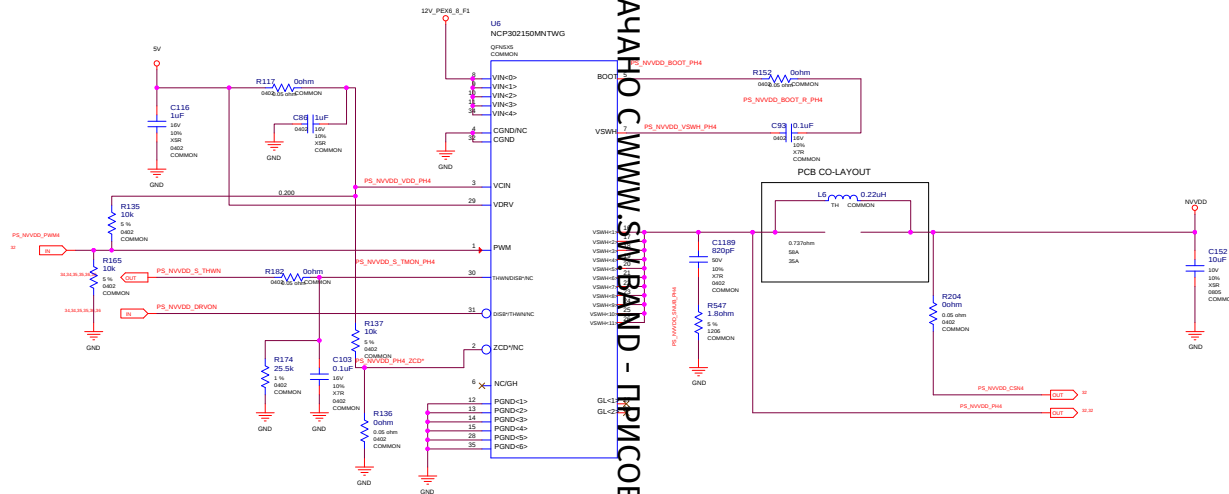
DROP OVR3i SOLUTION

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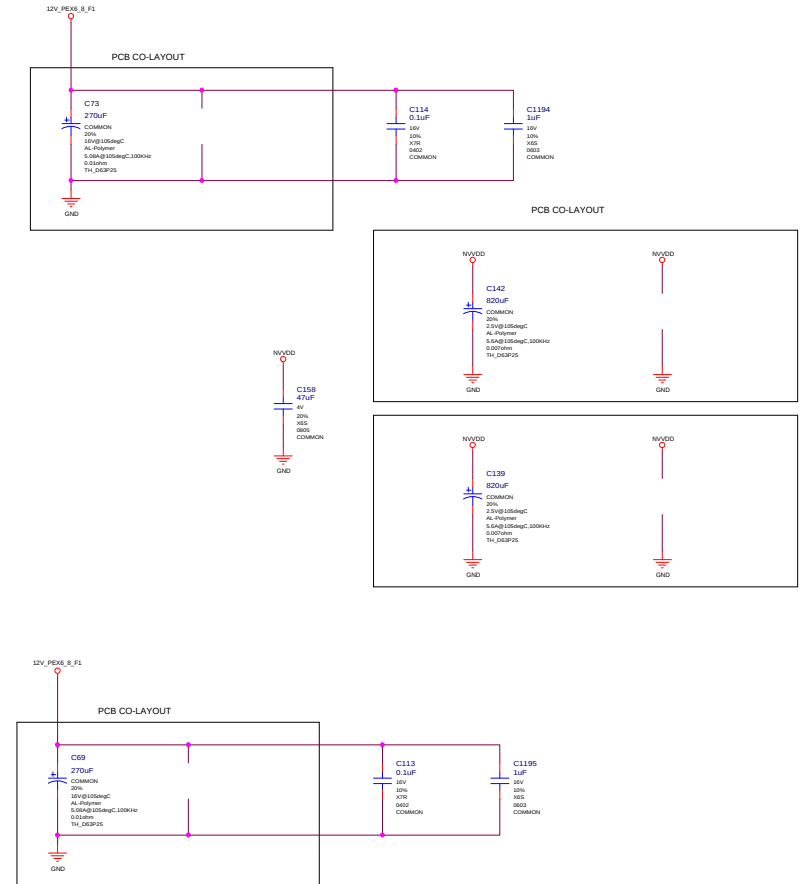


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| Custom                          | Page33: PS: NVVDD CTR OVR3i | 1.0            |
| Date: Monday, November 05, 2018 |                             | Sheet 33 of 53 |

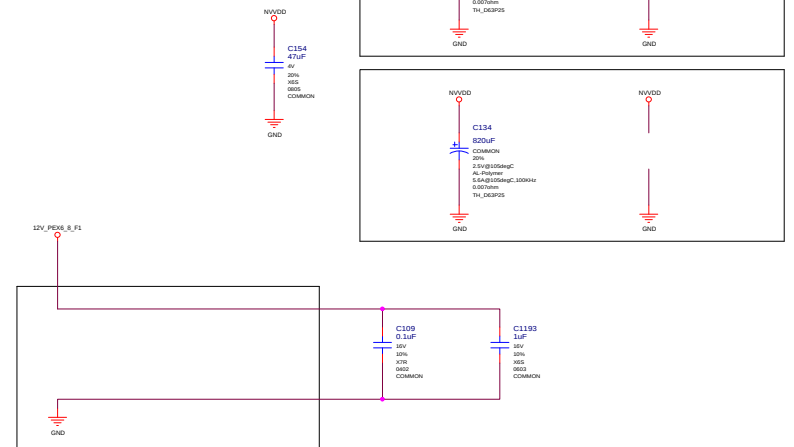
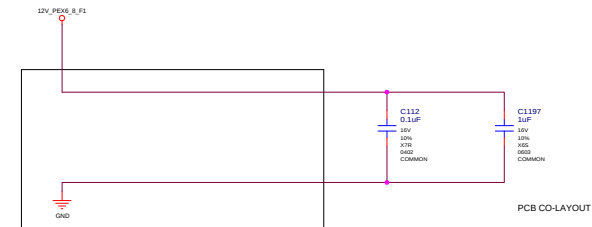


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| Date: Monday, November 05, 2018 | Sheet 26 of 53                                            |            |

DELETE RAIL BALANCE CIRCUIT

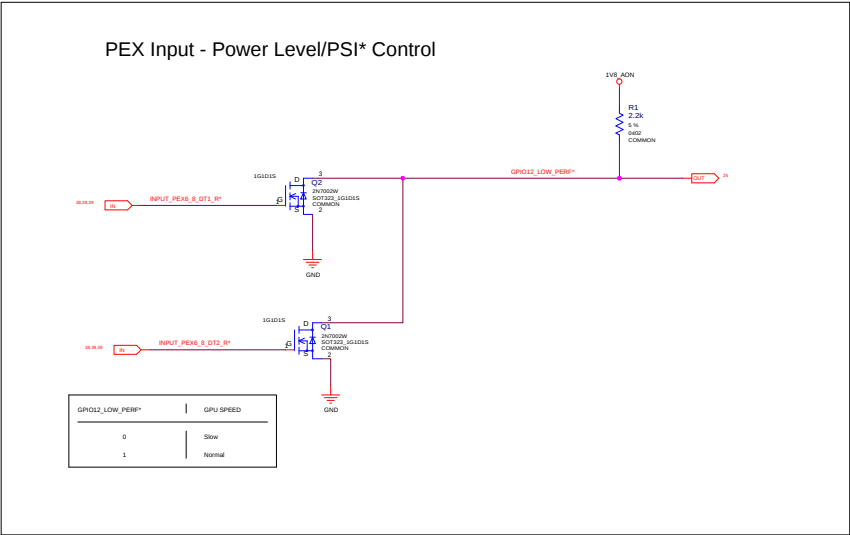
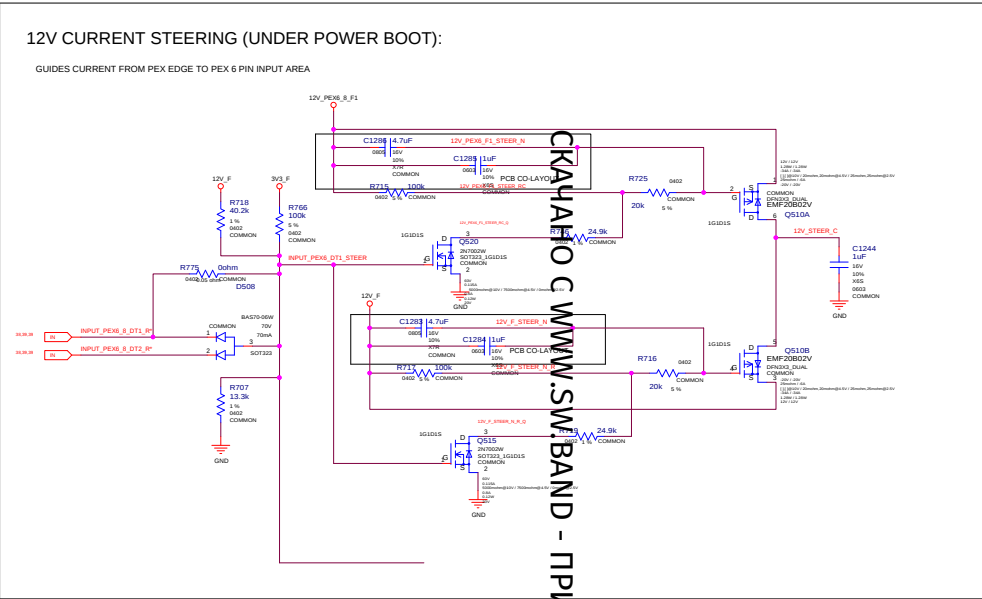
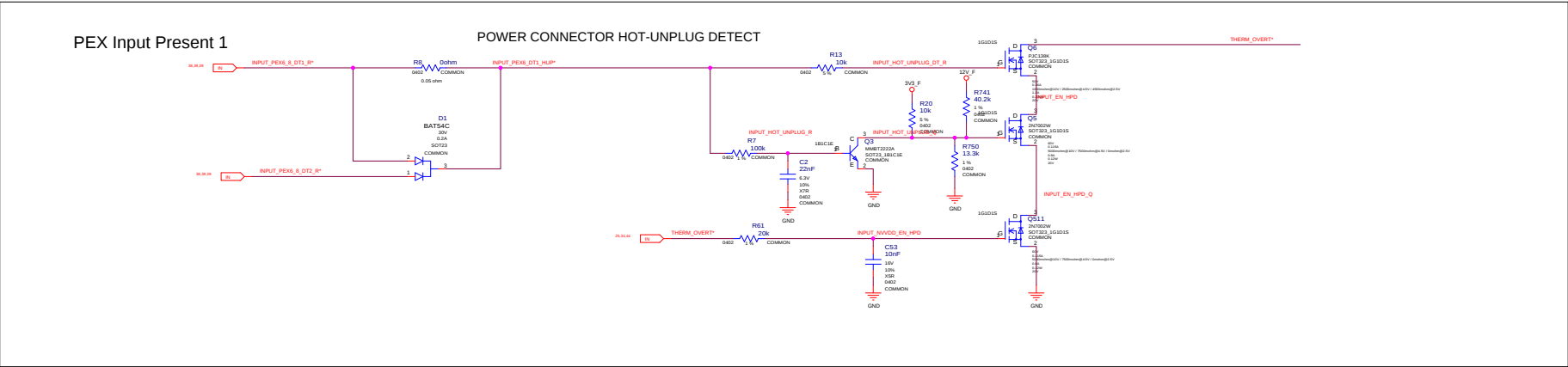
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| Custom                          | Page37: Input Power Balancing Switcher | 1.0            |
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| Custom                          | Page40: BUCKBOOST    | 1.0            |
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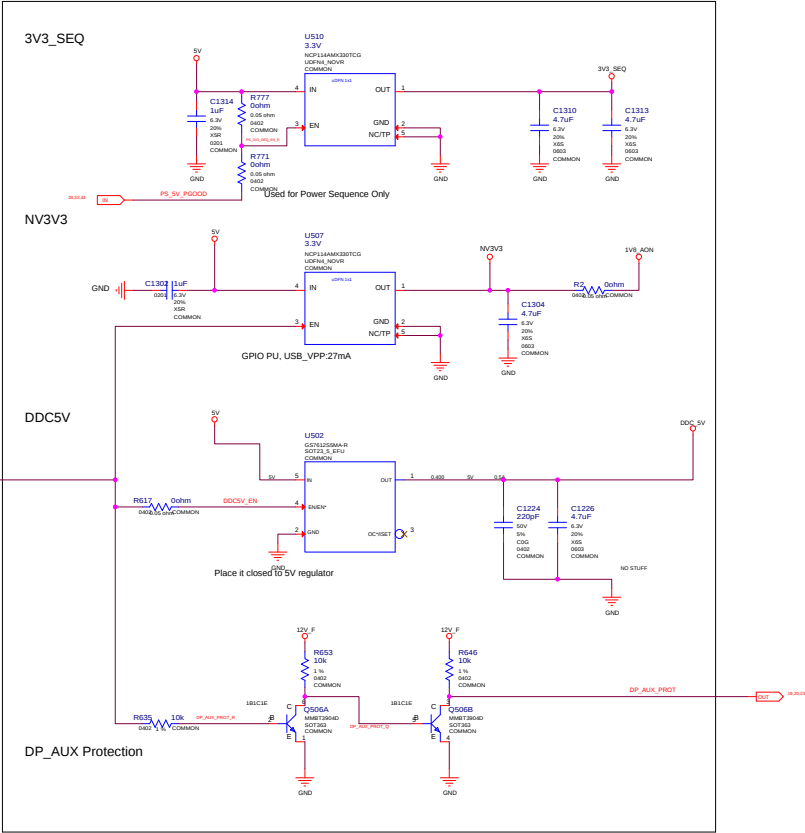
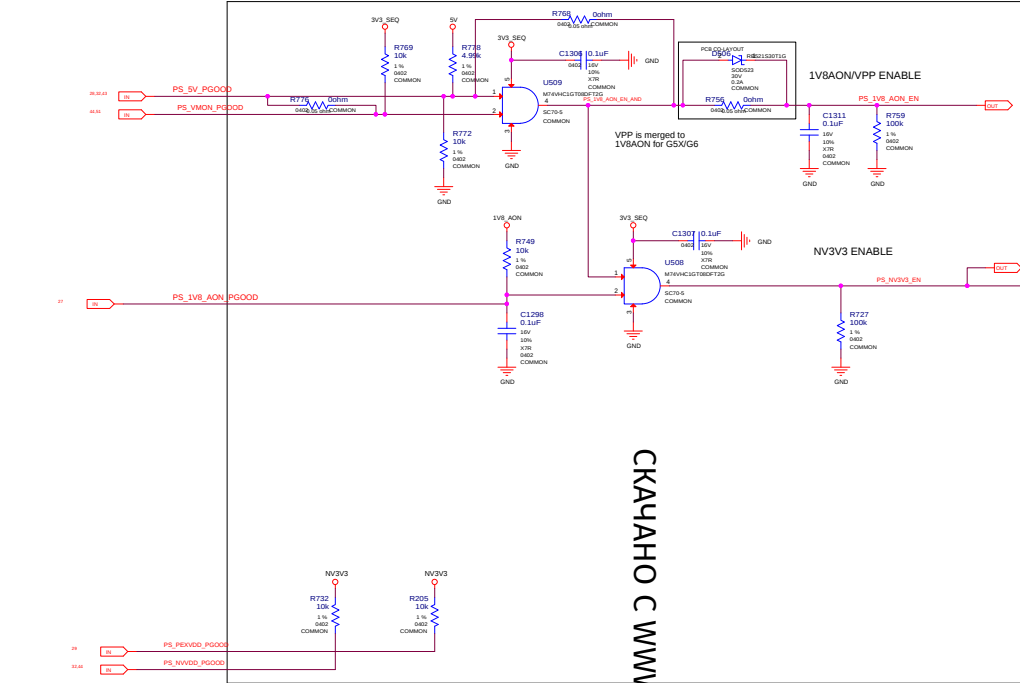
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| Custom                          | Page41: PPC          | 1.0            |
| Date: Monday, November 05, 2018 |                      | Sheet 41 of 53 |

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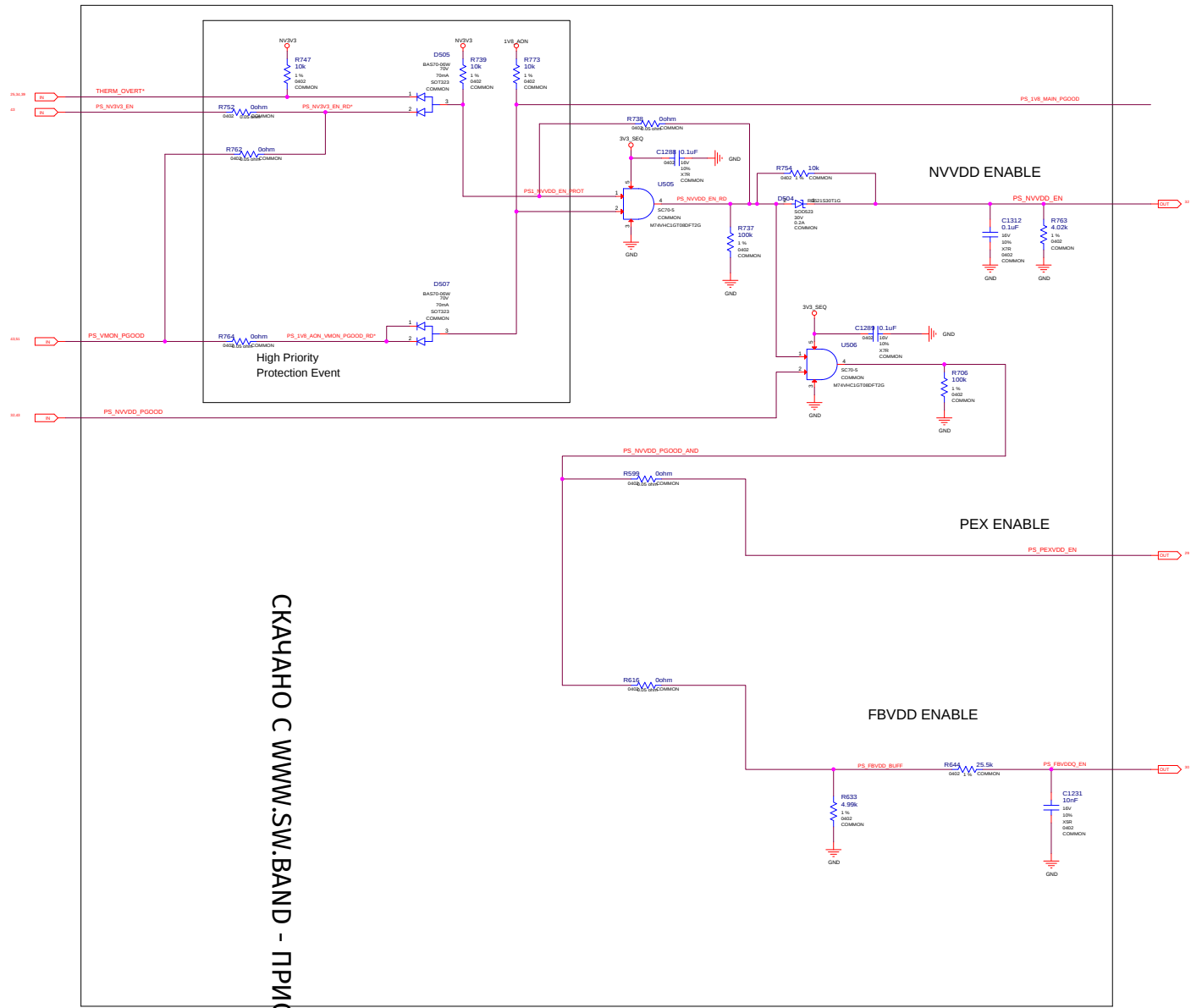
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| Custom                          | Page42: 12V & 3V3_A SWITCHER | 1.0            |
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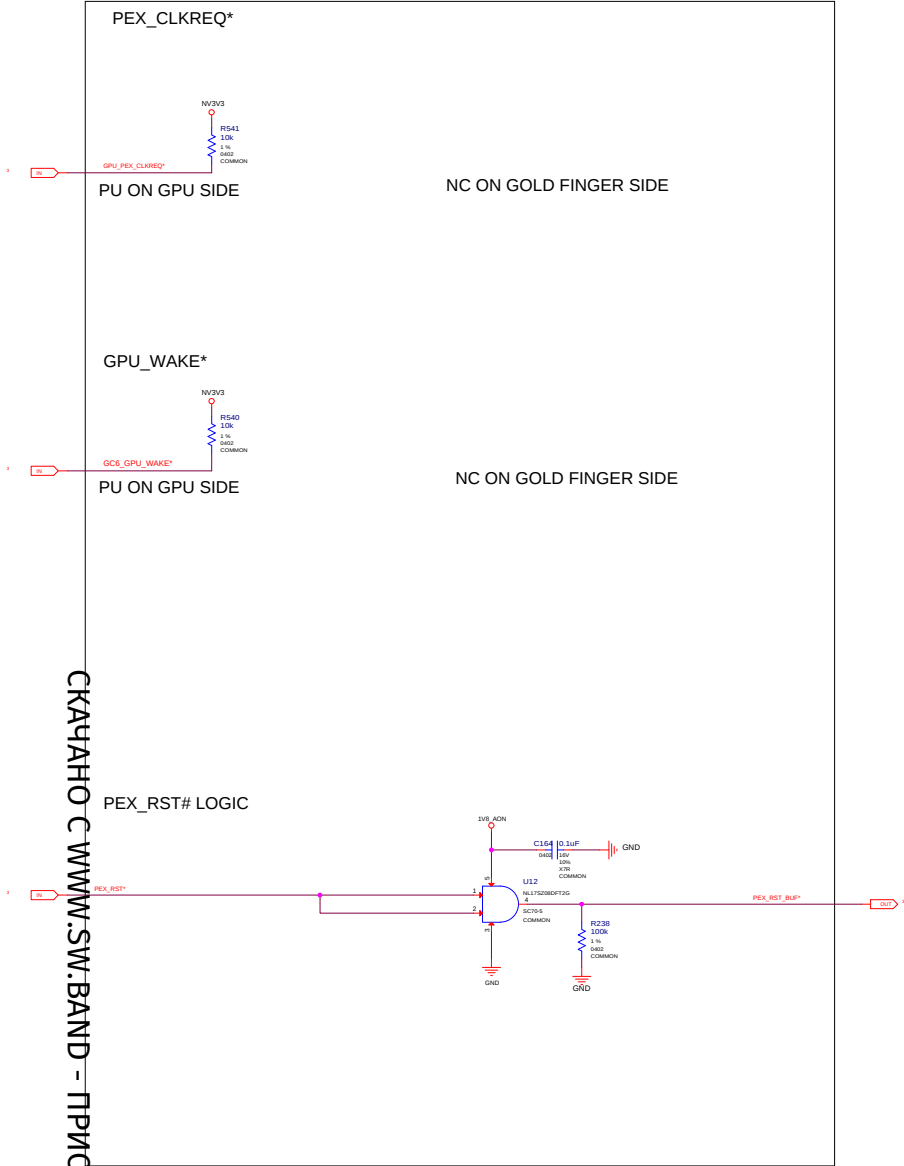
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| Custom                          | Page45: SEQUENCE:Voltage Monitor |                | 1.0 |
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| Custom                          | Page46: SEQUENCE:Discharge |          | 1.0   |
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| Custom                          | Page48: LOGO LED     | 1.0            |
| Date: Monday, November 05, 2018 |                      | Sheet 48 of 53 |

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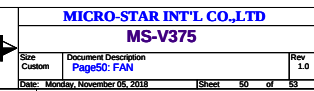
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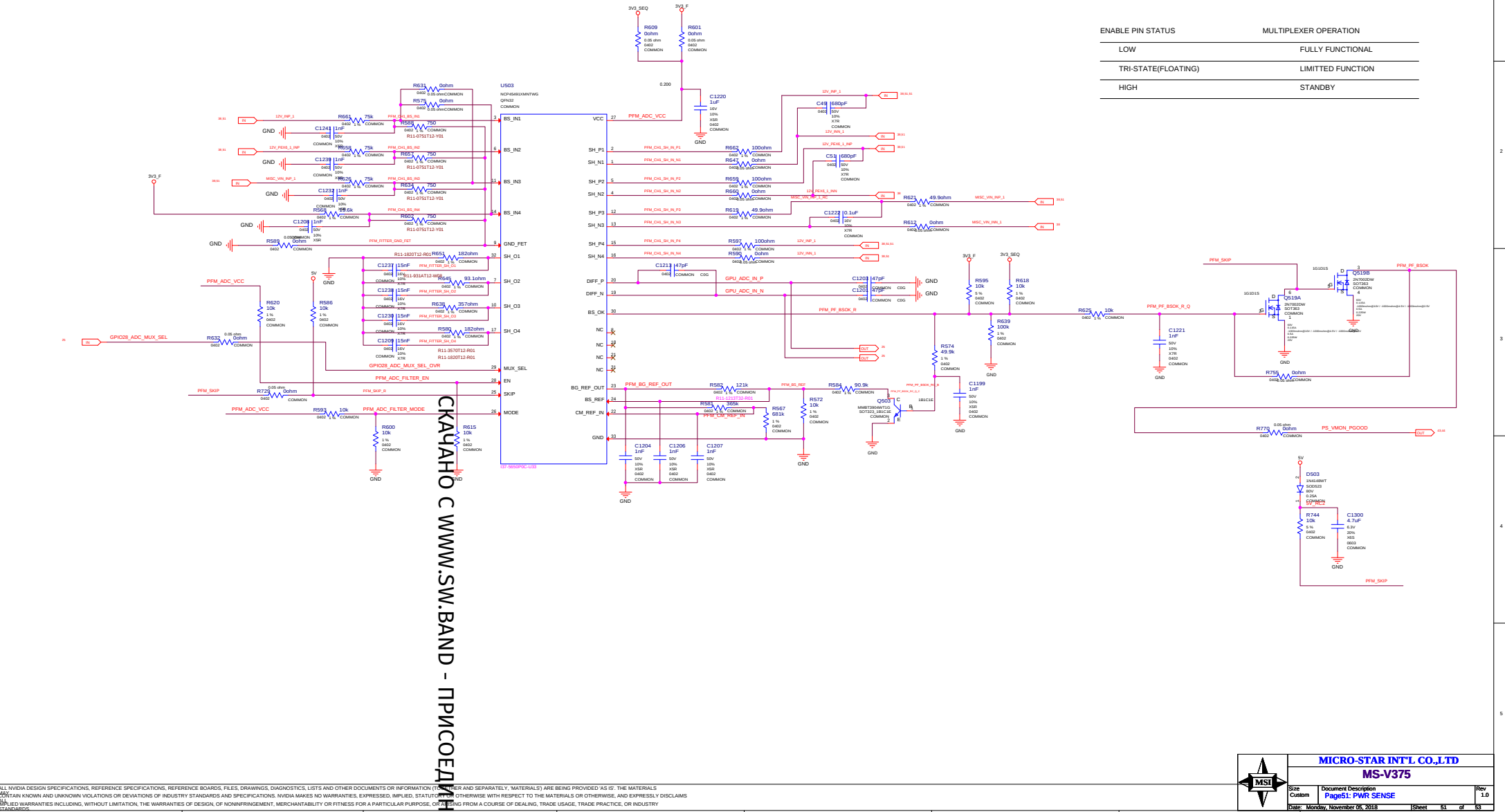


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| Custom                          | Page49: LED DRIVER & Boost |                | 1.0 |
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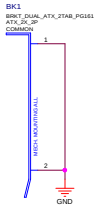
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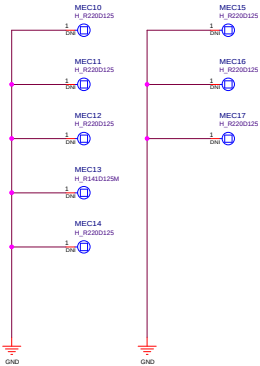


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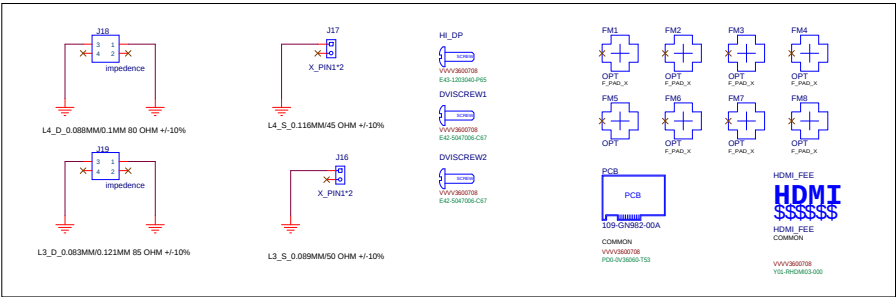
Bracket:



Mechanical Holes Symbol



BACK STIFFENER:



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| Custom                          | Page53: PTC          |       | 1.0      |
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