

PG174-A01

TU117, 128b GDDR5 x32, 75W
DVI-D DL/DUAL DP + HDMI

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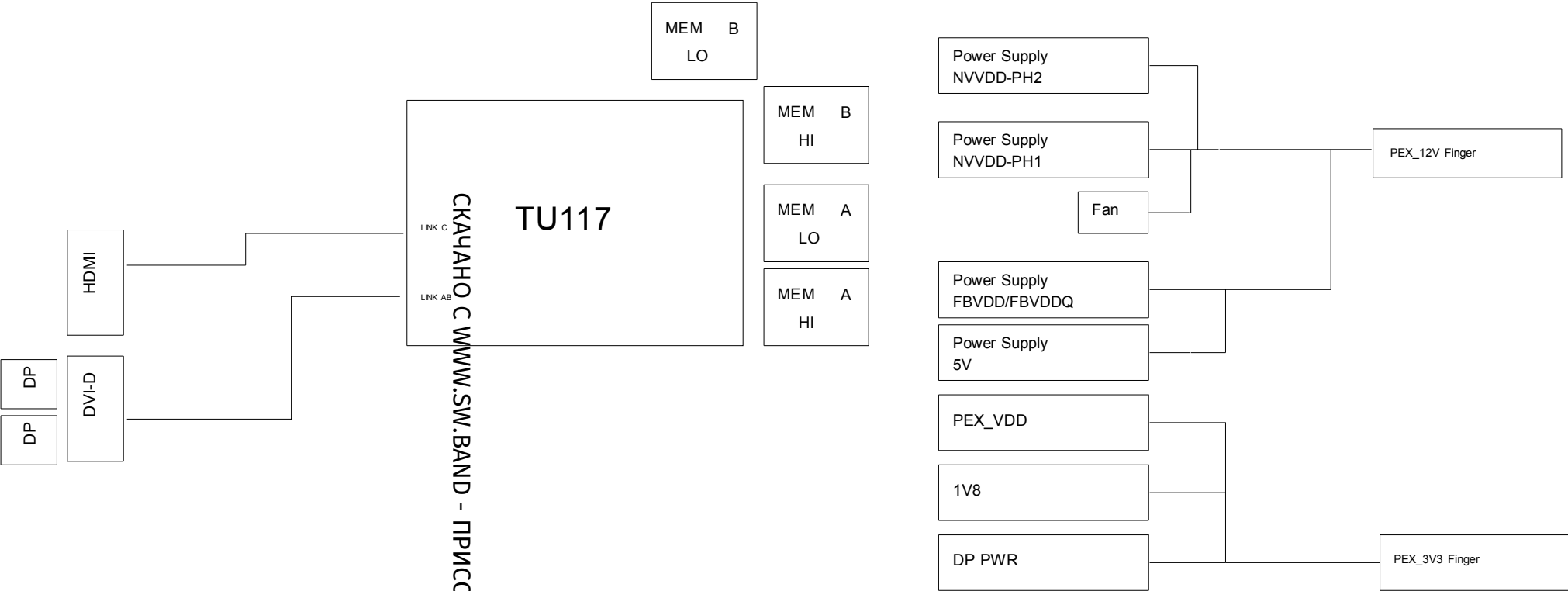
2701 SAN TOMAS EXPRESSWAY

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NV_PN	600-1G174-BASE-A01	PKG#	1 OF 32
PCB REV	PG174-A01	DATE	10-DEC-2018
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Block Diagram



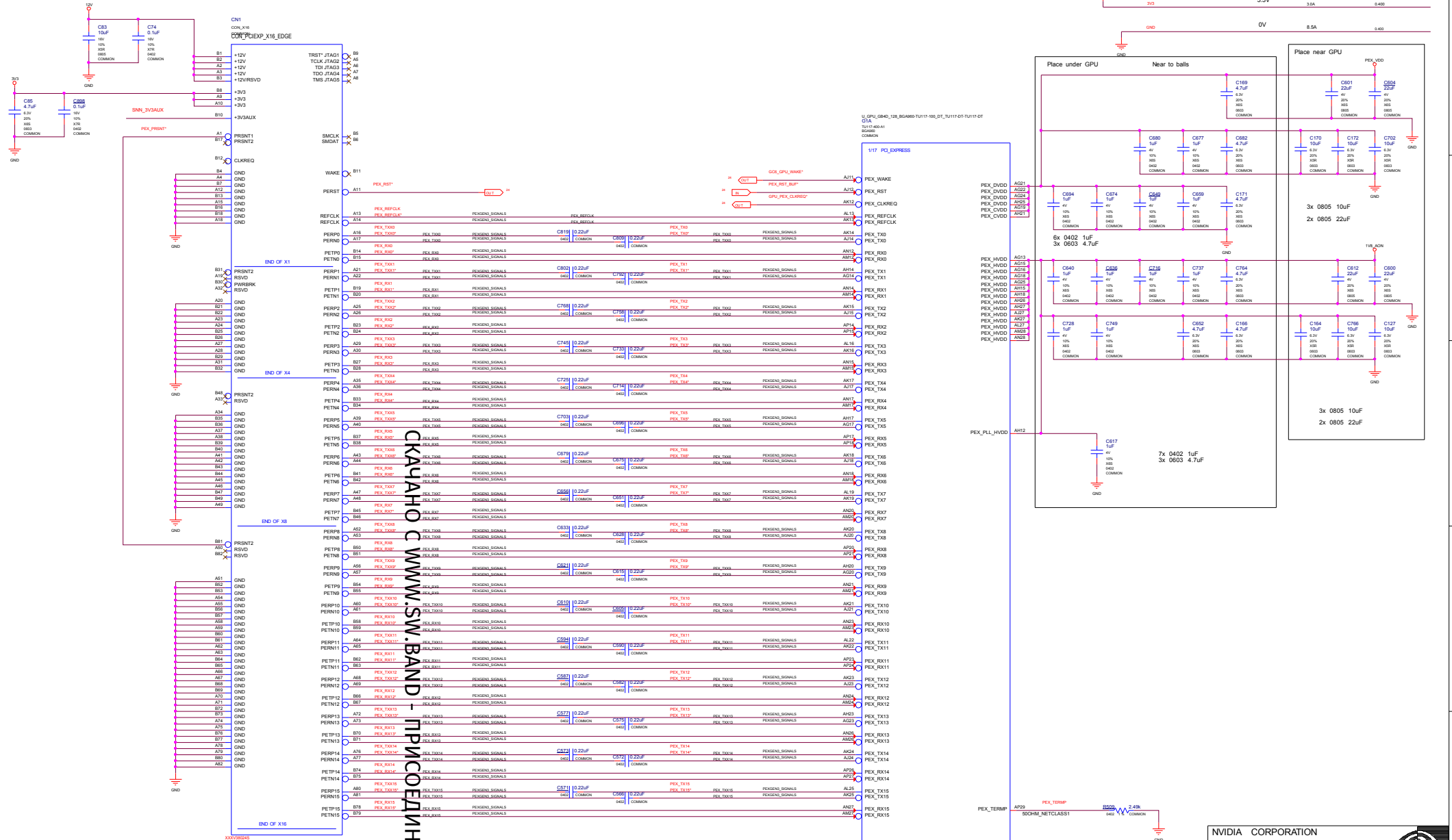
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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PCB DETAIL	Block Diagram

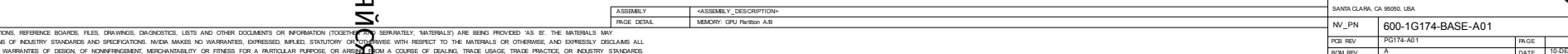
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NV_PN		600-1G174-BASE-A01	
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PCI Express

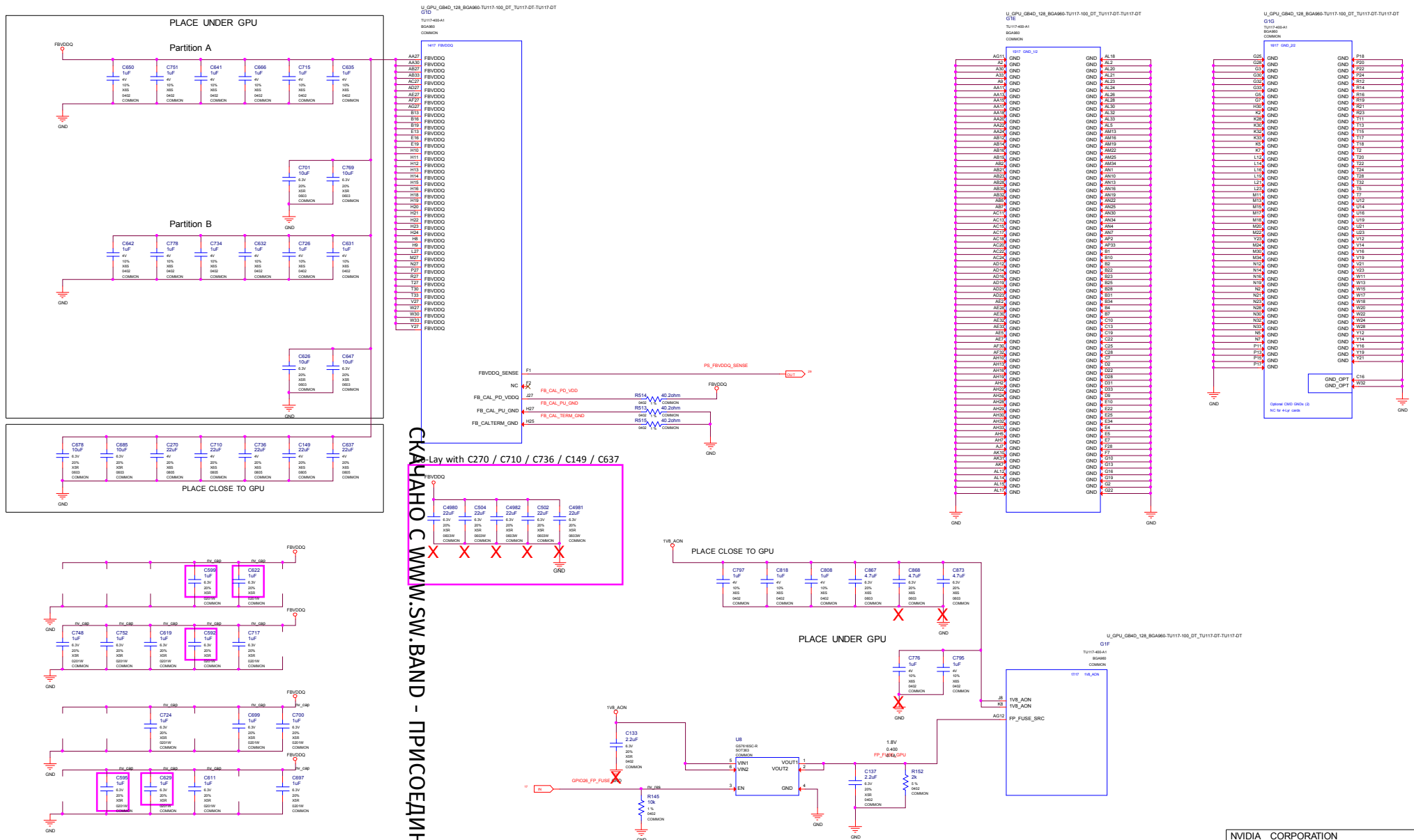


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PCB DETAIL		PC Express		NW_PN		600-1G174-BASE-A01	
REV		REV		PCB REV		3 OF 32	
SANTA CLARA ALL STANDARDS		SANTA CLARA ALL STANDARDS		PCB REV		DATE	
				A		10-DEC-2018	

[illegible]

GPU FBVDDQ & 1V8 Decoupling



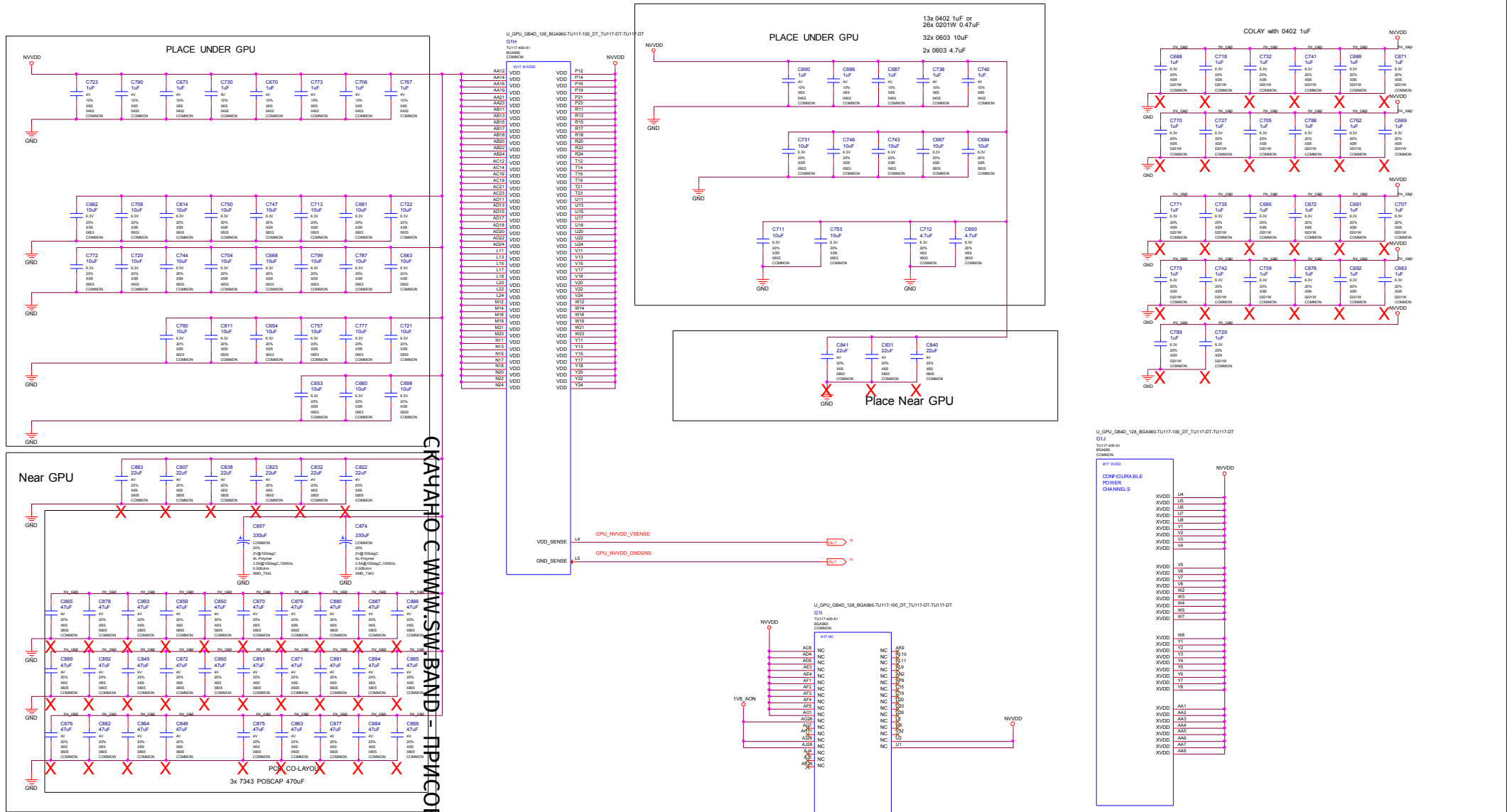
Layer with C270 / C710 / C736 / C149 / C637

Diagram illustrating the connection of capacitors (C49B0, C504, C49B2, C502, C49B1) to the BVDDQ0 signal line. The capacitors are connected to a common ground plane (GND). The diagram also shows a 1V8_AC signal line and a GND_FUSE connection.

Capacitor	Value	Voltage	Signal	Common
C49B0	22uF	6.3V	BVDDQ0	COMMON
C504	22uF	6.3V	BVDDQ0	COMMON
C49B2	22uF	6.3V	BVDDQ0	COMMON
C502	22uF	6.3V	BVDDQ0	COMMON
C49B1	22uF	6.3V	BVDDQ0	COMMON

ASSEMBLY	ASSEMBLY DESCRIPTION	SANTA CLARA, CA 95050, USA		
PAGE SERIAL	GPU FBVDD0 & IVS Decoupling	NV_PN	600-1G174-BASE-A01	
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GPU NVVDD Decoupling



GPU NVVDD Decoupling

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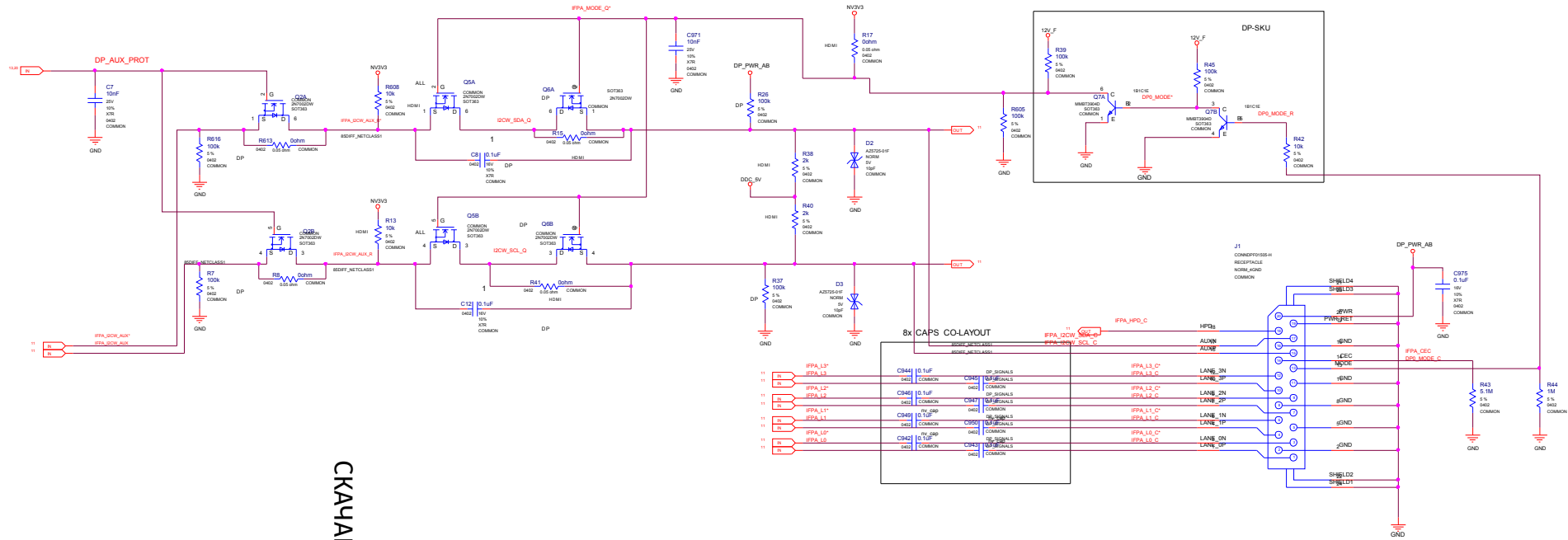
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PAGE DETAIL	GPU NVVDD DECOUPLING

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NV_PN		600-1G174-BASE-A01	
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IFPA DP CO-LAYOUT WITH IFPAB DVI-D-DL

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IFPA DP	

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NV_PN	600-1G174-BASE-A01	PGE	12 OF 32
PCB REV	PG174-A01	DATE	10/DEC/2018
BOM REV	A		

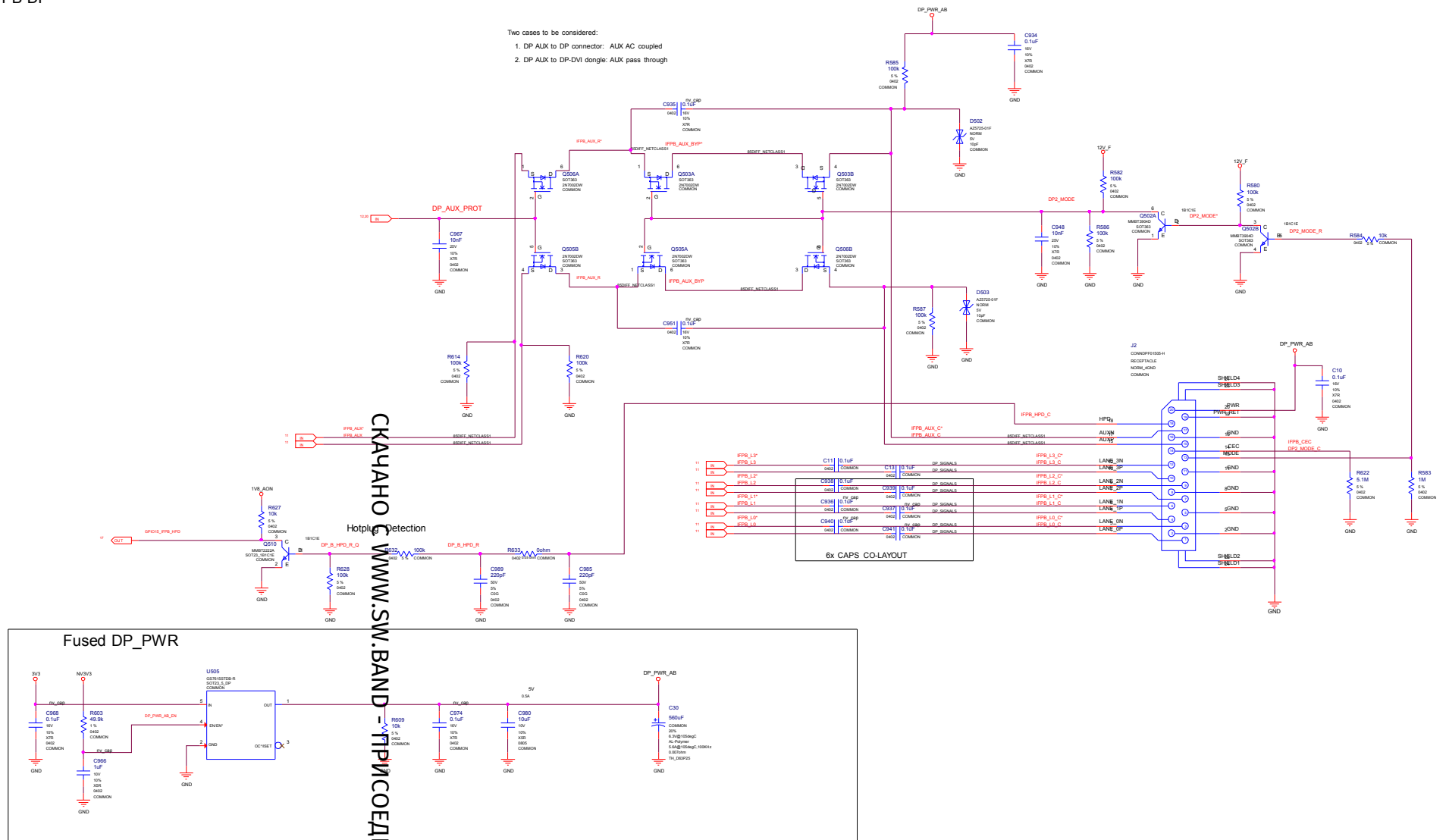


IFPB DP CO-LAYOUT WITH IFPB DVI-D-DL

Page13: IFPB DP

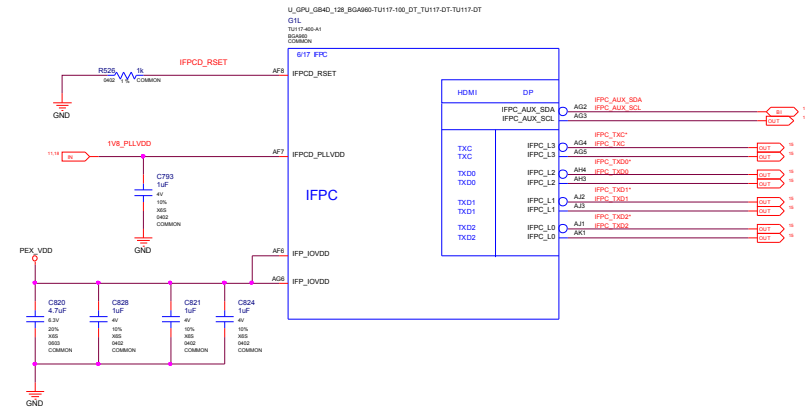
- Two cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP-DVI dongle: AUX pass through

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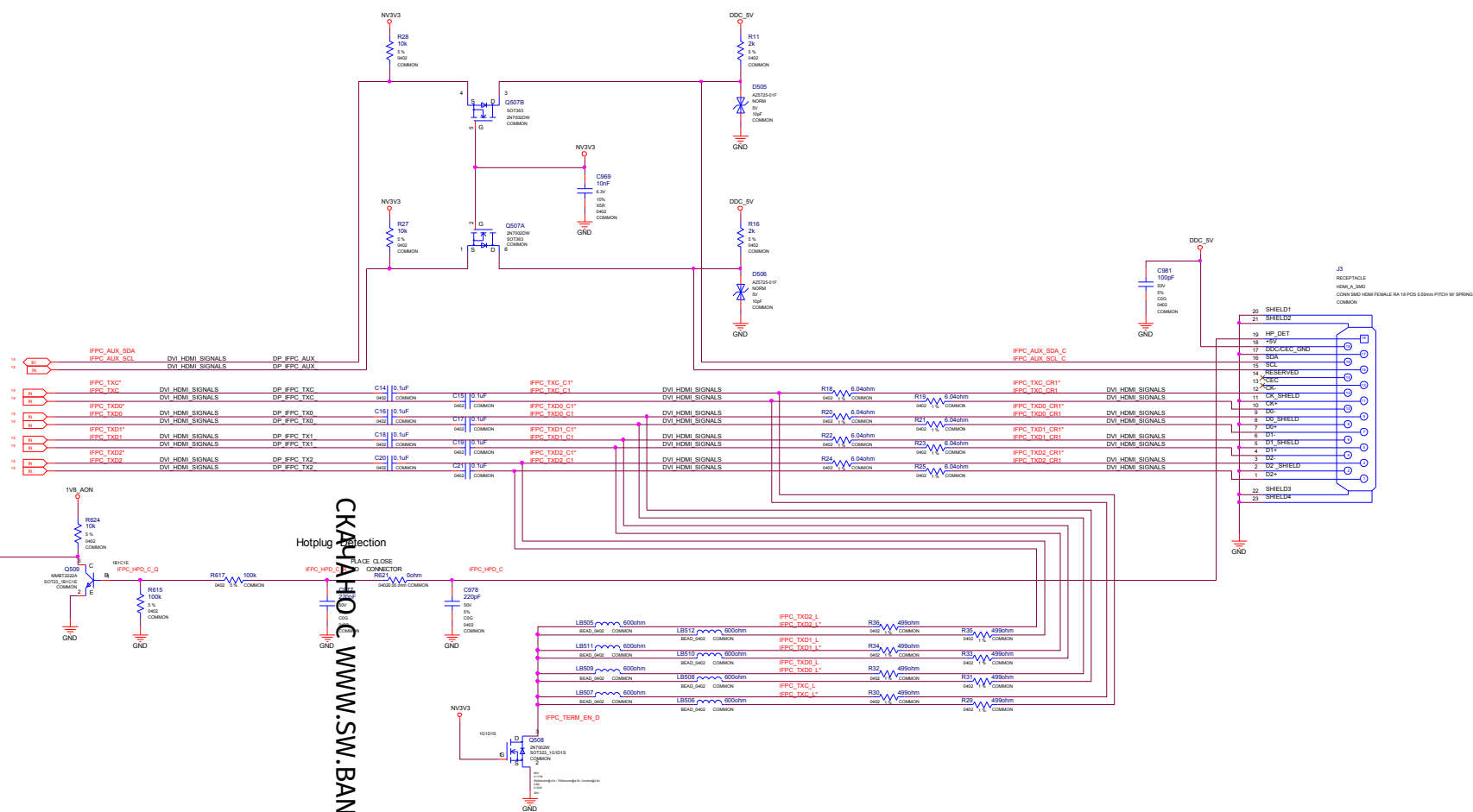
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NV_PN	600-1G174-BASE-A01
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REV DISCLAIMS ALL STANDARDS				PCB REV PG174-A01 RC/GE 14 OF 32	
				BOM REV A DATE 10-DEC-2018	

IFPC HDMI



СКАЧАНО С WWW.SV.BAND - ПРИСОЕДИНЯЙТЕСЬ!

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PAGE DETAIL	FPC HDMI

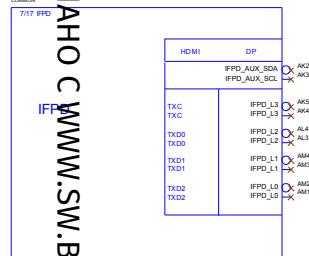
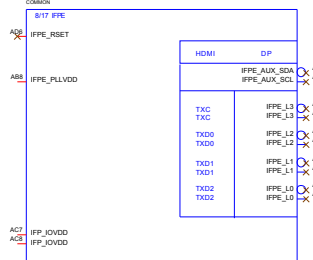
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NV_PN	600-1G174-BASE-A01		
POB_REV	PG174-A01	PAGE	15 OF 32
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IFPDEF UNUSED



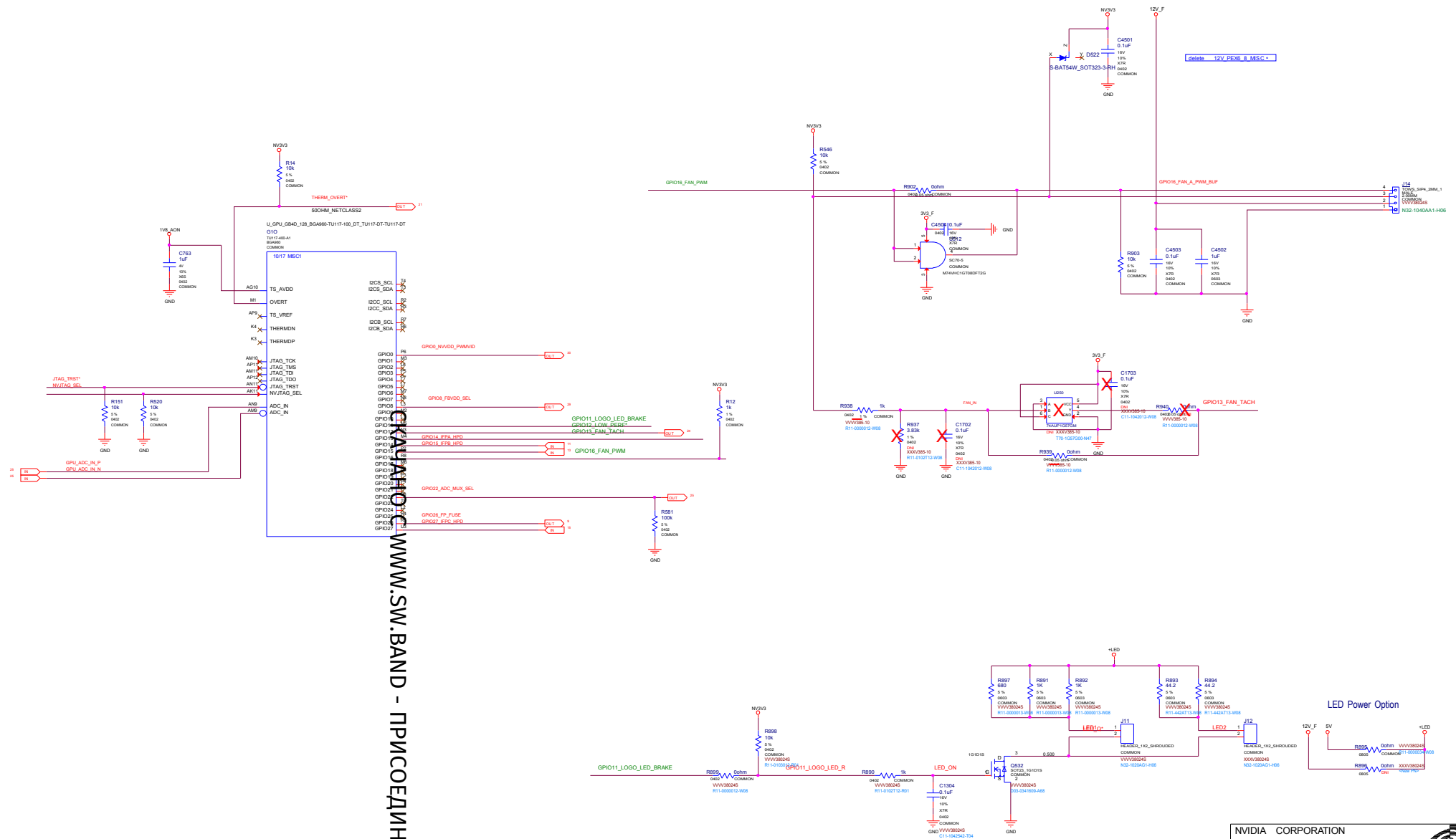
СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ

ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IFDEF UNUSED

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NV_PN	600-1G174-BASE-A01		
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MISC1: GPIO, Fan, Thermal, JTAG



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	MSC1: Fan, Thermal, JTAG, GPIO

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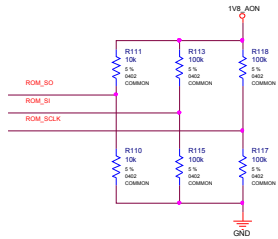


NV_PN	600-1G174-BASE-A01
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MISC2: ROM, XTAL, Straps

ROM_SO	ROM_SI	ROM_SCLK	DUMMY{2:0}.FS_OVERT	1:ENABLE 0:DISABLE	
L	L	L	xxx1	FS_OVERT ENABLE	Default
L	L	M	xxx0	FS_OVERT DISABLE	

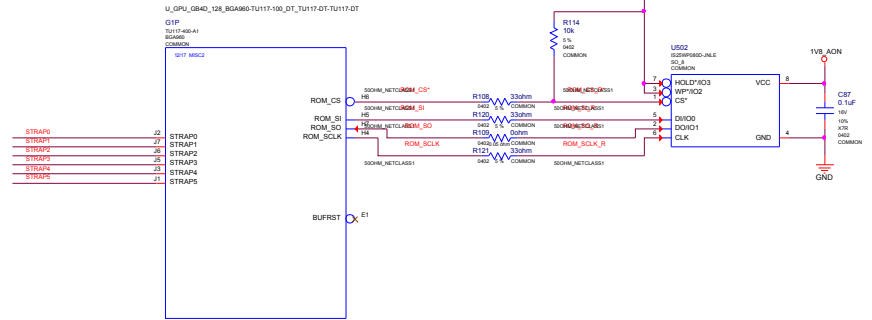


H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

STRAP2	STRAP1	STRAP0	RAMCFG{4:0}	
L	L	L	0000	Samsung 8Gb 256Mx32 K4G80325FC-HC25
L	L	H	0001	Micron 8Gb 256Mx32 MT51J256M32HF-80:B
L	H	L	0010	Hynix 8Gb 256Mx32 H5GC8H24AJR-R2C
L	H	H	0011	Hynix 8Gb 256Mx32 H5GQ8H24MJR-R4C
H	H	L	0110	
H	H	H	0111	
L	L	M	1000	
L	M	L	1001	

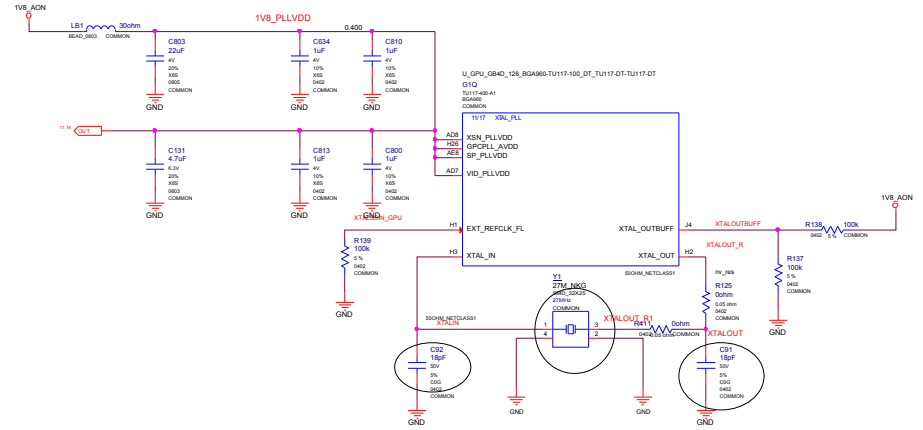
STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	0
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1
L	L	L	0	0	0	0

- 1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE
1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER
1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE



Smart Fan

XTALOUTBUFF	Volatge	SmartFan PWM	GPIO/PWM at the FAN
Low	0V	GPIO Disabled	100%
Middle	0.9V	66% PWM	33% PWM (Typical)
High	1.8V	33% PWM	66% PWM



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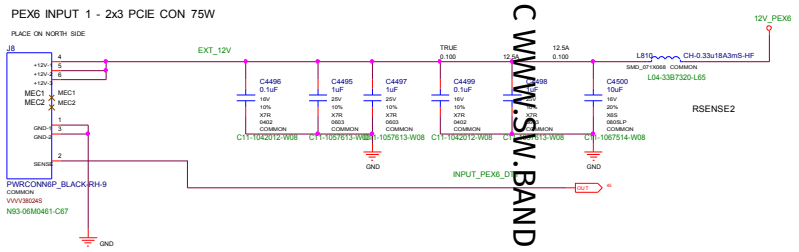
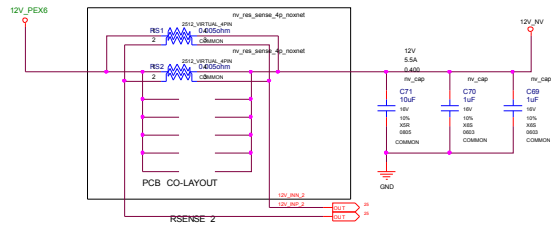
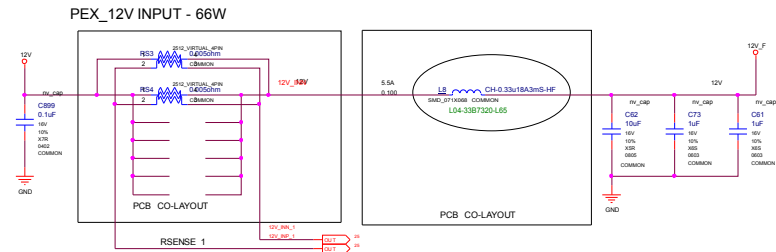
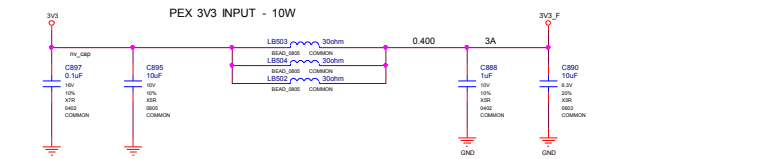
ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PCB DETAIL	MISC2 ROM XTAL Straps

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Power Inputs and Filtering

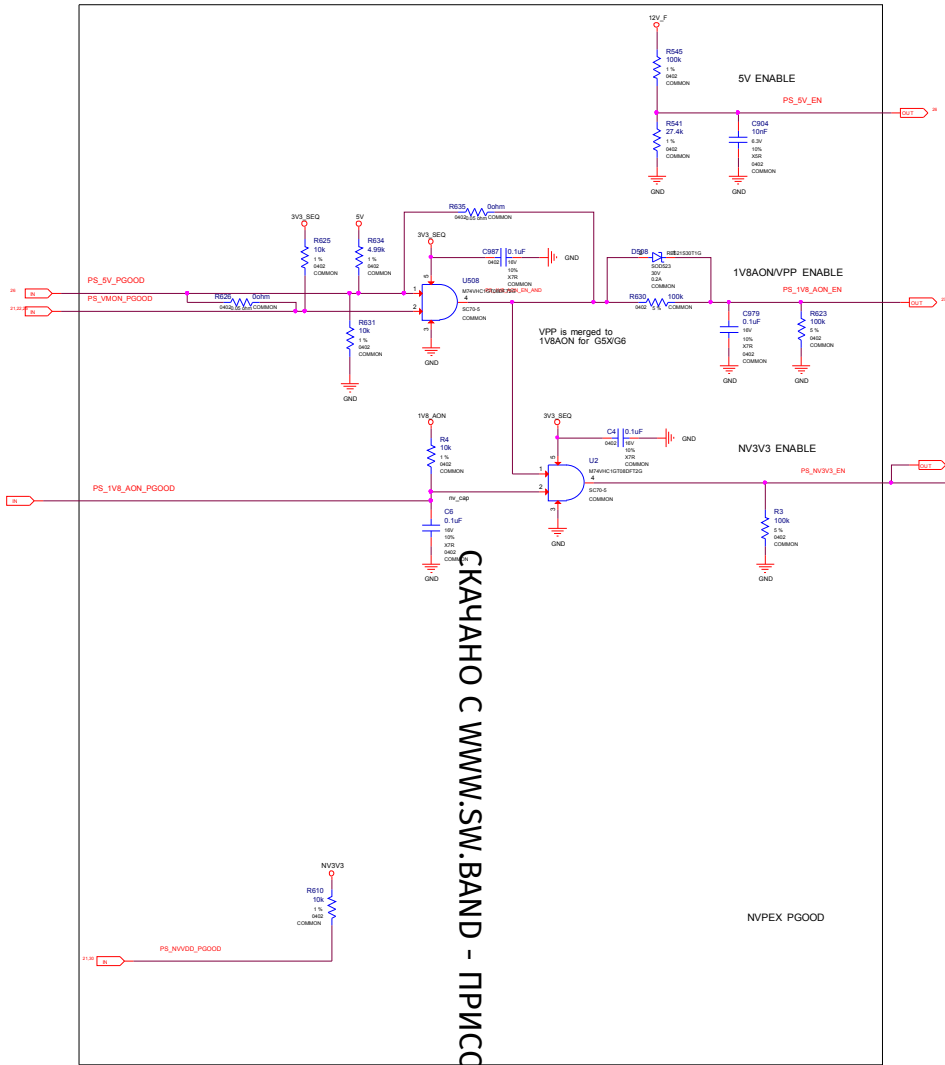


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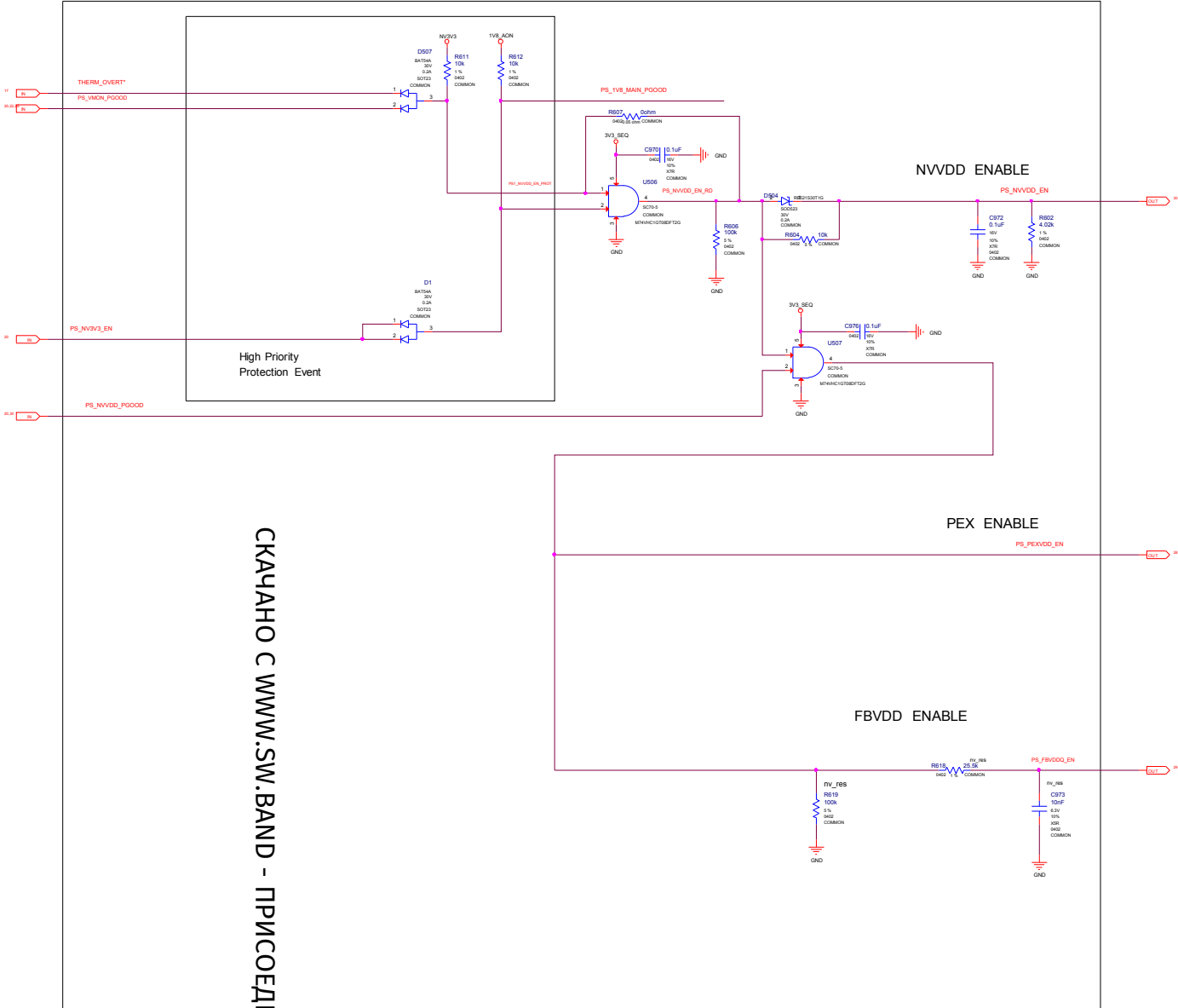
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NV_PN	600-1G174-BASE-A01		
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SEQ:5V,1V8,NV3V3 ENABLE



SEQ:NV,PEX,FB ENABLE



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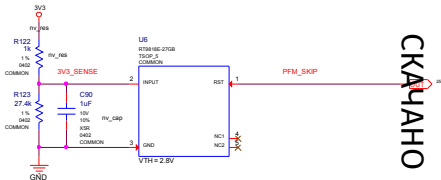
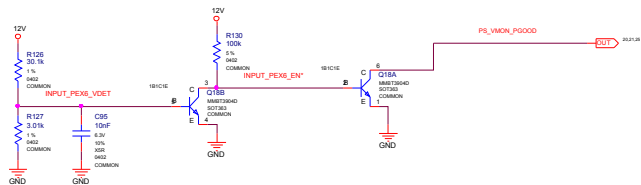
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SEQUENCE:Voltage Monitor



Route the trace to PEV Golden Finger

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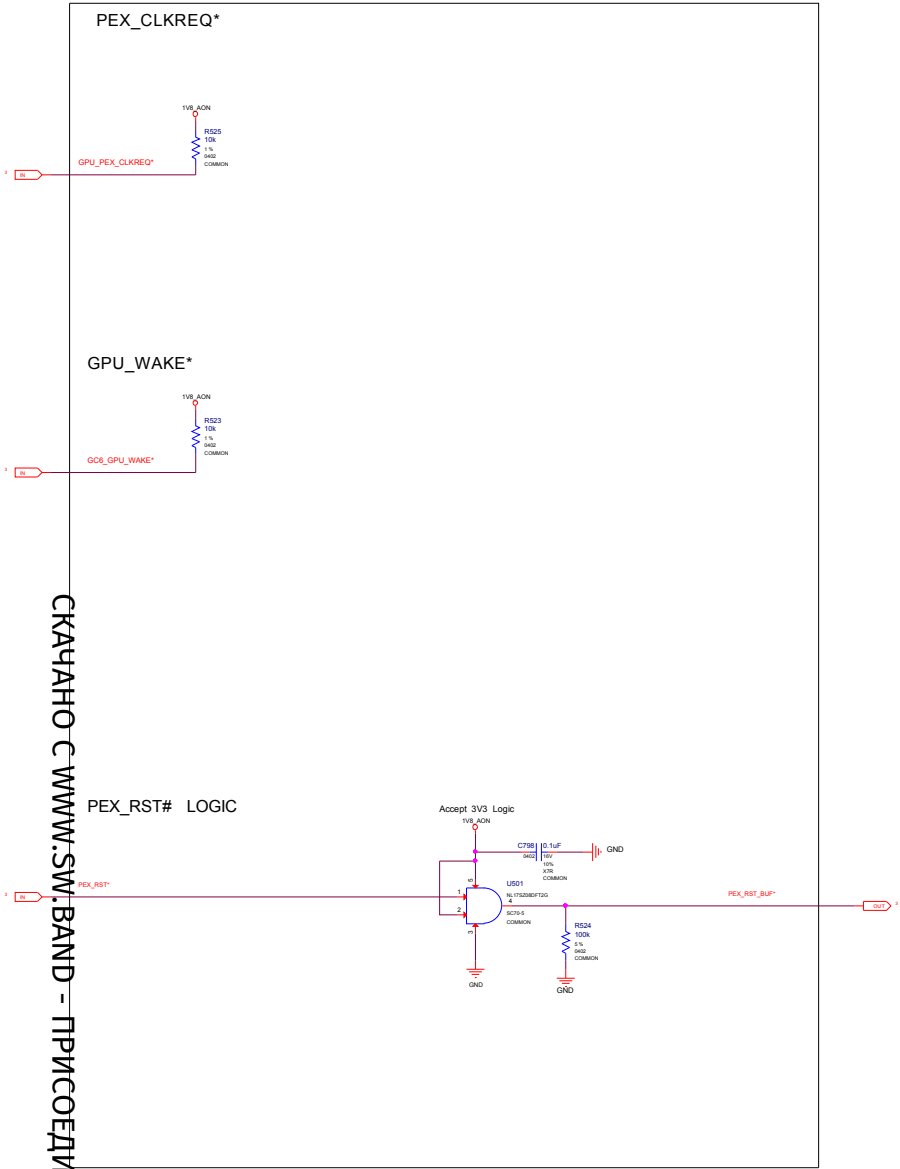
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NV_PN	600-1G174-BASE-A01	22 OF 32
PCB REV	PG174-A01	DATE
BOM REV	A	10-DEC-2018

SEQUENCE:MISC



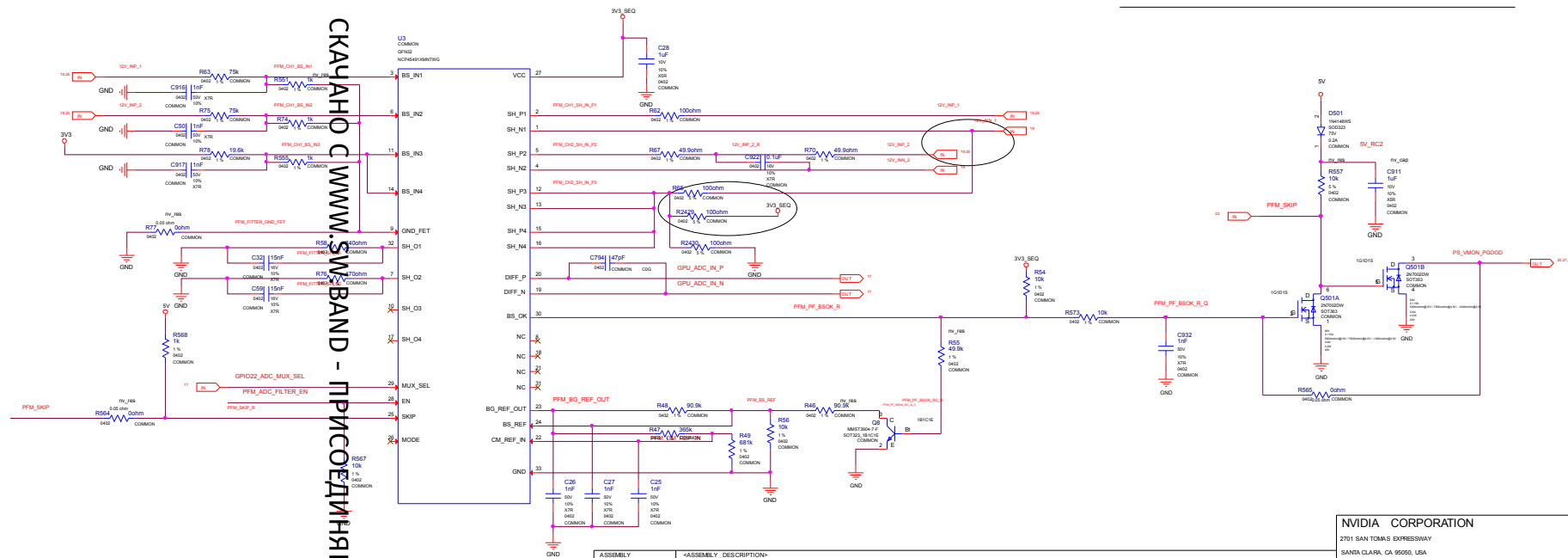
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NV_PN		600-1G174-BASE-A01	
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BOM REV	A	DATE	10/DEC/2018



PWR SENSE



MODE PIN STATUS	MULTIPLEXER OPERATION
LOW	DEVICE A
TRI-STATE(FLOATING)	STAND-ALONE
HIGH	DEVICE B

ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY

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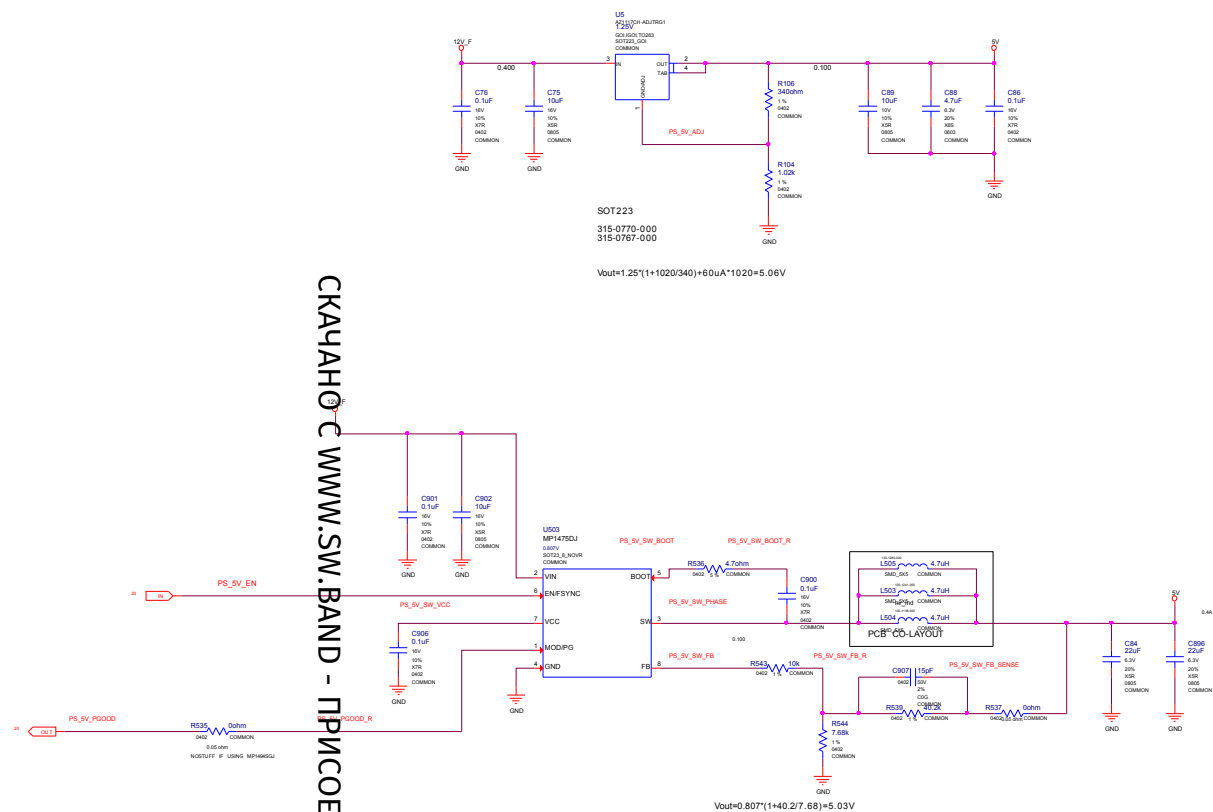
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NV_PN	600-1G174-BASE-A01
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PCB REV	PG174-A01	PAGE	25 OF 32
BOM REV	A	DATE	10-DEC-2018

PS: 5V



СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ

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PAGE DETAIL	PS: 5V

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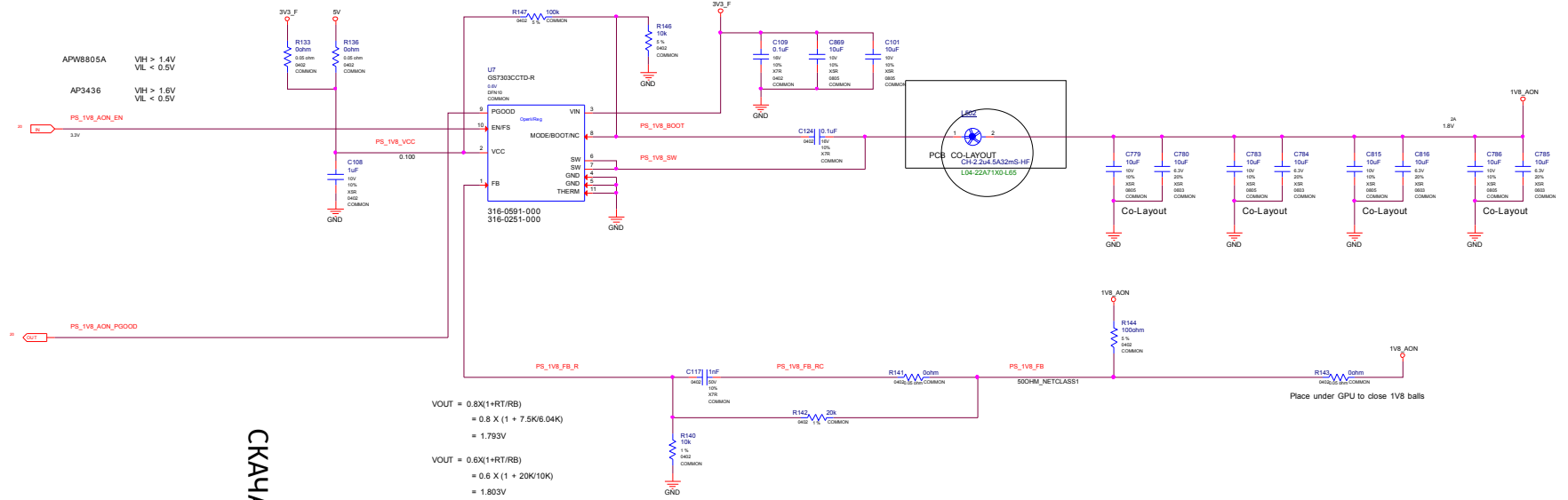


NV_PN	600-1G174-BASE-A01
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PCB REV	PG174-A01	PAGE	26 OF 32
ROM REV	A	DATE	10-DEC-2018

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PS: 1V8



СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

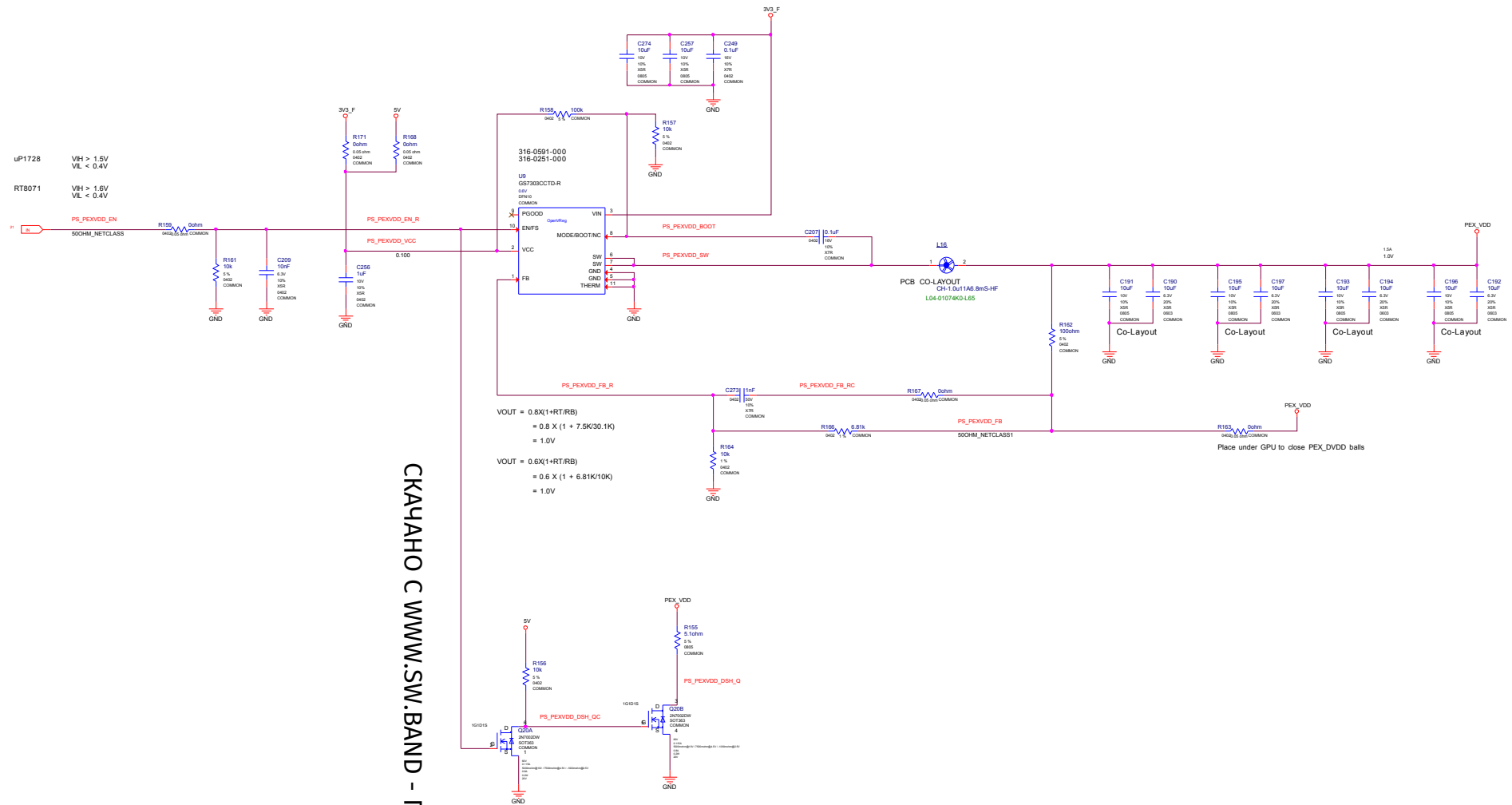
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NVIDIA CORPORATION 2701 SAN TOMAS EXPRESSWAY SANTA CLARA, CA 95050, USA				
NV_PN		600-1G174-BASE-A01		
PCB REV	PG174-A01	RAGE	27 OF 32	
BOM REV	A	DATE	10-DEC-2018	



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PS: PEXVDD



СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: PEX/DD

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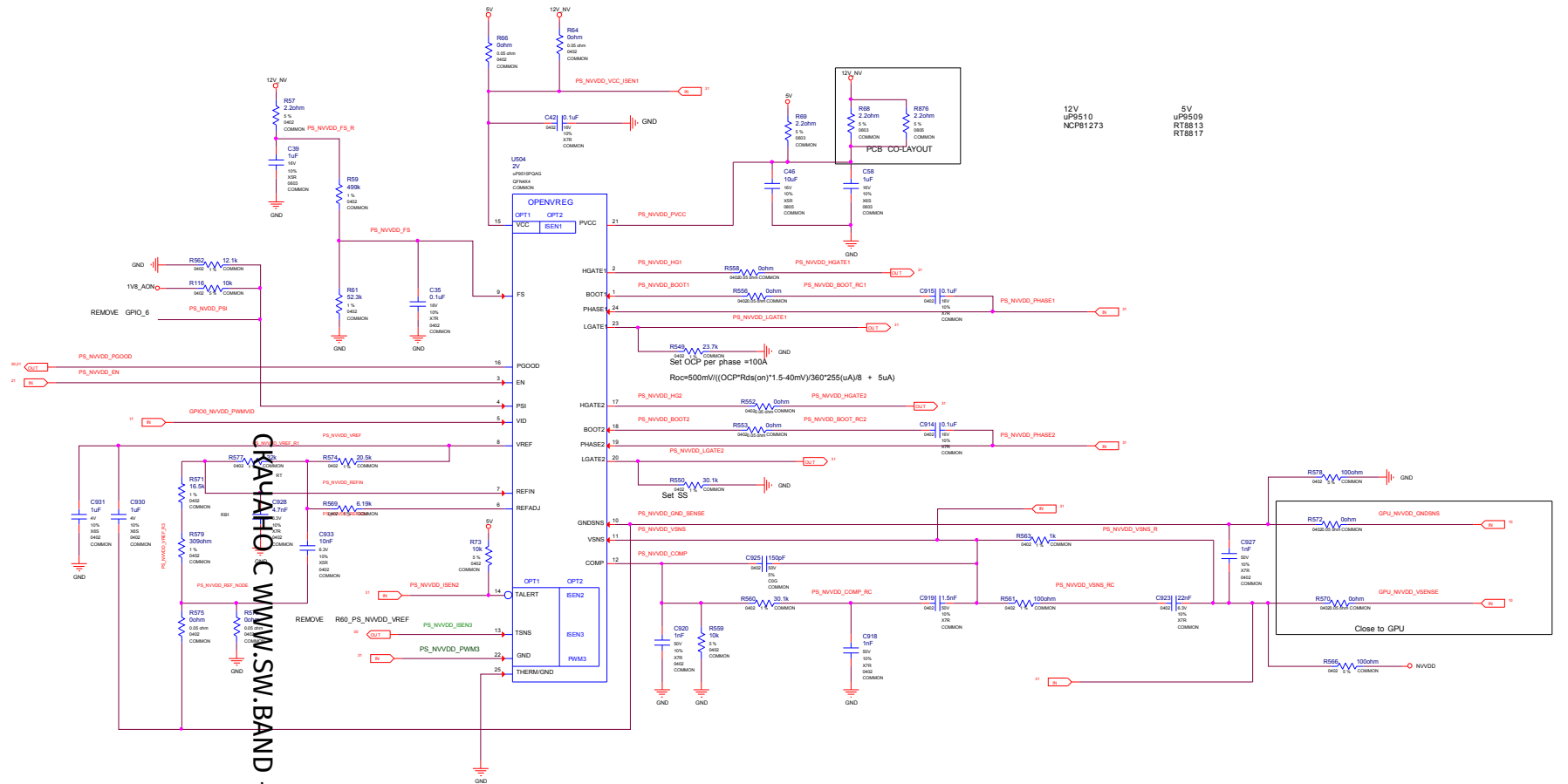
NV_PN	600-1G174-BASE-A01
PCB REV	PG174-A01
BOM REV	A

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BOM REV	A	DATE	10-DEC-2018

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PS: NVVDD OVR2+1

NCP81273	EN VIH > 1.5V, VIL < 0.7V
UP9509/10	EN VIH > 1.2V, VIL < 0.6V
RT8817A	EN VIH > 1.2V, VIL < 0.6V



NVPN	Name	PVCC(21)	VCC(15)
------	------	----------	---------

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PAGE DETAIL	PS: N/VDD OVR2+1

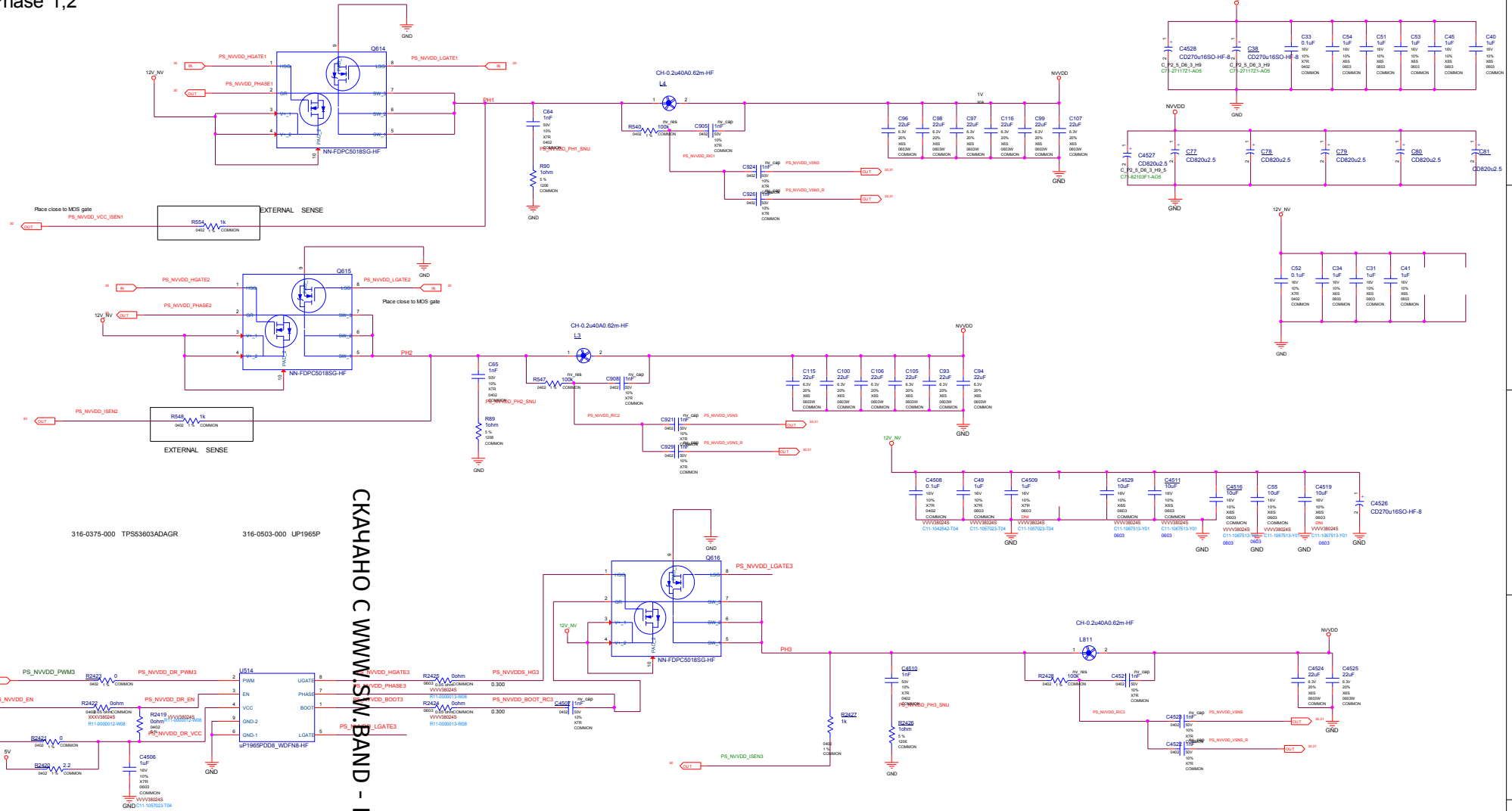
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NW_PN	600-1G174-BASE-A01		
PCB REV	PG174-A01	PAGE	30 OF 32
BOM REV	A	DATE	10-DEC-2018

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PS: NVVDD Phase 1,2



СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

ASSEMBLY	<ASSEMBLY_DESCRIPTION
PAGE DETAIL	PS: NVDD Phase 1,2

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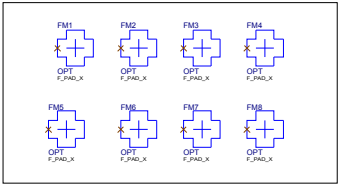
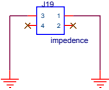
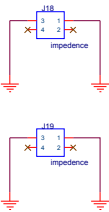
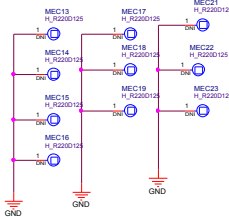
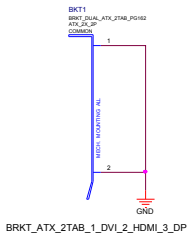


NV_PN	600-1G174-BASE-A01		
PCB REV	PG174-A01	PAGE	31 OF 32
BOM REV	A	DATE	10-DEC-2018

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Mechanical: Bracket/Thermal Solution

Brackets:



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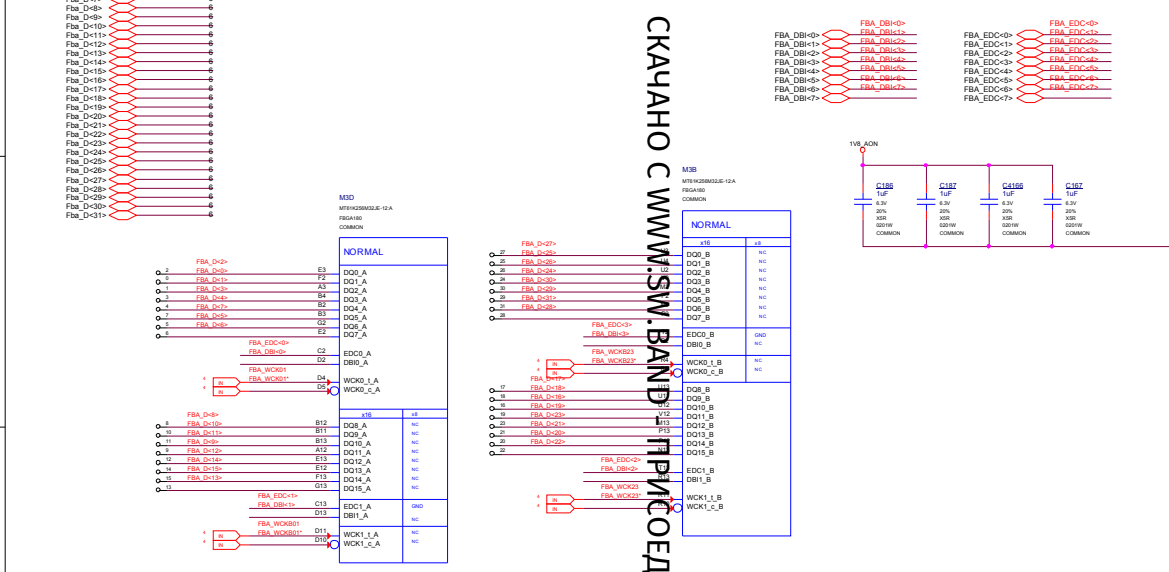
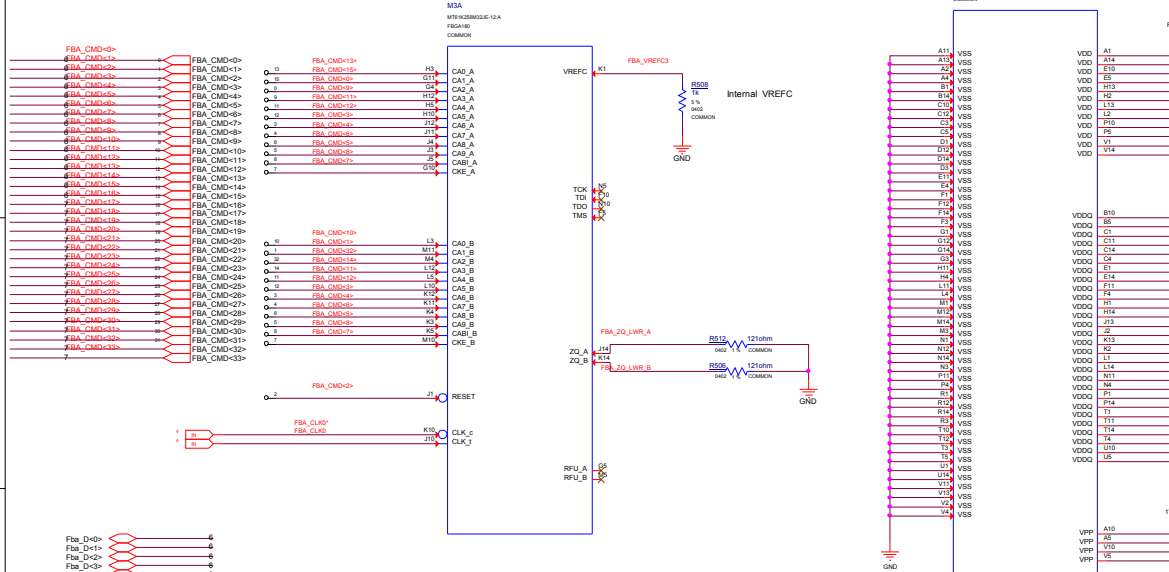
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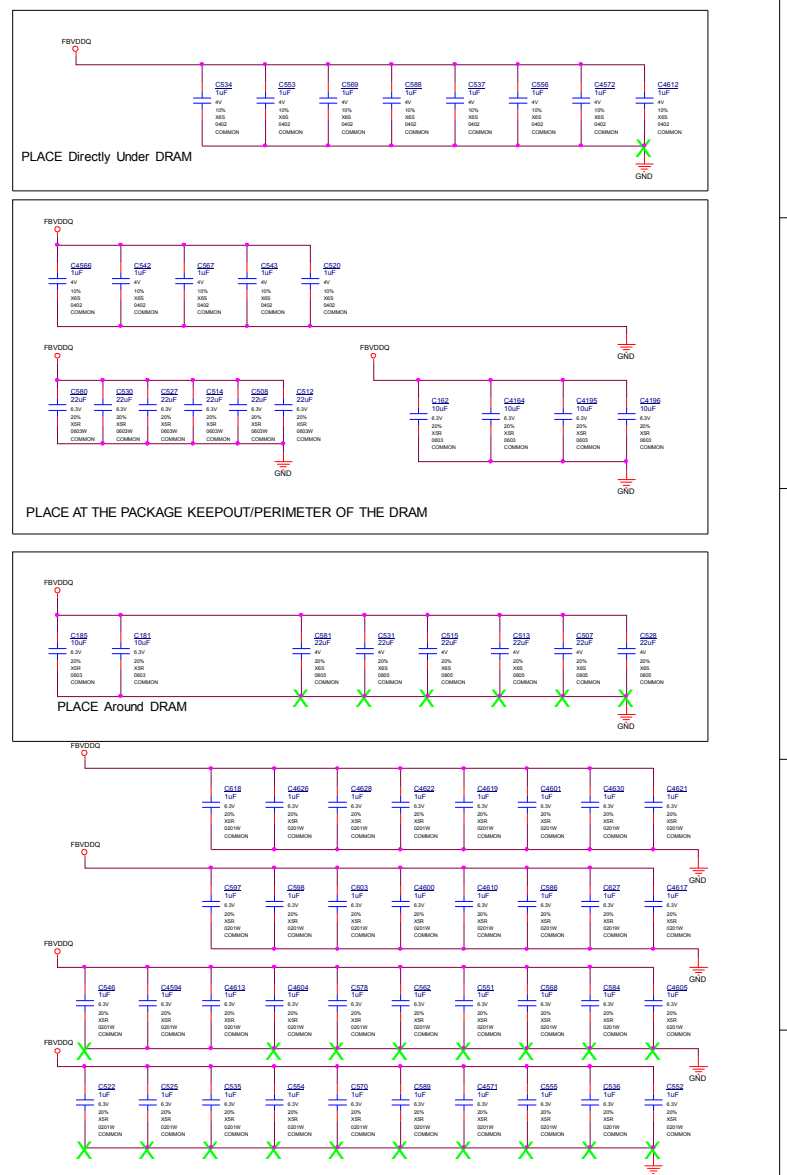


NV_PN	600-1G174-BASE-A01	PAGE	32 OF 32
PCB REV	PG174-A01	DATE	10/DEC/2018
BOM REV	A		

MEMORY: FBA Partition 31..0

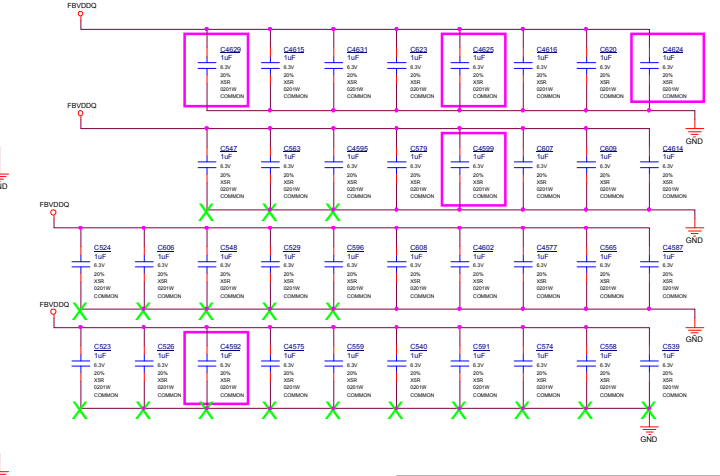
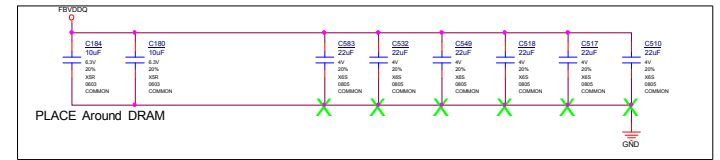
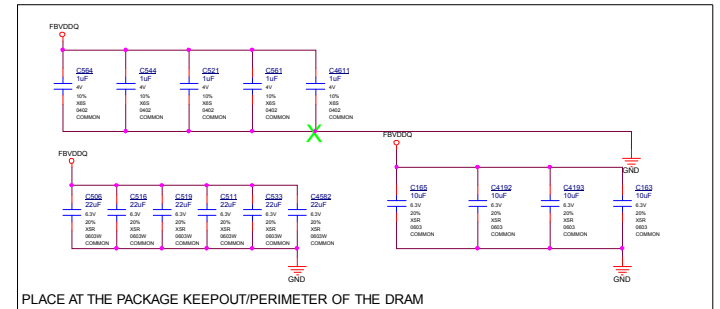
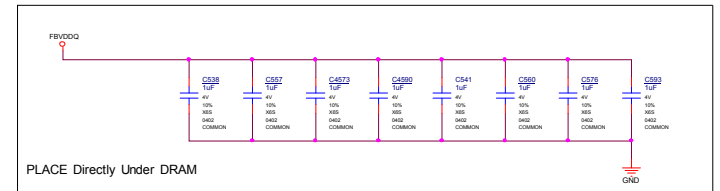
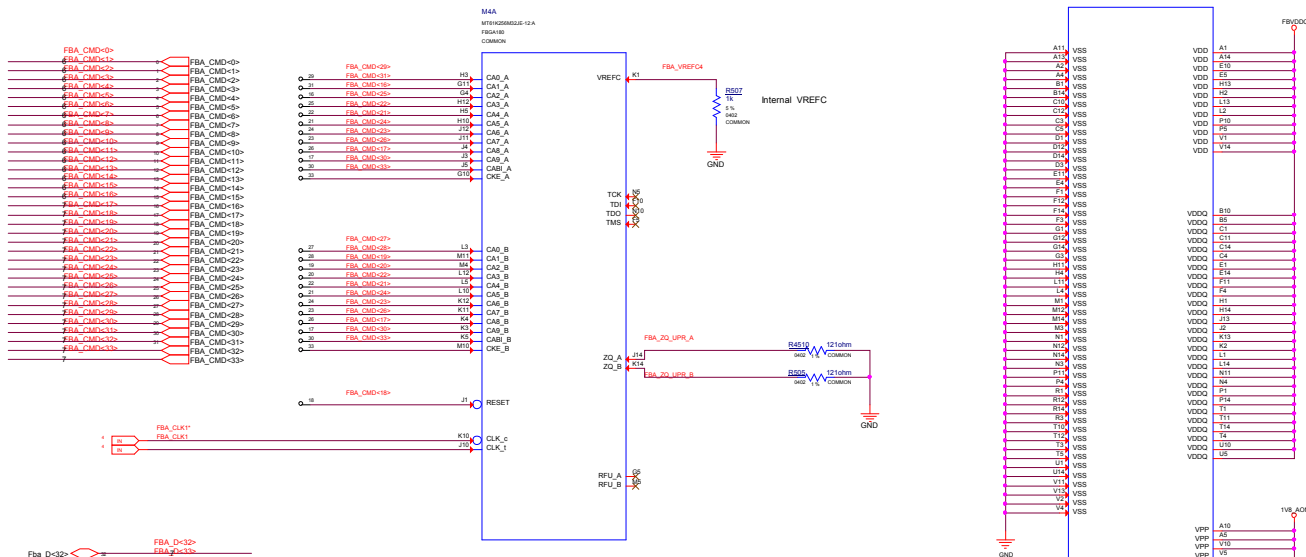


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PG177-ADD	MEMORY: FBA[31:0]



ASSEMBLY	ASSEMBLY DESCRIPTION
PG177-ADD	MEMORY: FBA[31:0]

MEMORY: FBA Partition 63..32



СКАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!

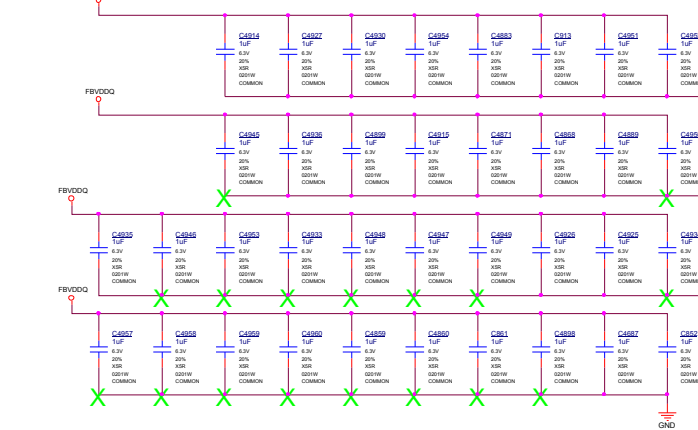
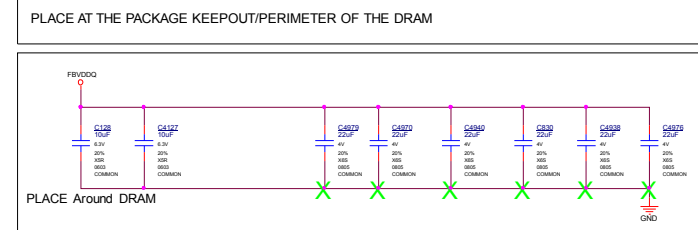
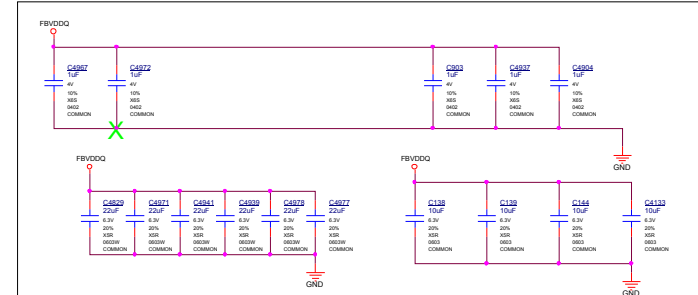
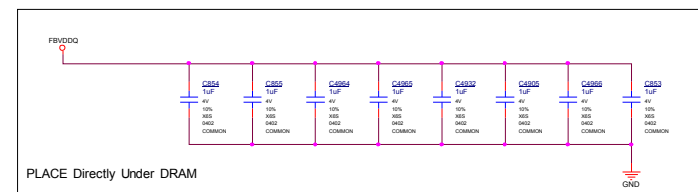
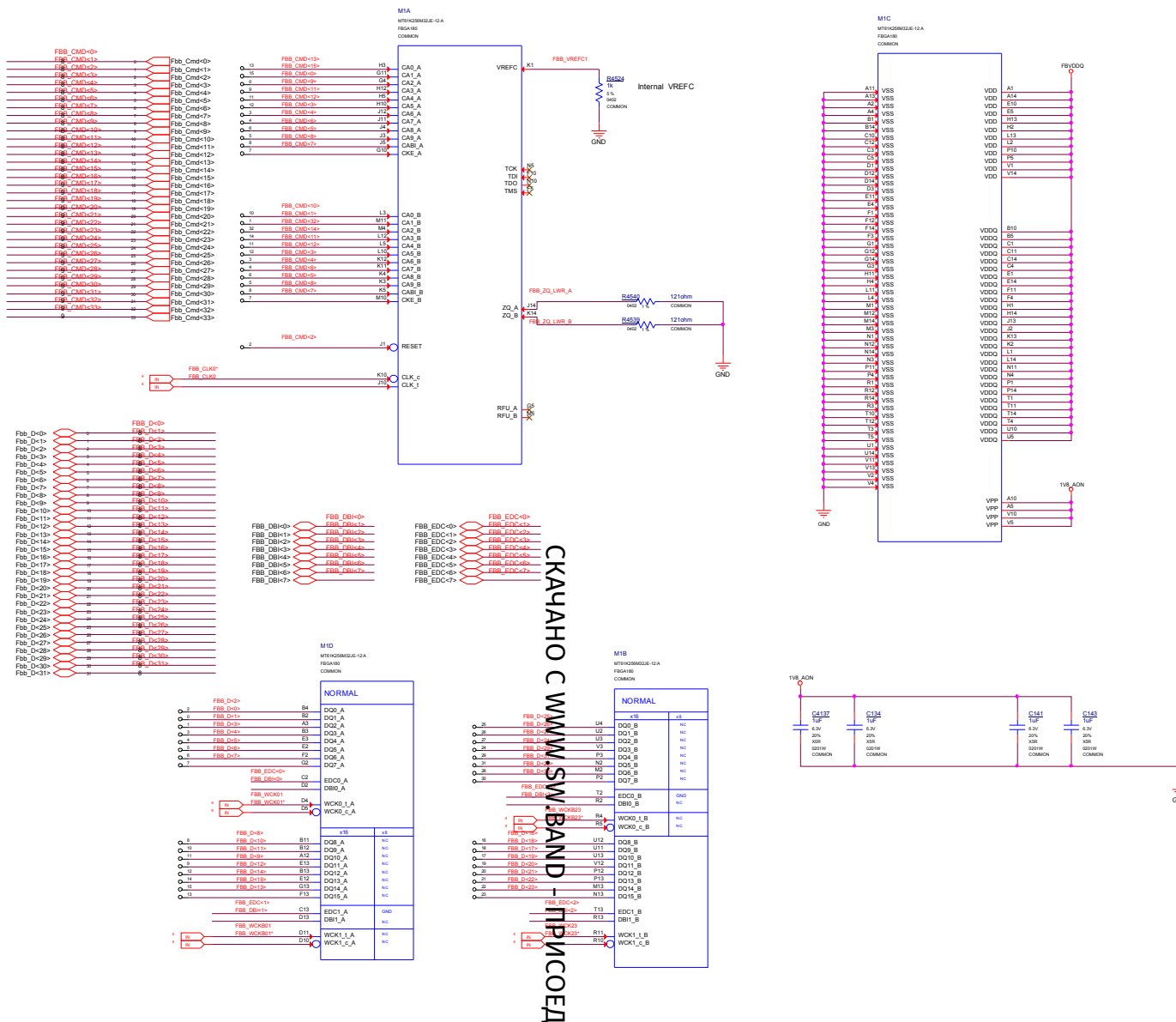
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NORMAL		
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	DQ1_B	NC
	DQ2_B	NC
	DQ3_B	NC
	DQ4_B	NC
	DQ5_B	NC
	DQ6_B	NC
	DQ7_B	NC
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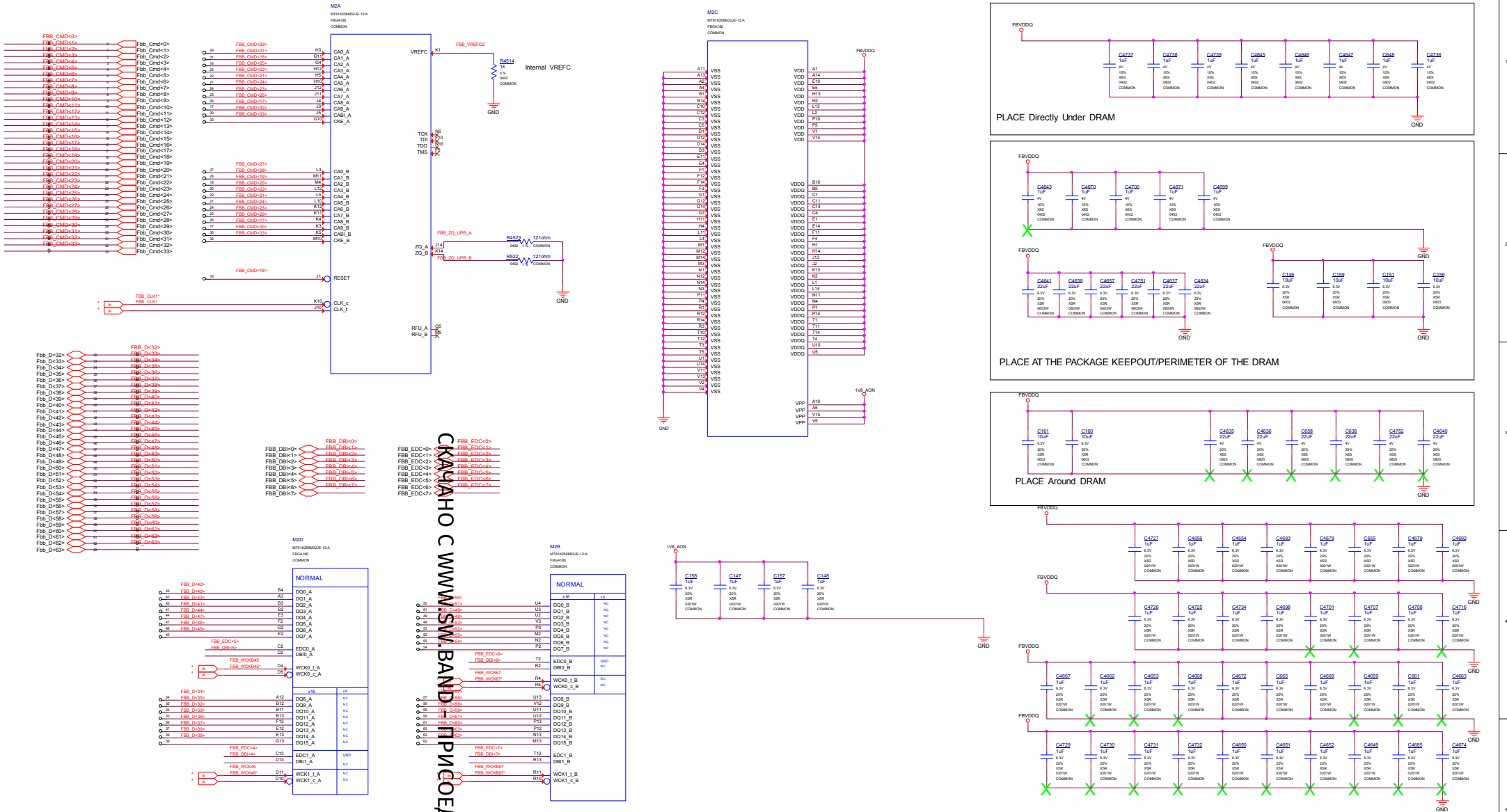


MEMORY: FBB Partition 31..0



СКАЧАНО С WWW.SW.BAND - ТЫРСОЕДИНЕНИЯ

MEMORY: FBB Partition 63..32



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