

PG161-B00

TU106 8GB GDDR6, 192b, X16

DVI-D + DP + HDMI

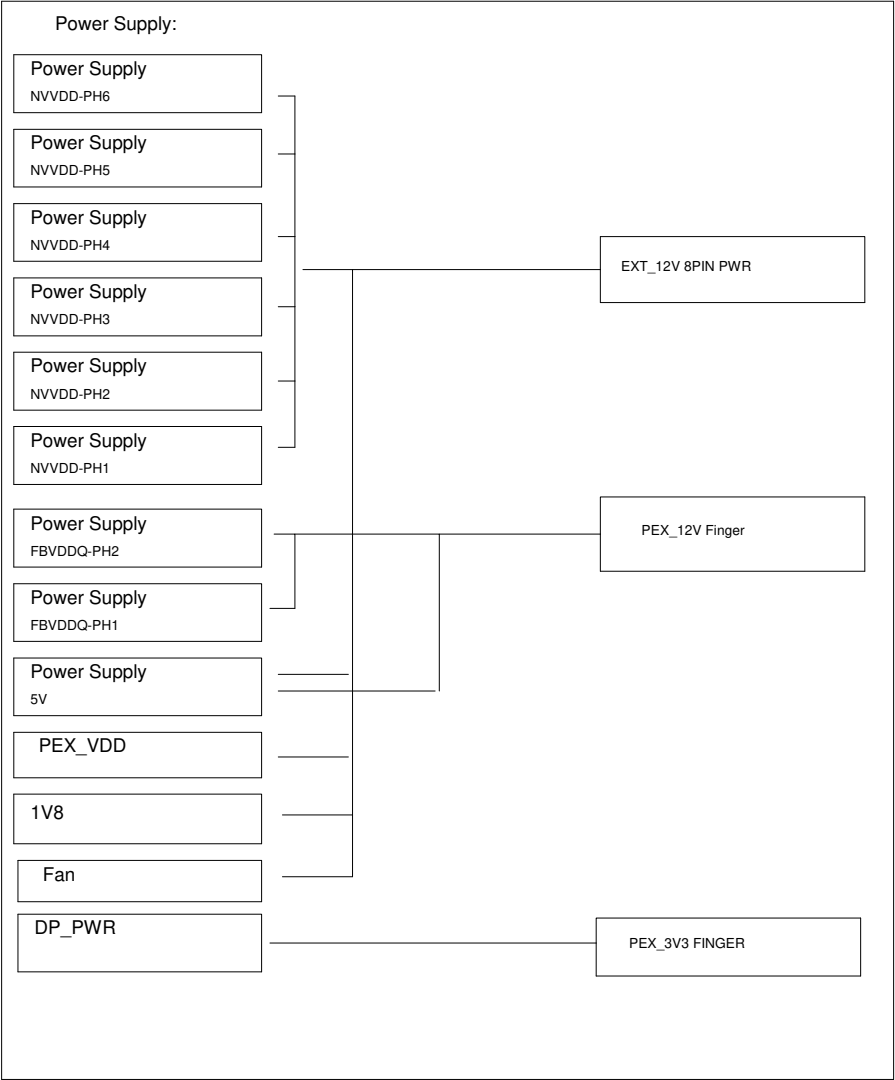
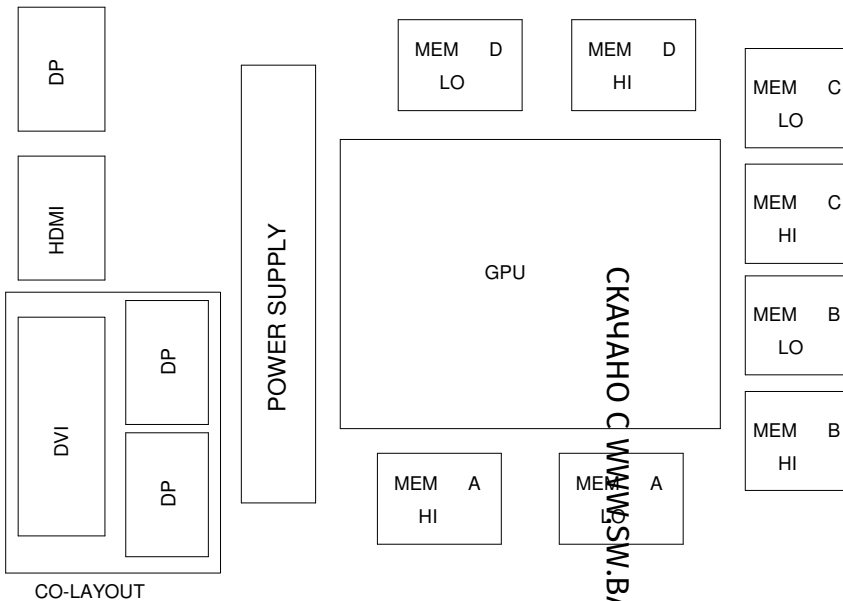
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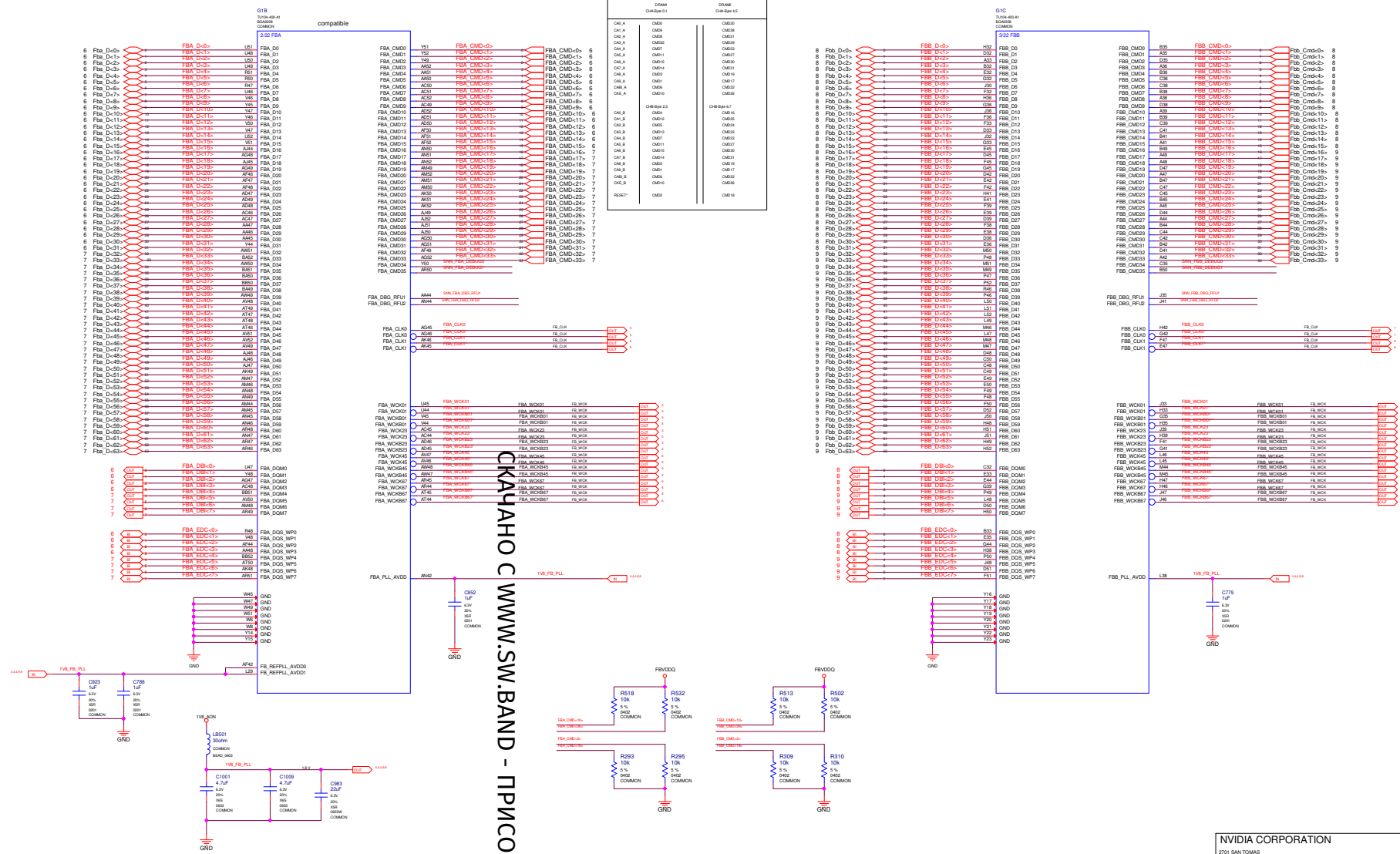
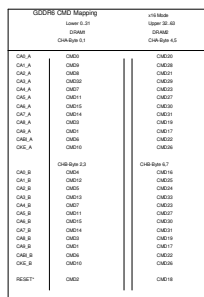
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6	MEMORY: FBA[63:32]
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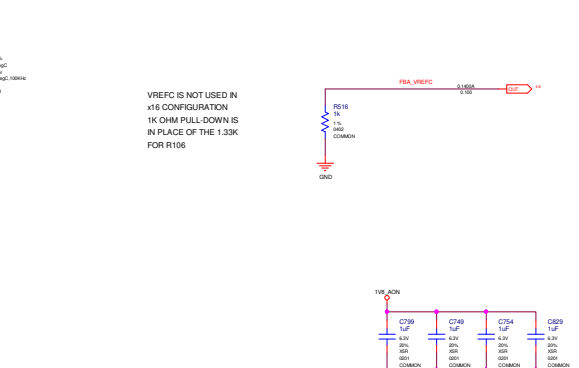
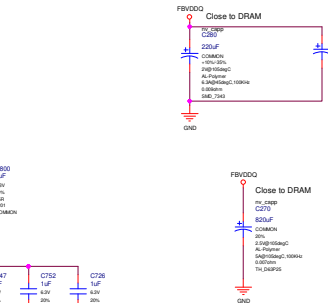
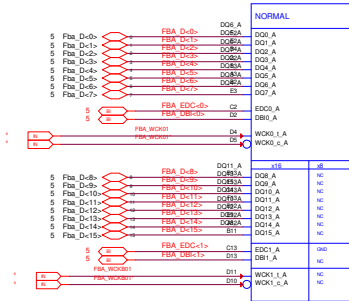
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30	PS: FBVDDQ PH2
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41	PS: OVRM_PWR SENSE
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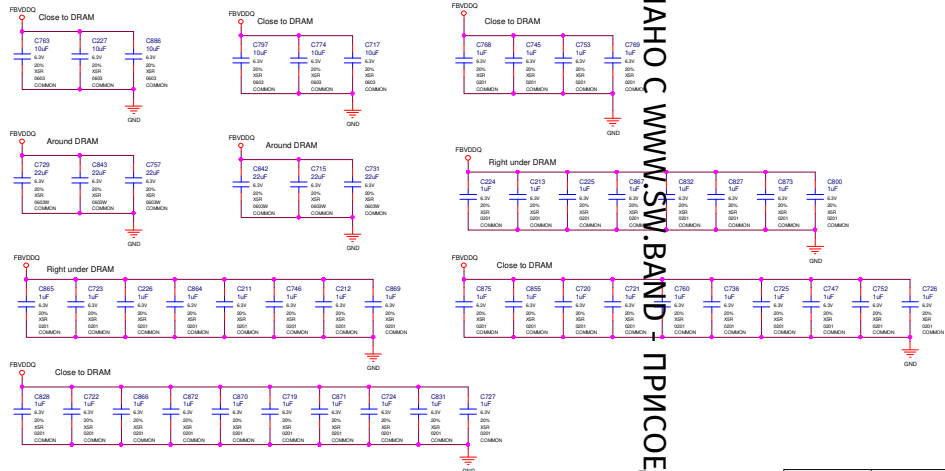
СКАЧАТЬ С WWW.SW.BAND - ПРИБОРАМ







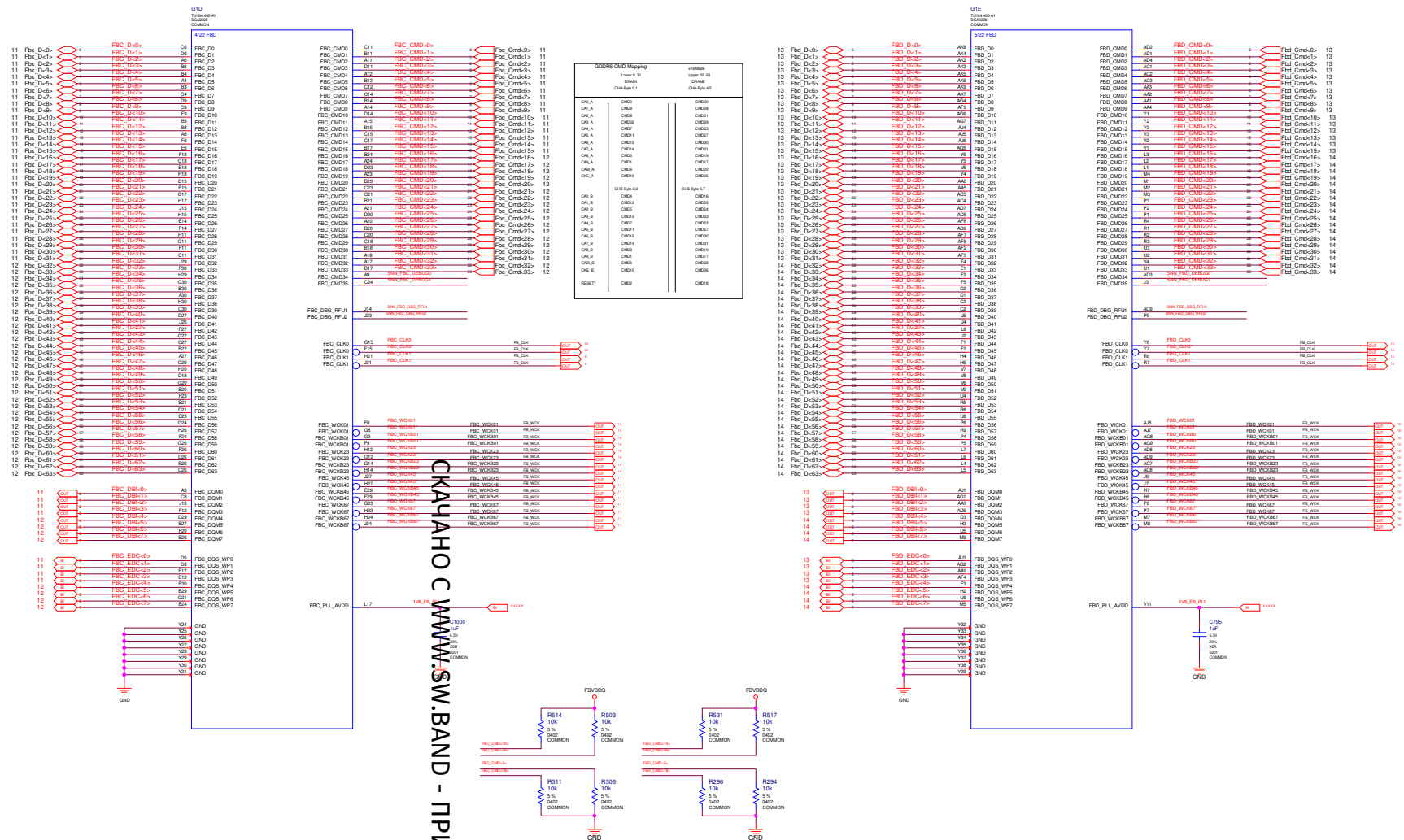
VREFC IS NOT USED IN
x16 CONFIGURATION
1K OHM PULL-DOWN IS
IN PLACE OF THE 1.33K
FOR R106

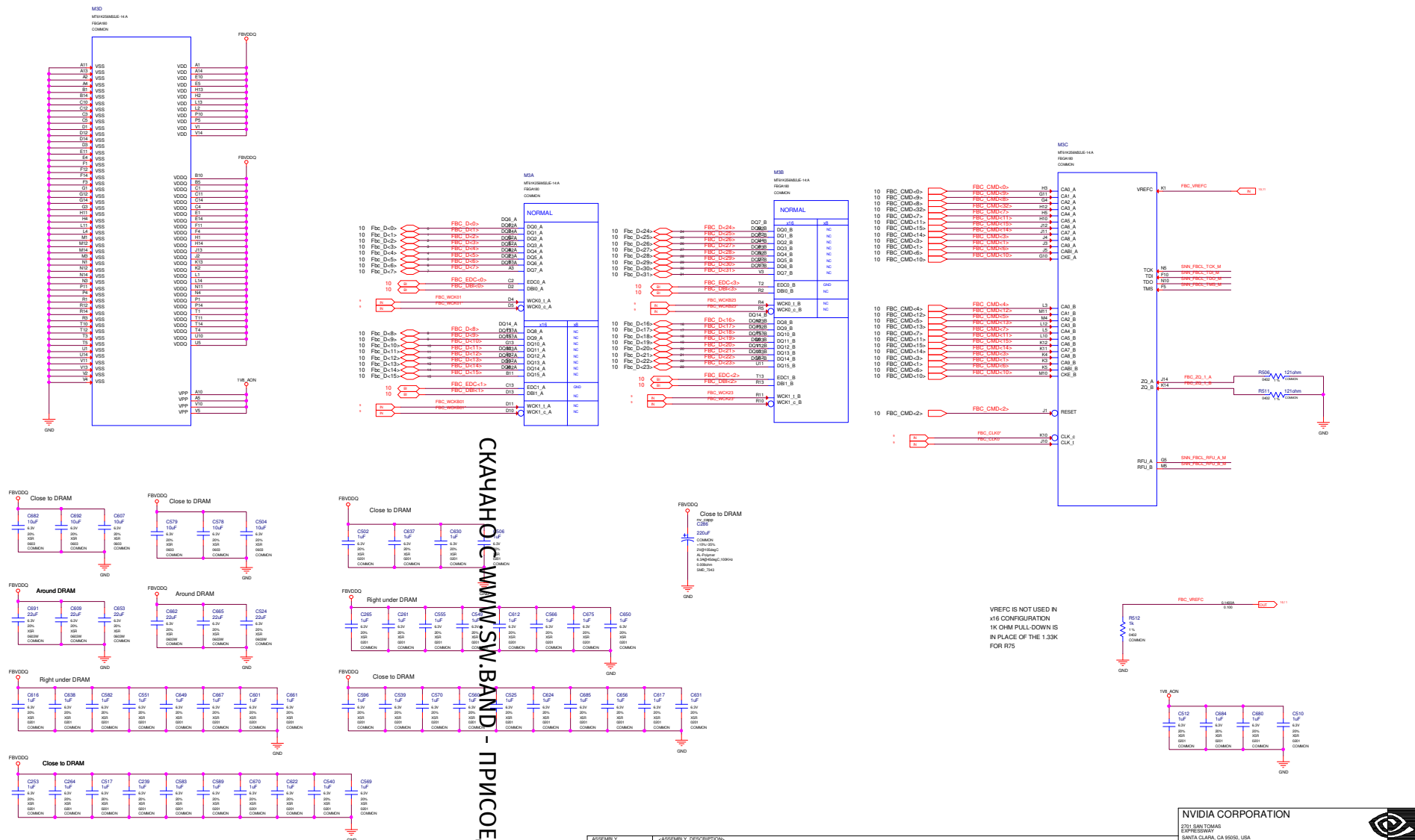


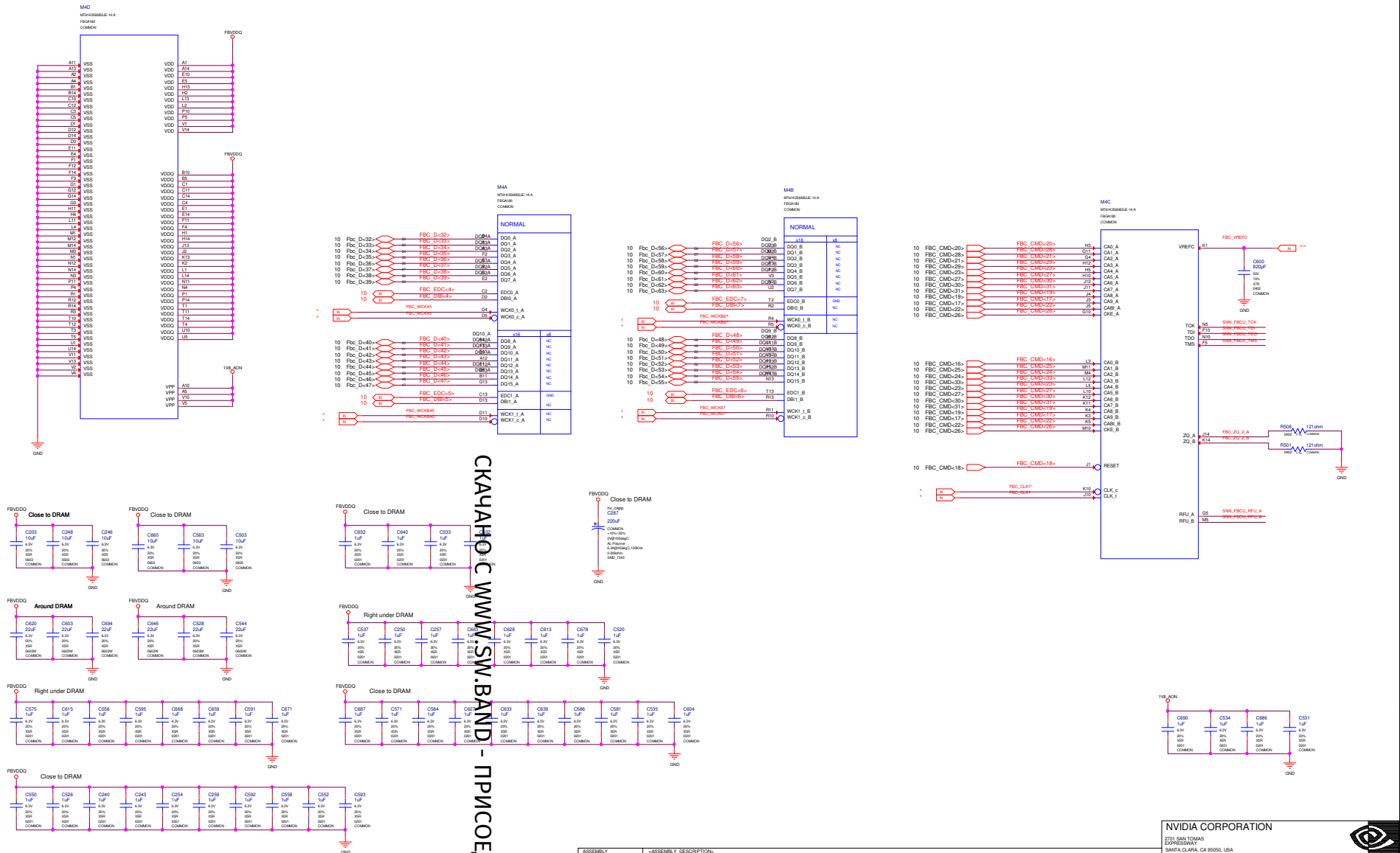


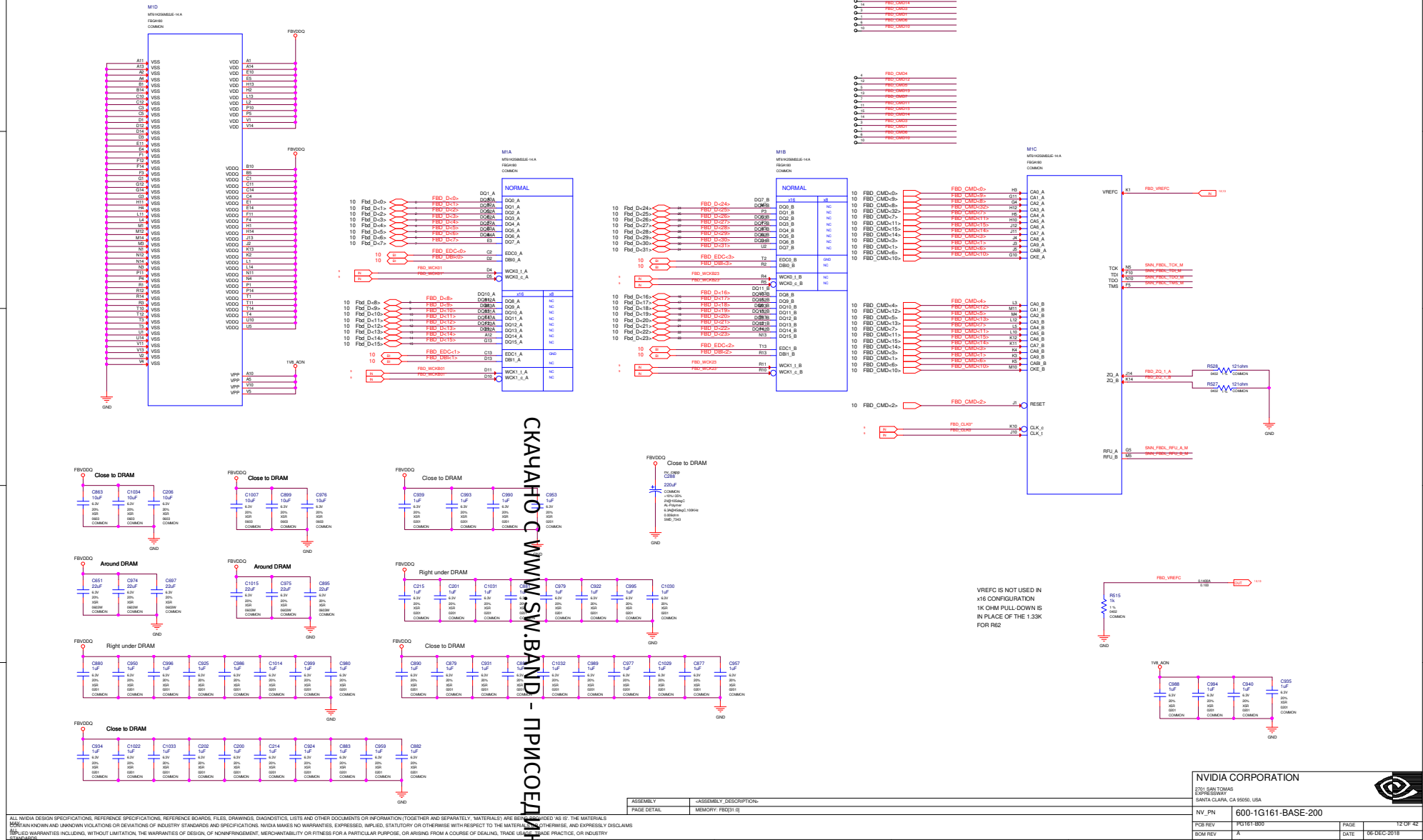


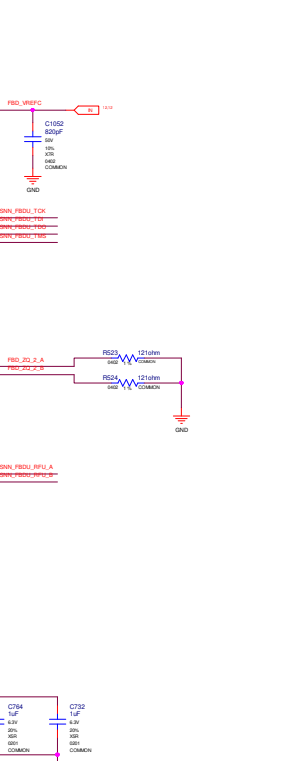
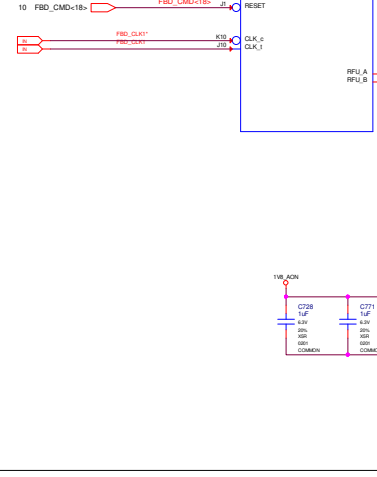
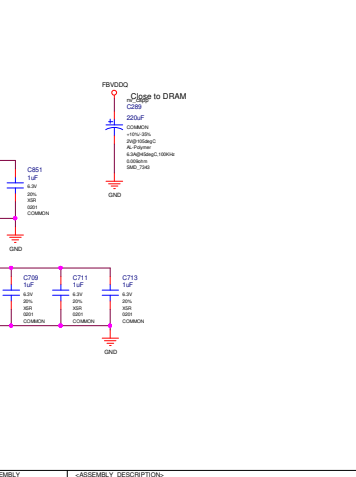
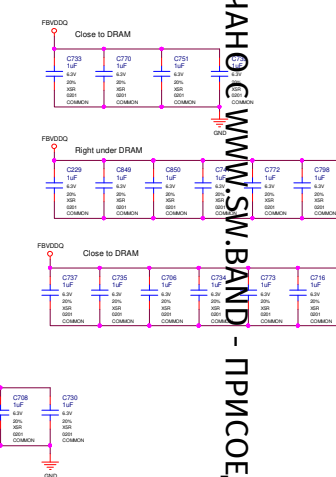
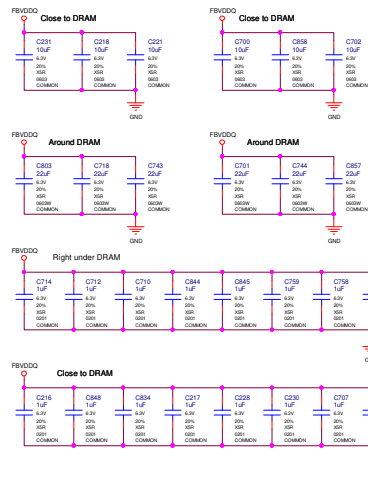


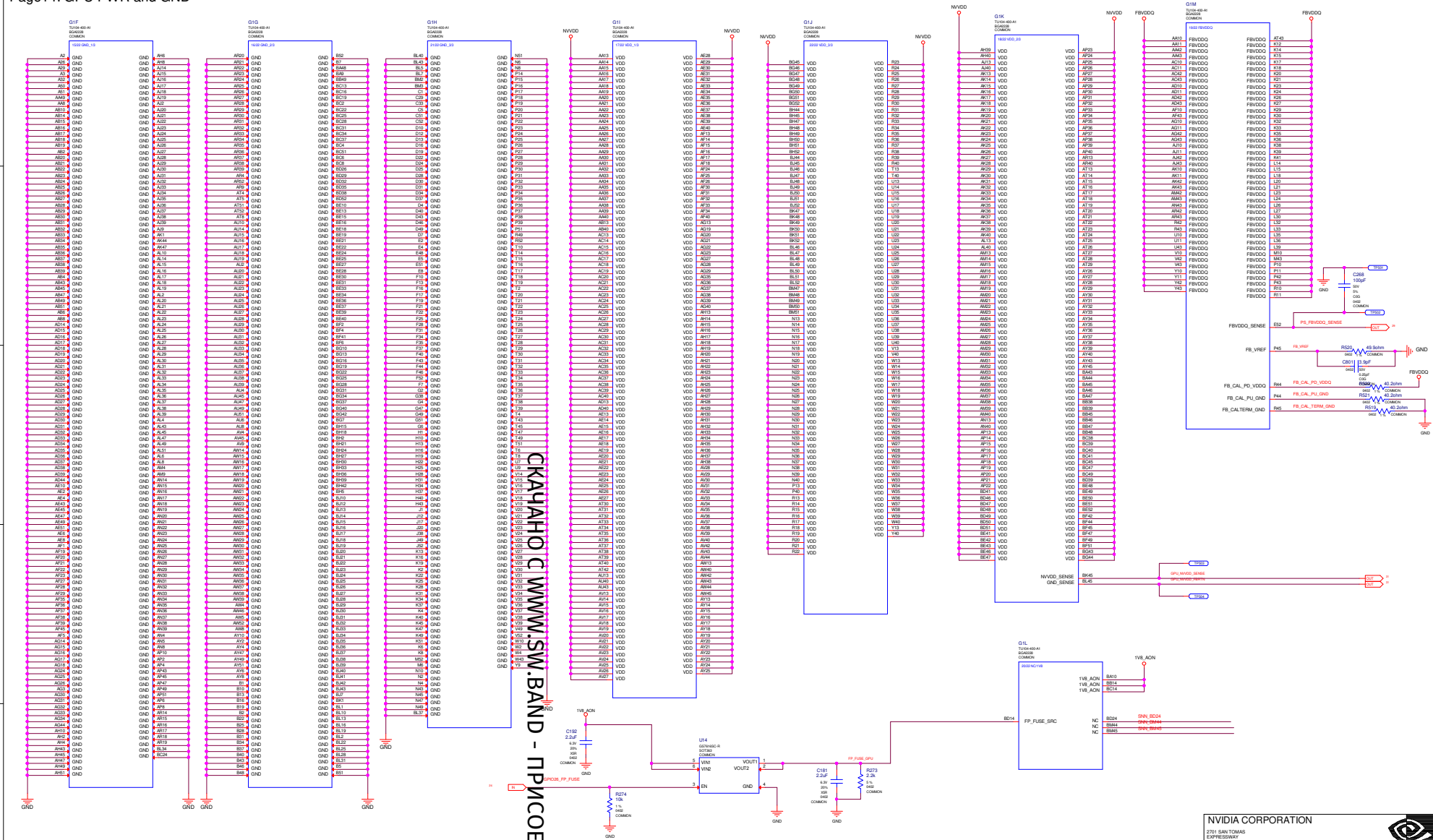




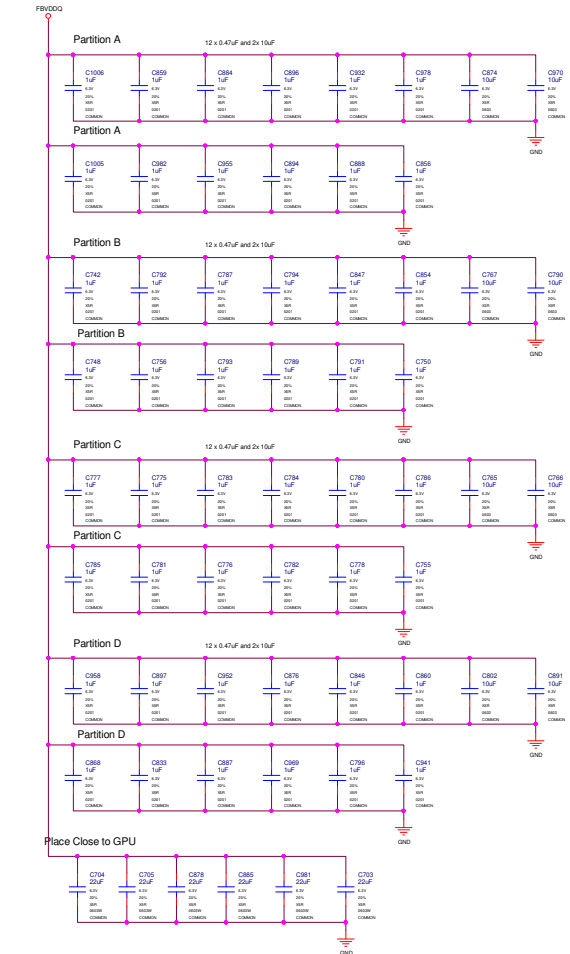




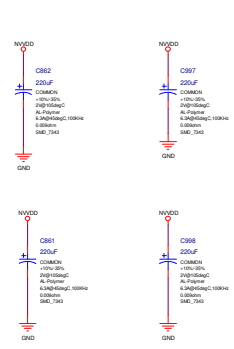




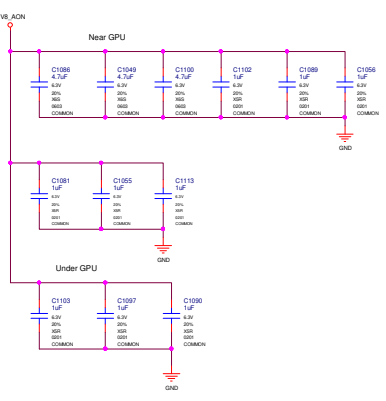
FBVDDQ



NVVD



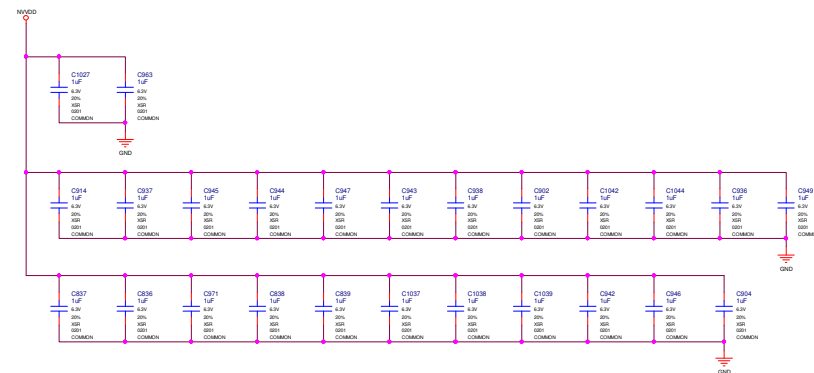
1V8_AON

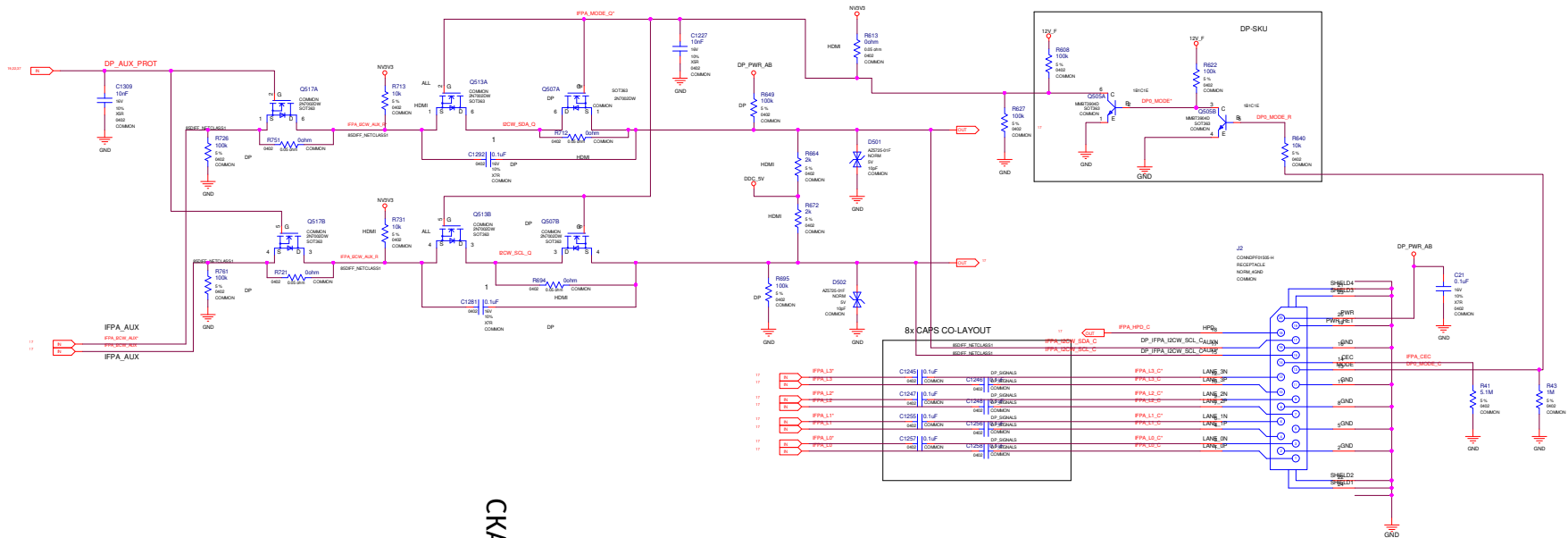


СКАЧАНО С WWW.SW.BAND - ПРИБОРАМИ

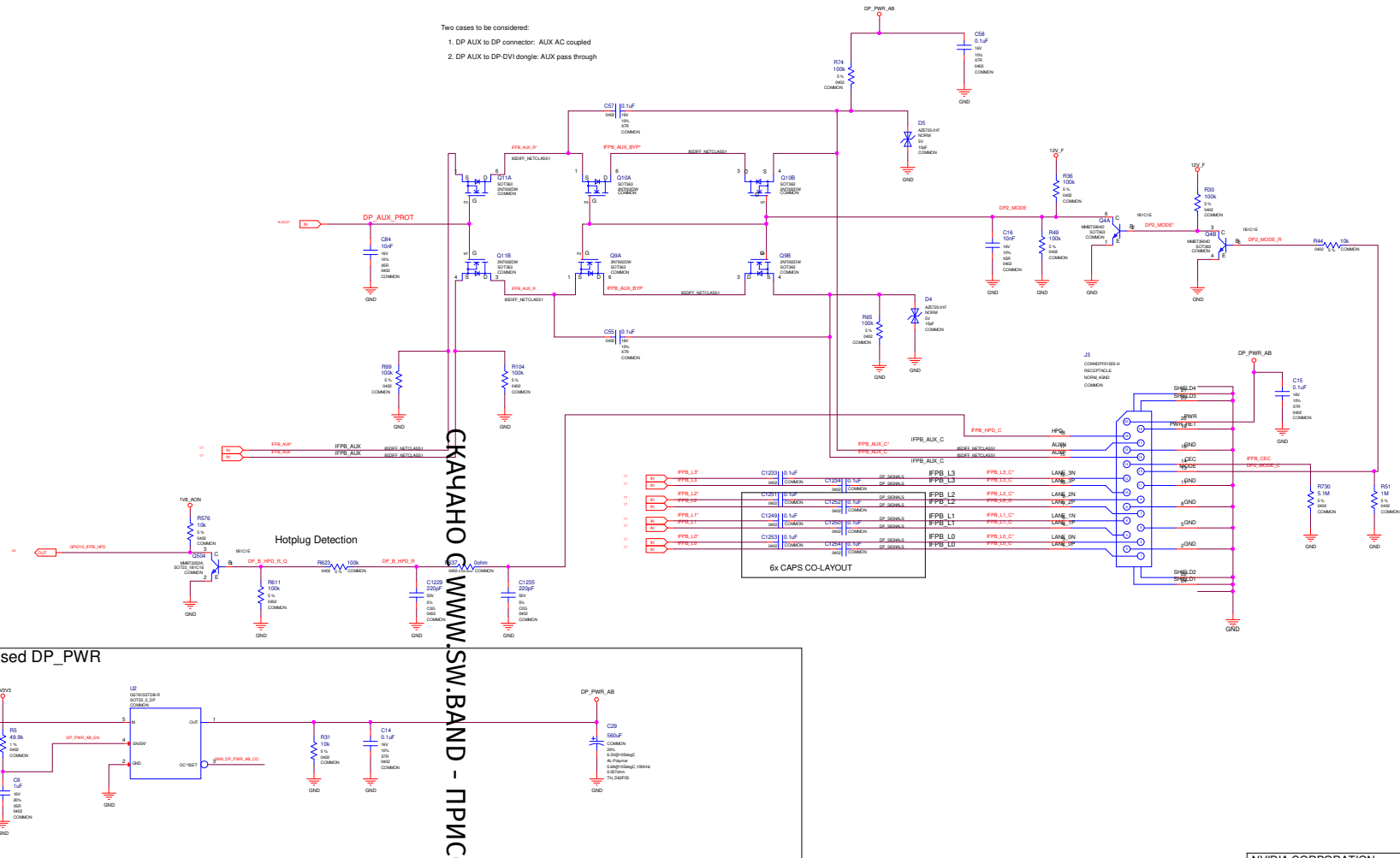
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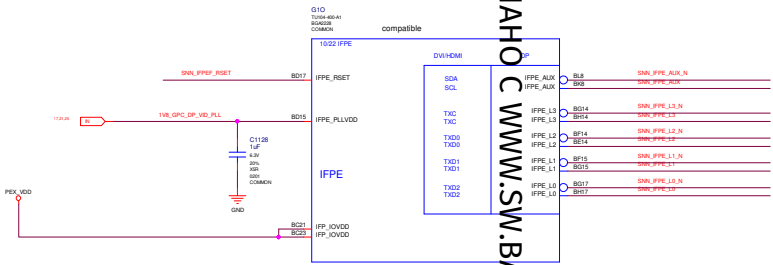
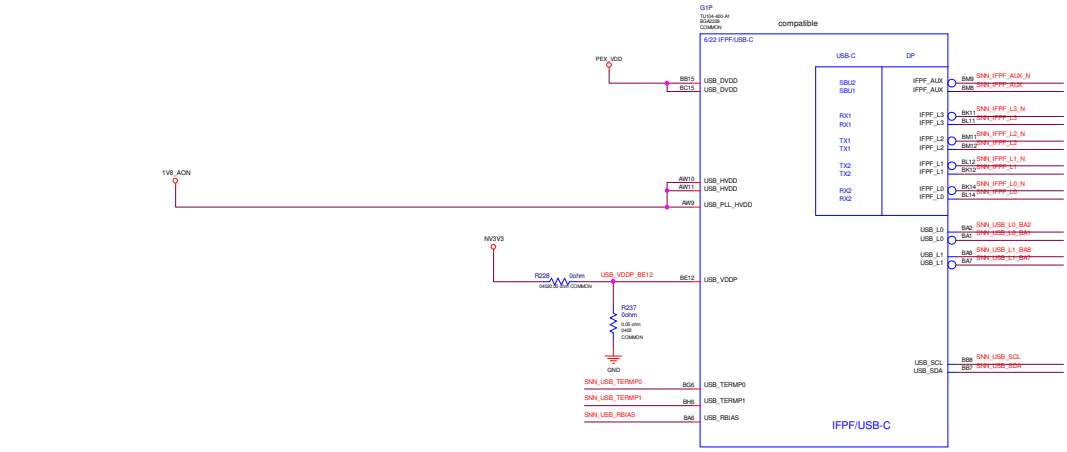


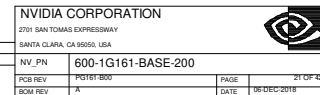


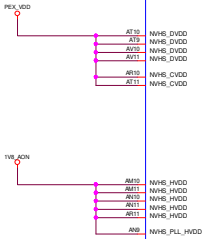


СКАЧАНО С WWW.SW.BAND - ПРИБОРАМИ









СКАЧАНО С WWW.SW.BAND - ILPRICOE.LINK

ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IO: NVHS INTERFACE AND FRAME LOCK

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STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
L	L	L	00000
L	L	H	00001
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
L	L	M	01000

RAMCFG[4:0]	DENSITY	WIDTH	VENDOR	DIE
00000	8Gb	256-bit	Samsung	C
00001	8Gb	256-bit	Micron	A
00010	8Gb	256-bit	Hynix	M
00011	8Gb	256-bit	Samsung	C
00100	8Gb	256-bit	Micron	A
00101	8Gb	256-bit	Hynix	M
00110	16Gb	256-bit	Samsung	M

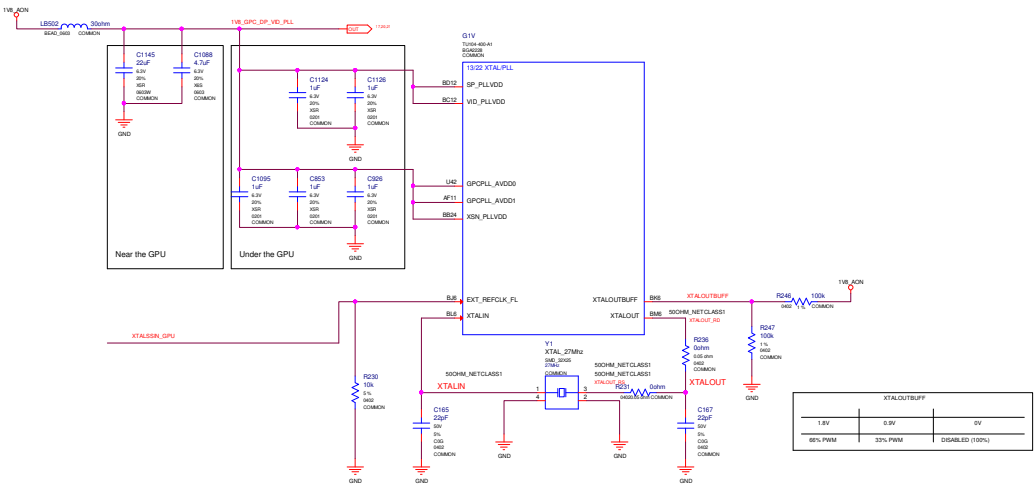
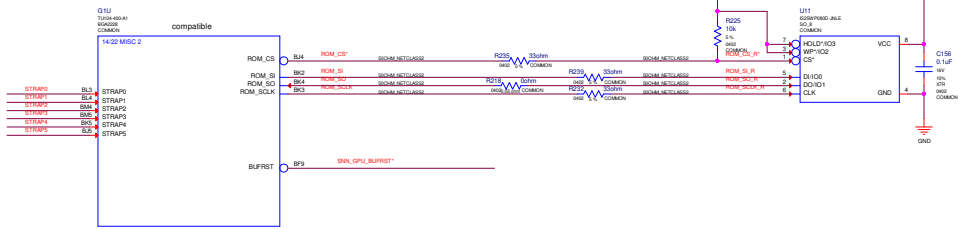
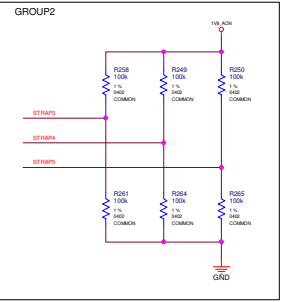
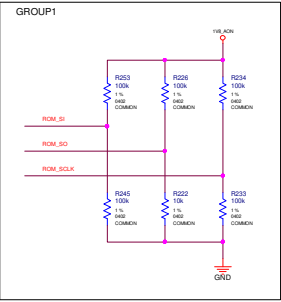
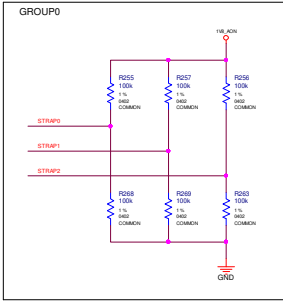
ROM_SO	ROM_SI	ROM_SCLK	DUMMY[2:0],FS_OVERT	1:ENABLE 0:DISABLE	DEFAULT
L	L	L	XXX1	FS_OVERT ENABLE	
L	L	M	XXX0	FS_OVERT DISABLE	

STRAPS	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE
1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL

1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER
1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE



XTALOUTBUFF		
1.8V	0.9V	0V
80% PWM	33% PWM	DISABLED (100%)



$$0.6V \cdot (1 + 10K/4.99K) = 1.802V$$

ASSEMBLY	<ASSEMBLY_DESCRIPTION
PAGE DETAIL	PS: 1V8_AON

NVIDIA CORPORATION
2701 SAN TOMAS EXPRESSWAY
SANTA CLARA, CA 95050, USA

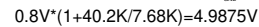


NV PN	600-1G161-BASE-200
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PCB REV	PG161-B00
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BOM REV	A	DATE	06-DEC-2018
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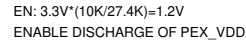
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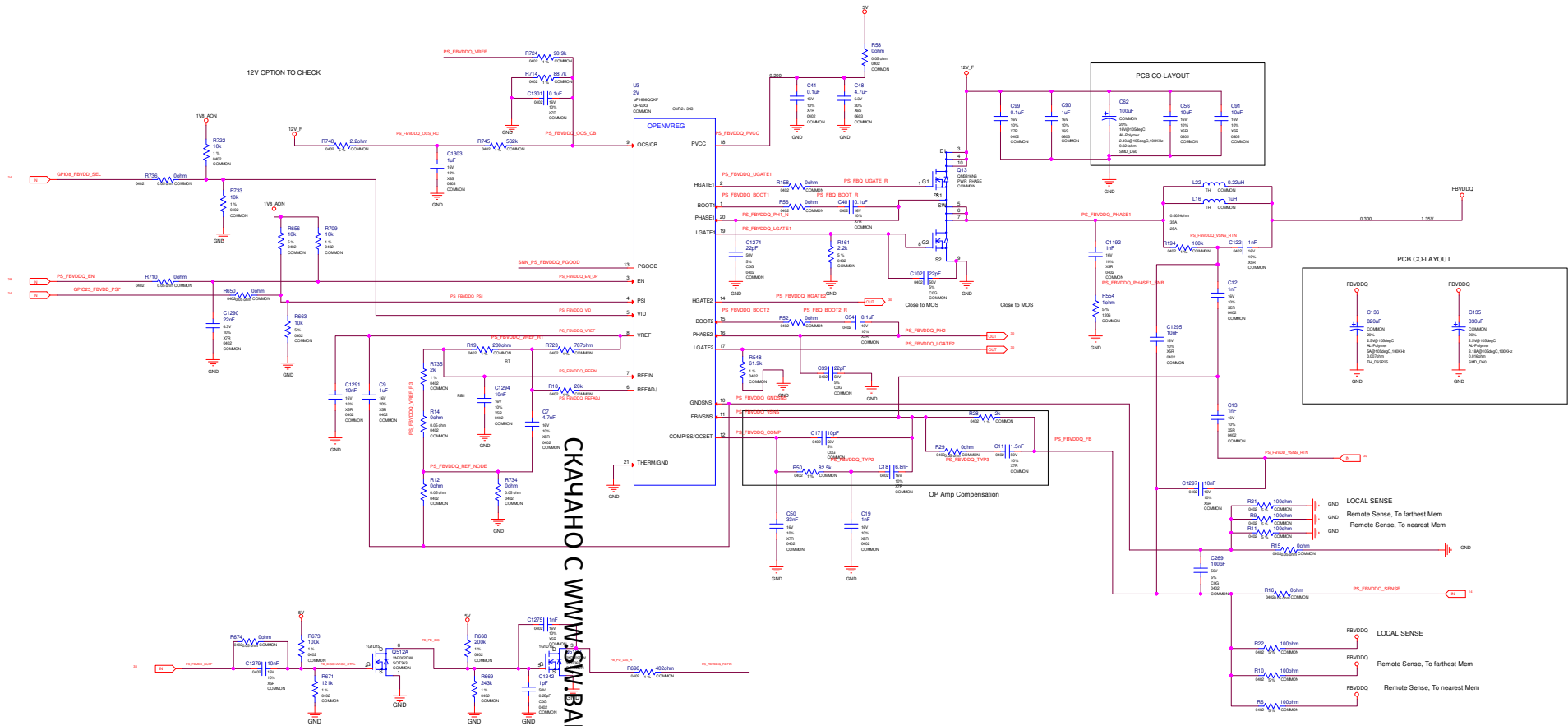
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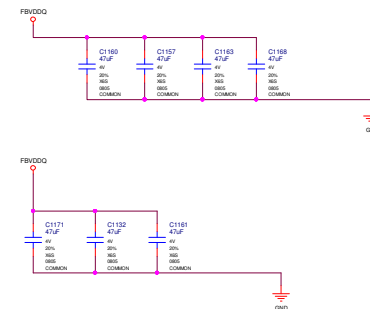
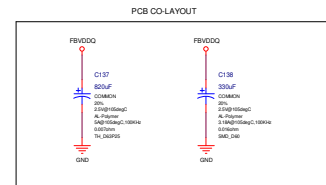
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СКАЧАНО С WWW.SIV.BAND - ПРИСОЕДИНИТЕСЬ



$$0.6V \cdot (1 + 10K/14.7K) = 1.008V$$



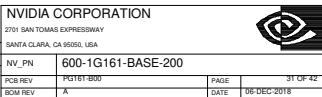


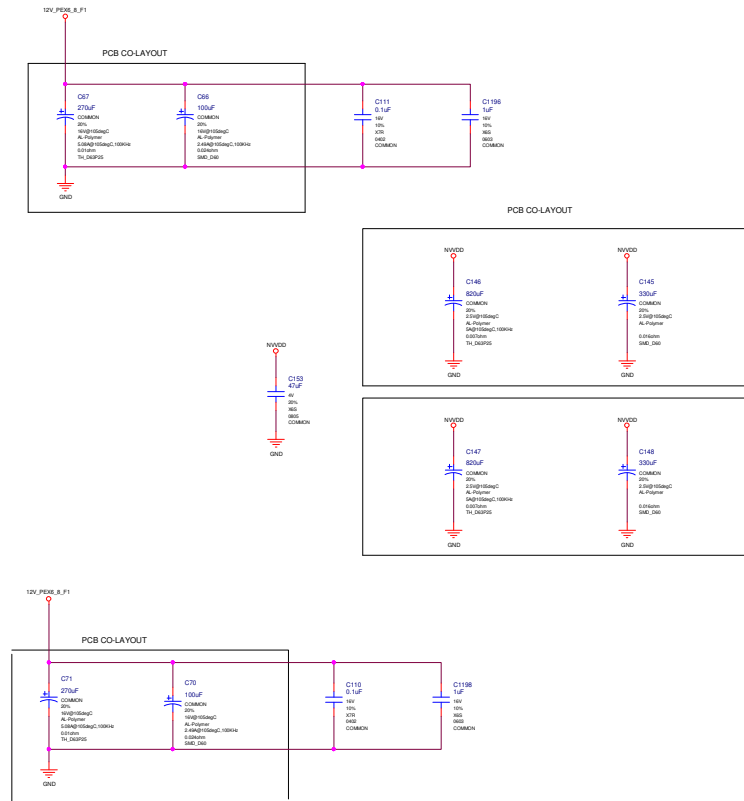
СКАЧАНО С WWW.S.W.BAND - ПРИСОЕДИНИТЕСЬ К НАМ

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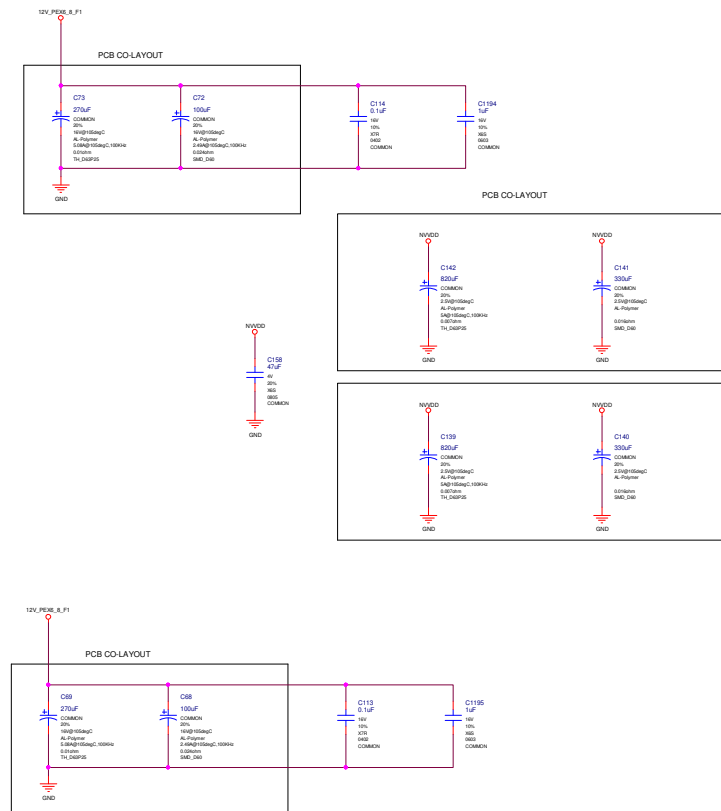






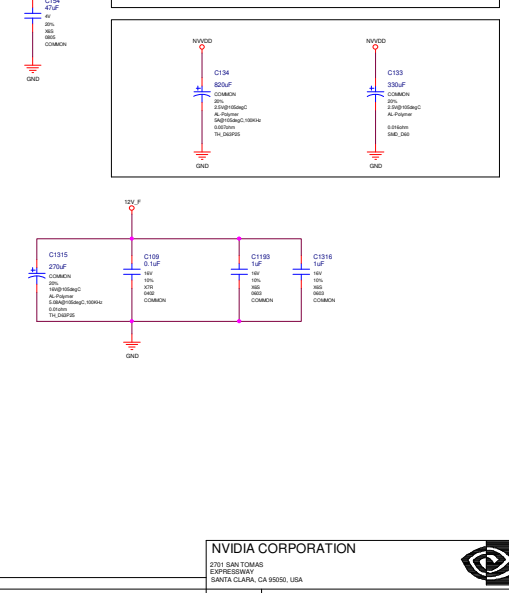
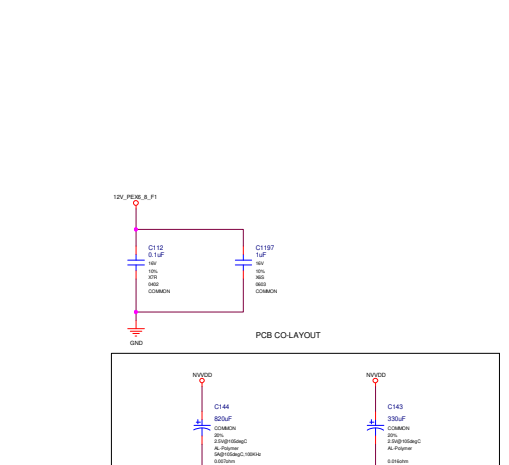
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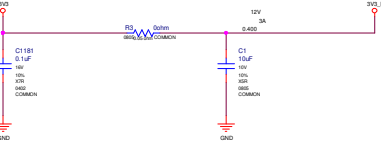
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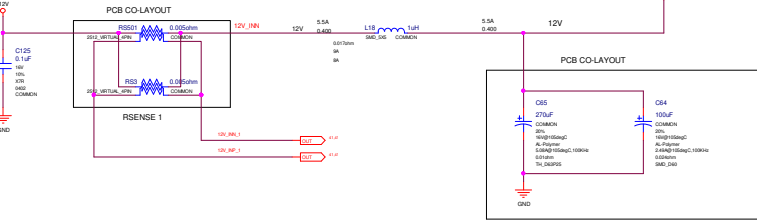


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NV_PN	600-1G161-BASE-200		
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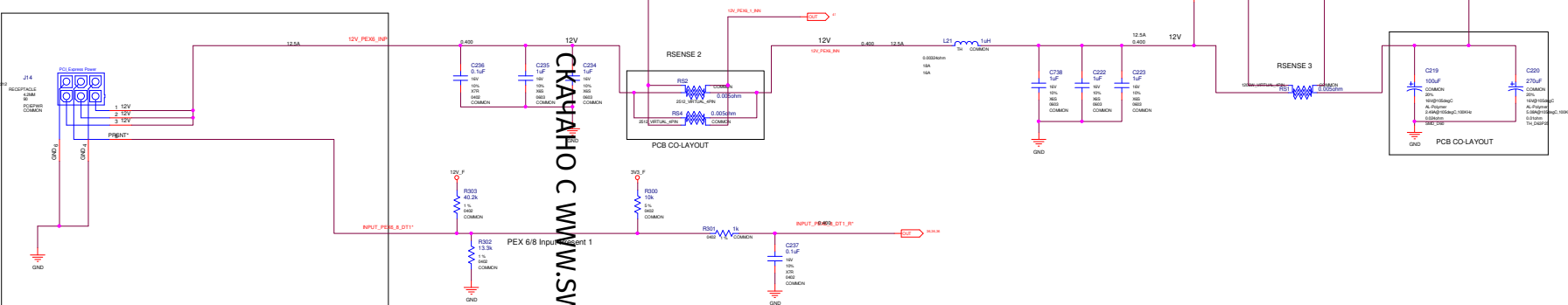
PEX 3V3 INPUT - 10W



PEX_12V INPUT - 66W



PEX8 INPUT 1 - 2x4 PCIe CON 150W



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ASSEMBLY	ASSEMBLY DESCRIPTION
PAGE DETAIL	PS: PEX_INPUT, 12V_EXT_INPUT, FILTERING

NVIDIA CORPORATION

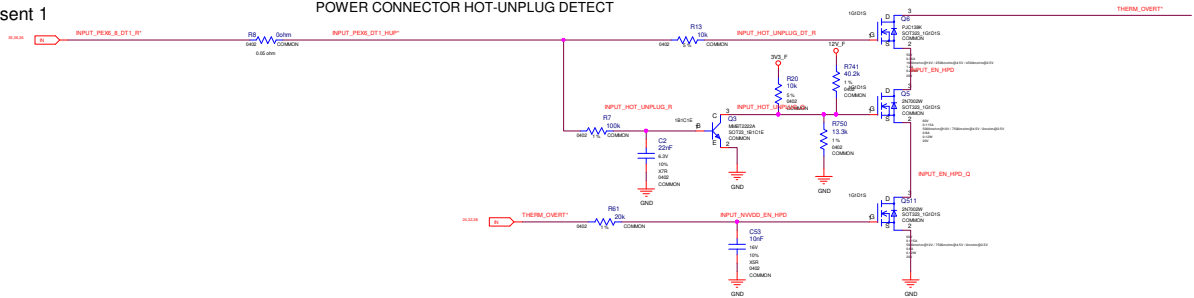
2701 SAN TOMAS
EXPRESSWAY
SANTA CLARA, CA 95050, USA



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PS: REV	PS:16T-000	DATE	06/DEC/2018
SOM REV	A		

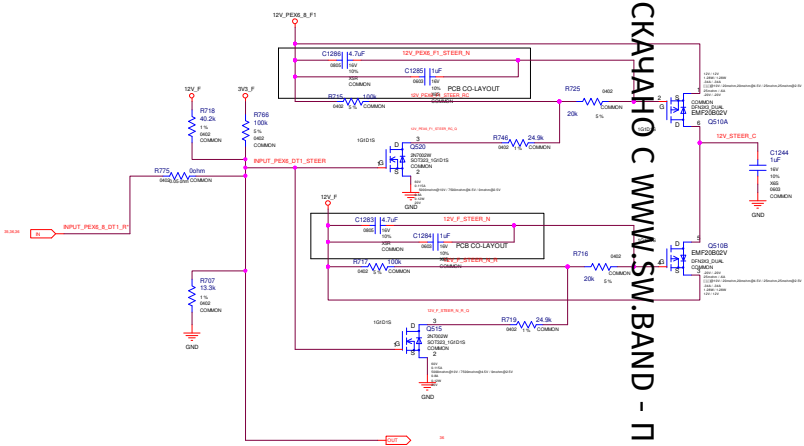
PEX Input Present 1

POWER CONNECTOR HOT-UNPLUG DETECT

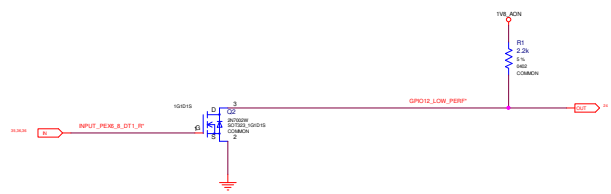


12V CURRENT STEERING (UNDER POWER BOOT):

GUIDES CURRENT FROM PEX EDGE TO PEX 6 PIN INPUT AREA



PEX input - Power Level/PSI* Control



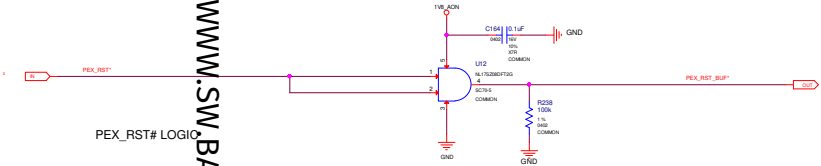
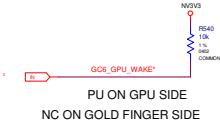
GPU12_LOW_PERR*		GPU SPEED	
0		Slow	
1		Normal	



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CK4YAH0 C WWW.SW BAND - ПРПСОЕДН

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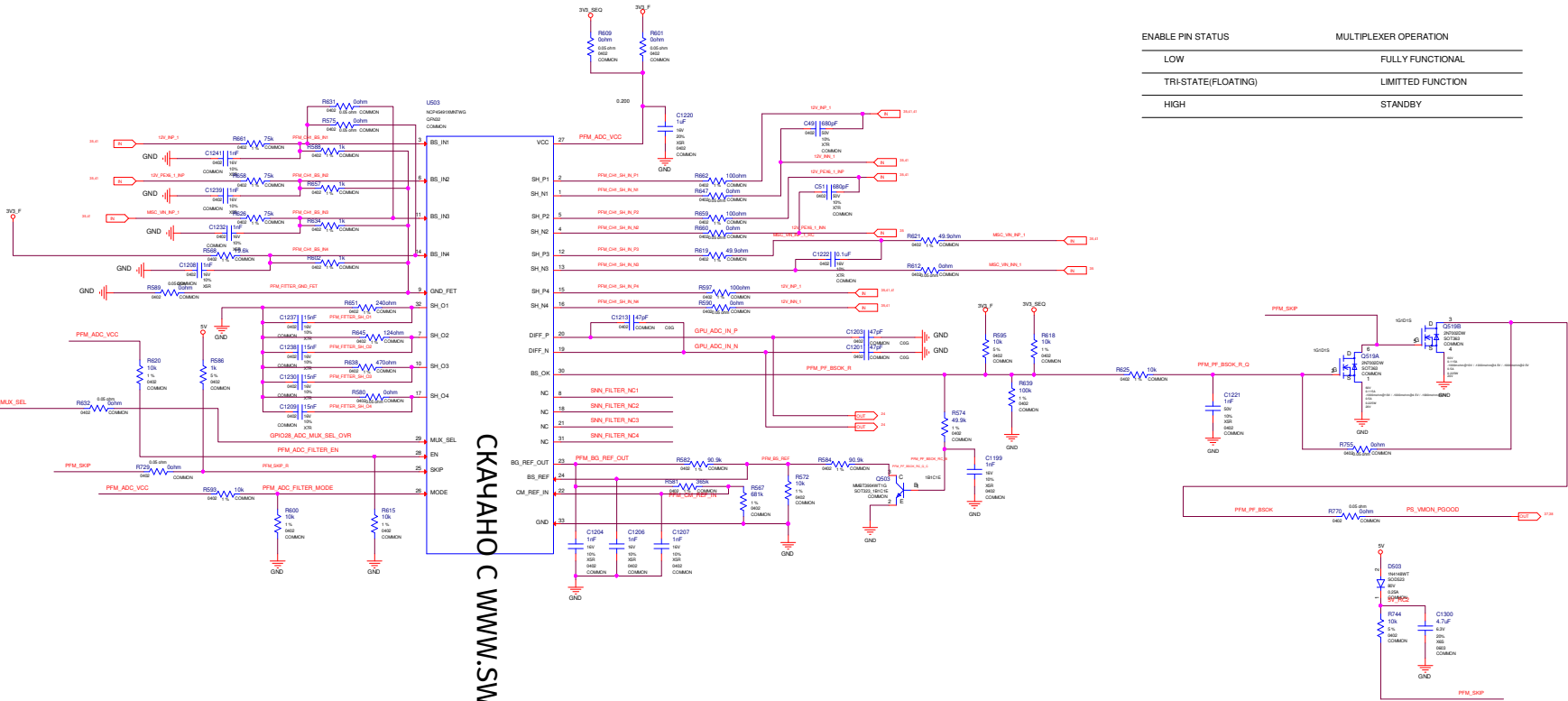
2701 SAN TOMAS
EXPRESSWAY
SANTA CLARA, CA 95050, USA



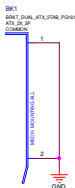
NV_PN	600-1G161-BASE-200		
PCB REV	PG161-B00	PAGE	40 OF 4
BOM REV	A	DATE	06-DEC-2018

MODE PIN STATUS	MULTIPLEXER OPERATION
LOW	DEVICE A
TRI-STATE(FLOATING)	STAND-ALONE
HIGH	DEVICE B

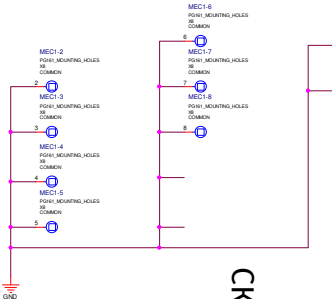
ENABLE PIN STATUS	MULTIPLEXER OPERATION
LOW	FULLY FUNCTIONAL
TRI-STATE(FLOATING)	LIMITED FUNCTION
HIGH	STANDBY



Bracket:



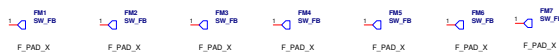
MOUNTING HOLES FOR HS:



BACK STIFFENER:



EMI Clip Options:



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