

GP104 - 8GB GDDR5X, 256b, 256Mx32
Tall DVI-D + DP + DP + DP/HDMI + DP

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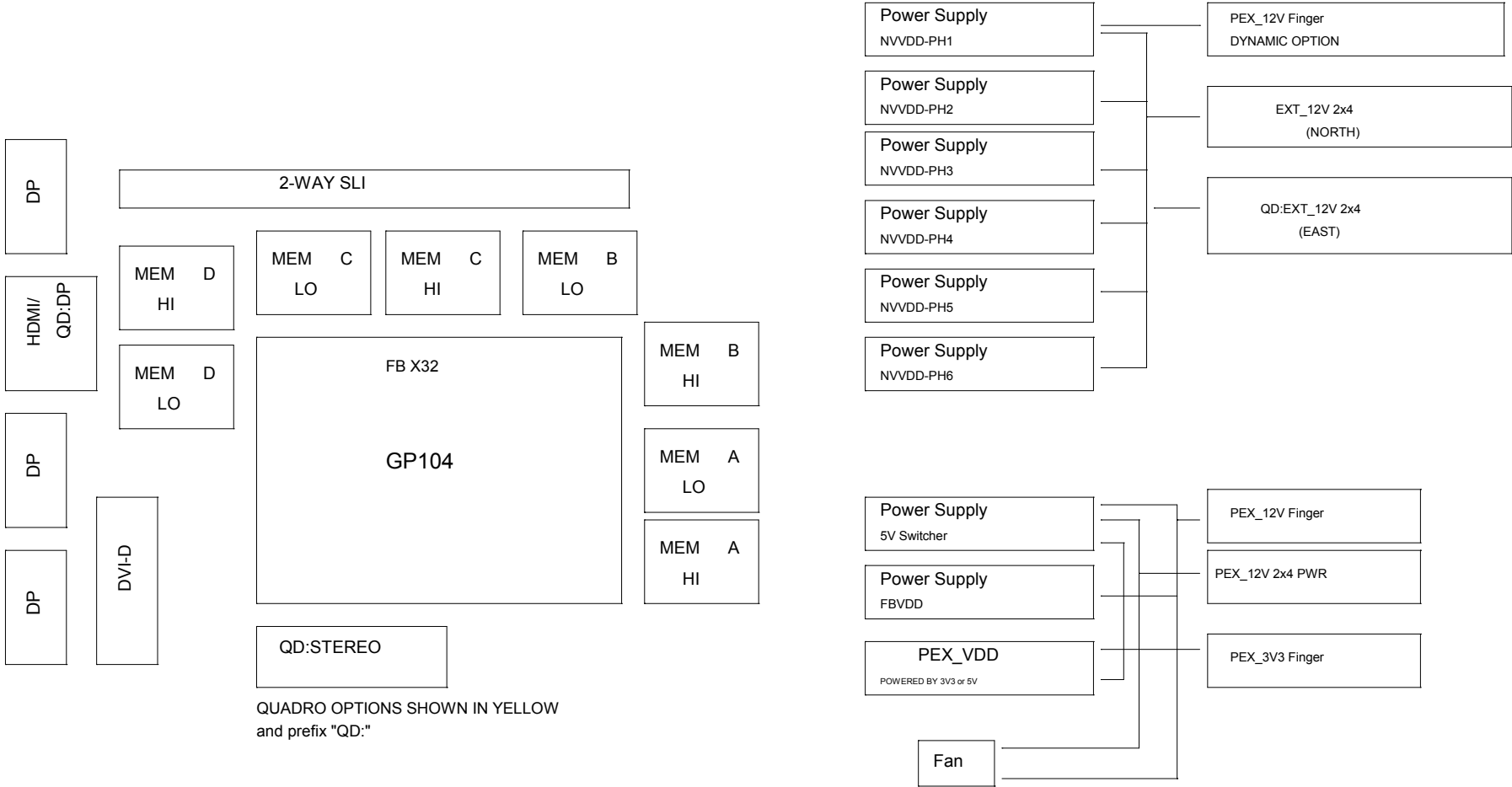
			
#PC <Title>			
Size	Document Number		Rev
Custom	GV-N1080AORUS X-GKD		1.0
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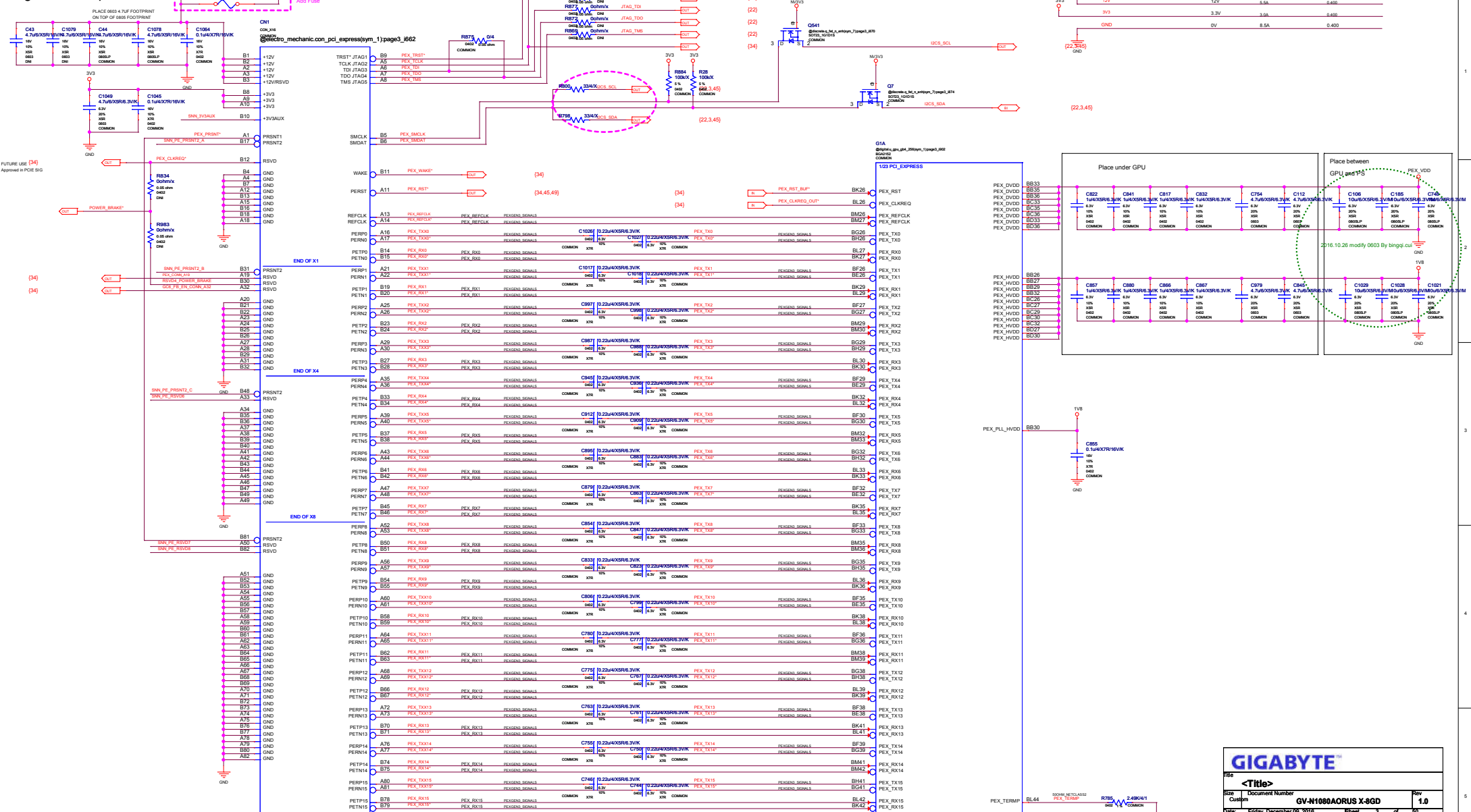
NV_PN 600-1G413-BASE-000	
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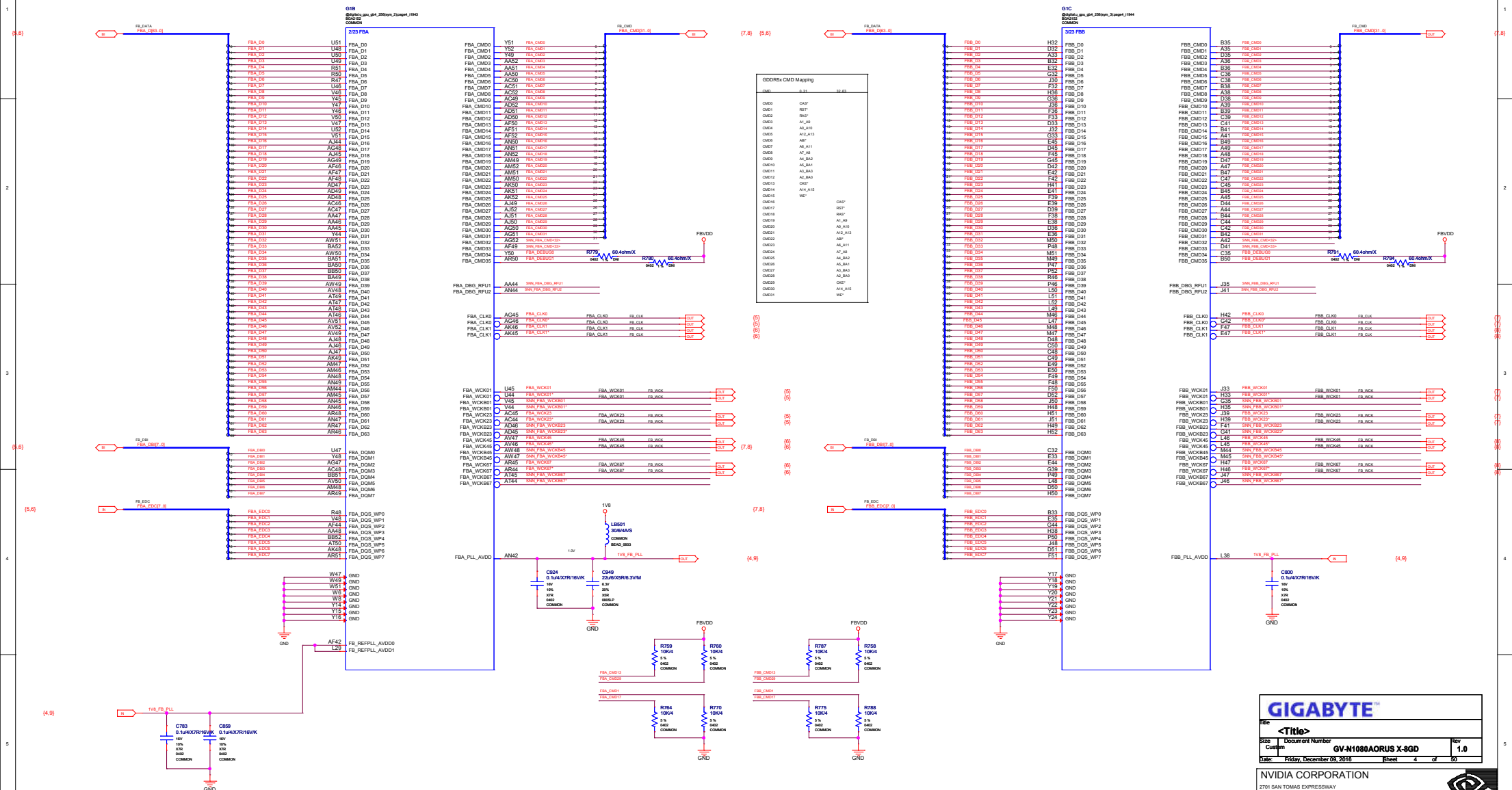
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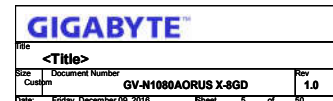
			
Title			
<Title>			
Size	Document Number		Rev
Custom	GV-N1080AORUS X-8GD		1.0
Date	Editor	Sheet	of
	December 06, 2014	1	50

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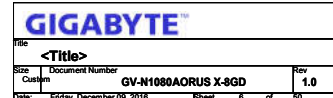
PCB REV	1G413-A00
---------	-----------

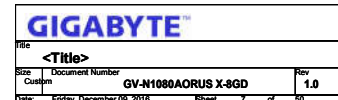
BOM REV	A
---------	---

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PAGE	5 OF 38
DATE	29-MAR-2016

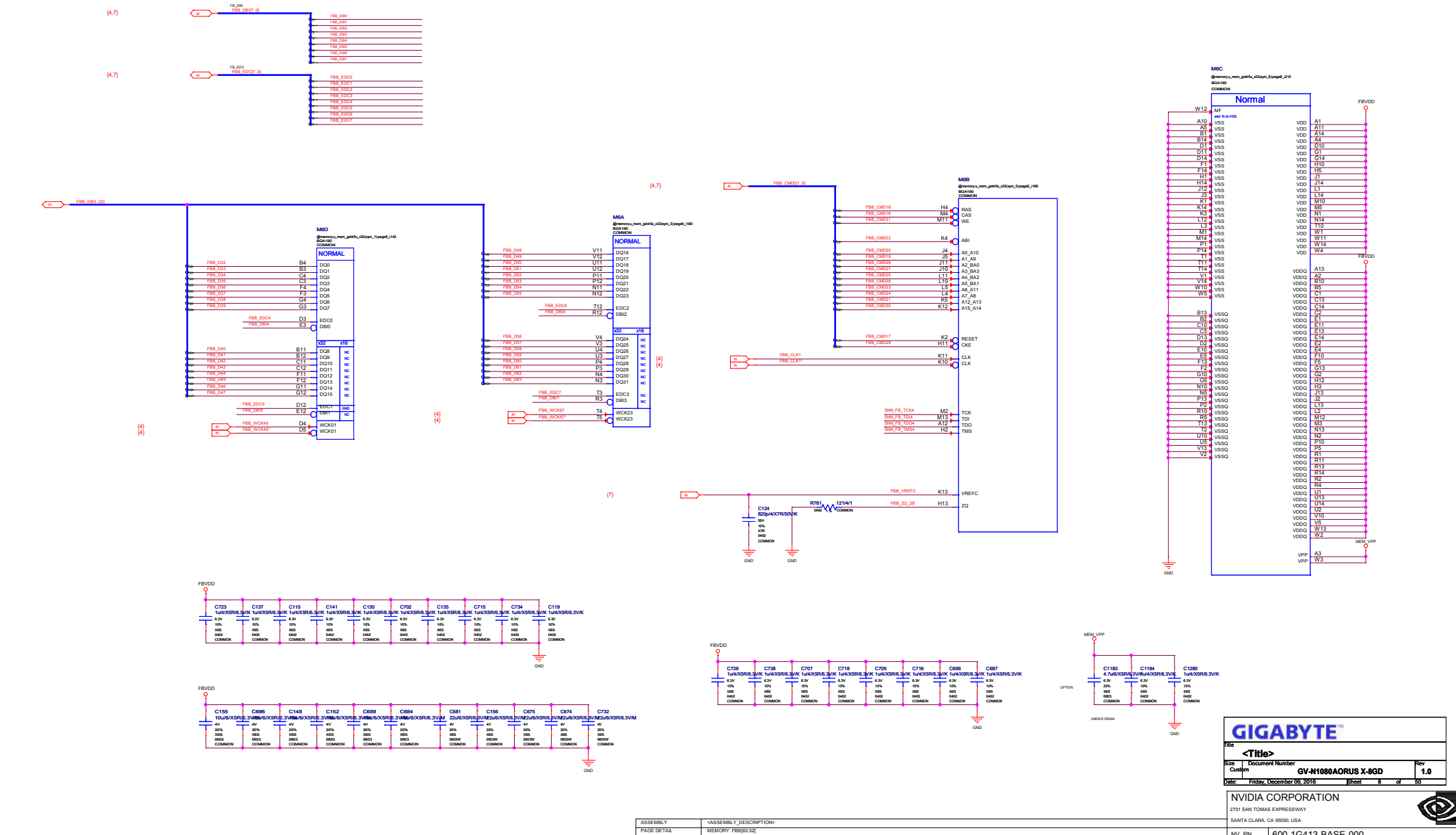
[illegible]

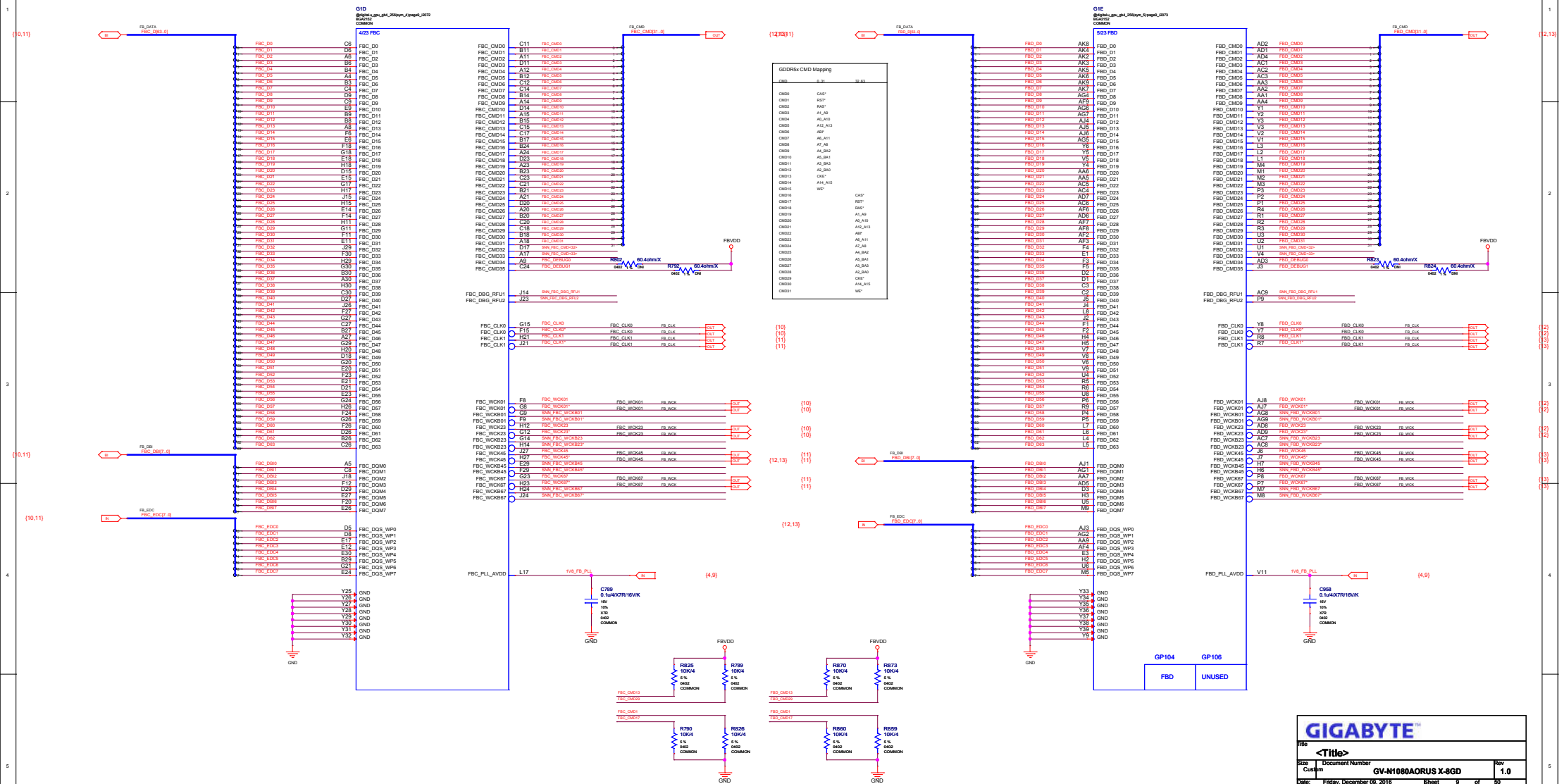


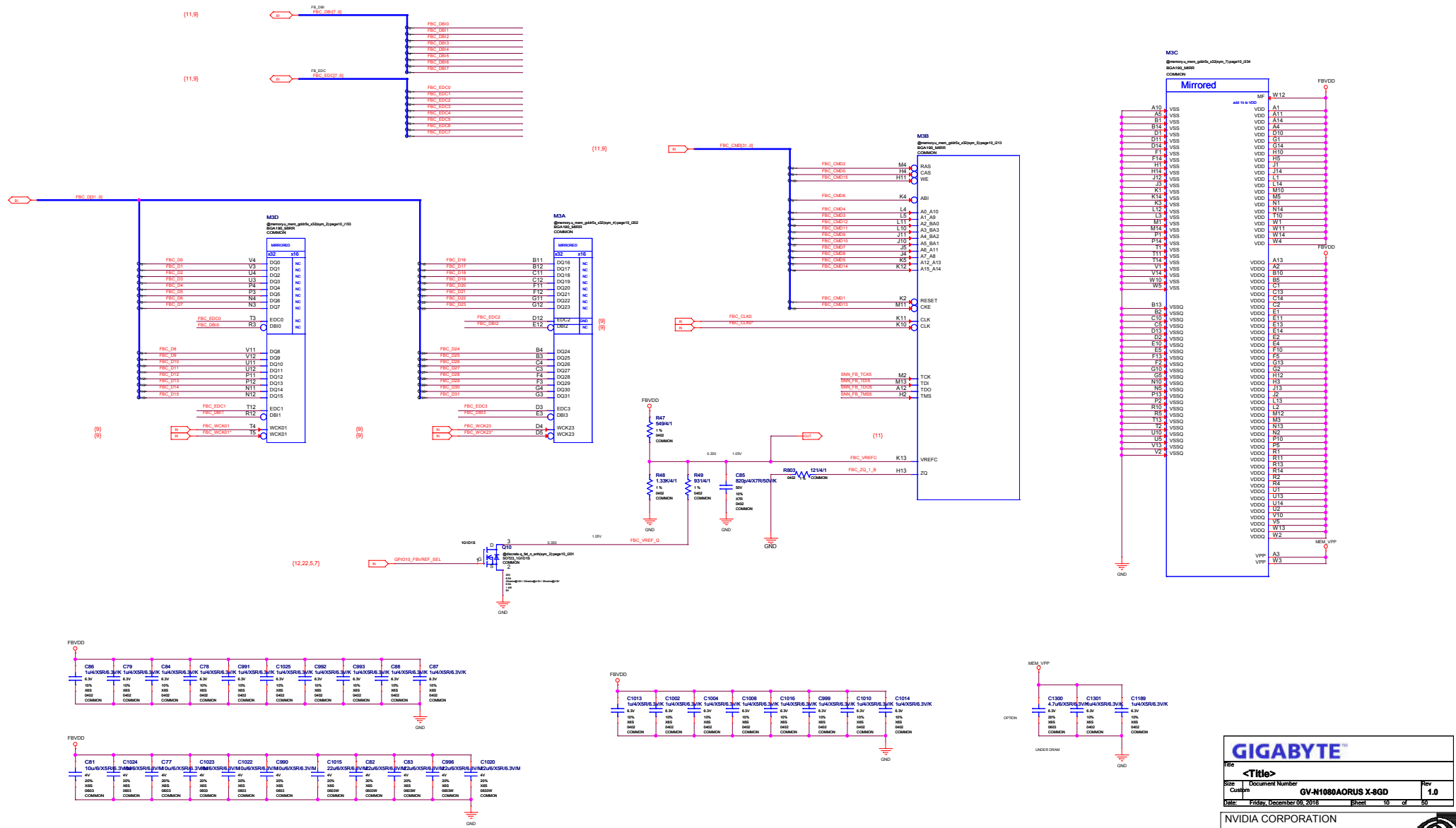


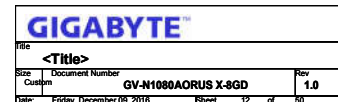
PCB REV	1G413-A00
---------	-----------

DATE	29-MAR-2016
------	-------------

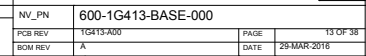


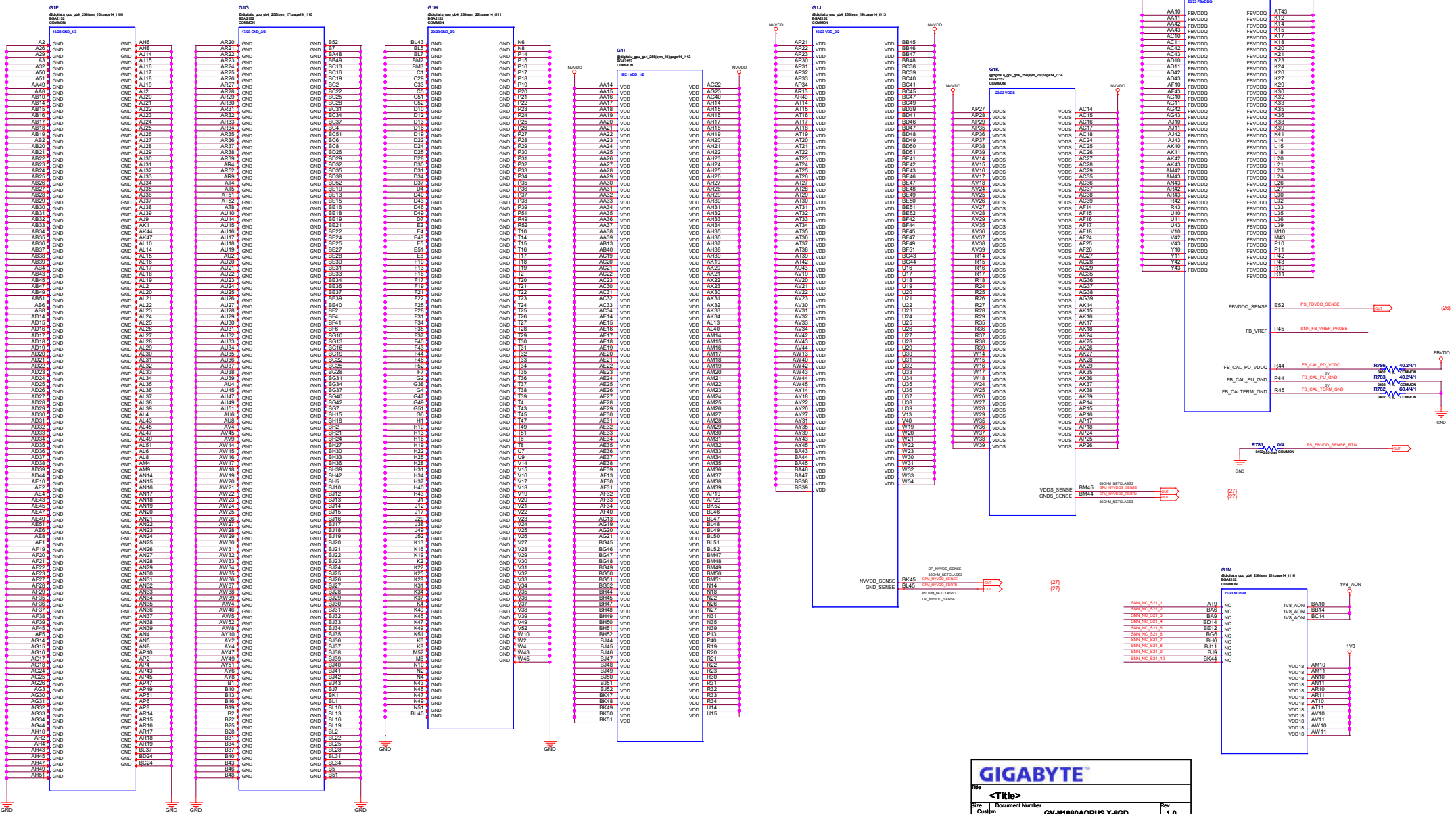




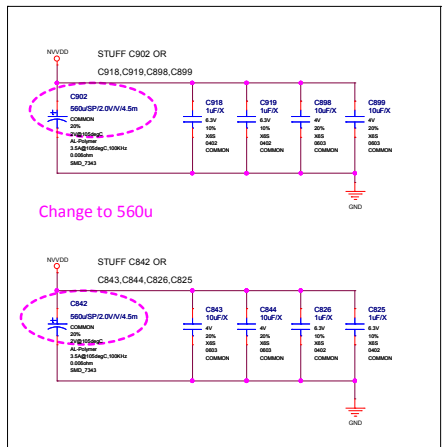
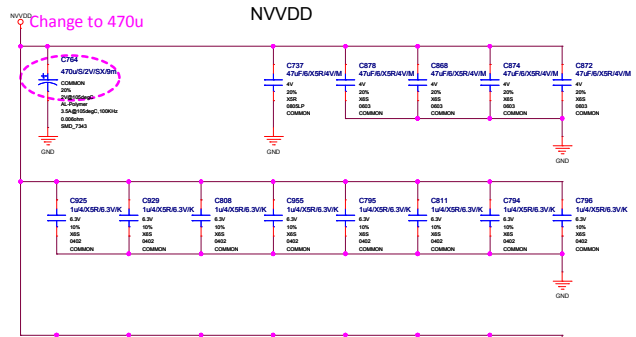
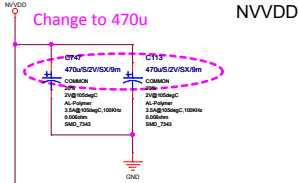
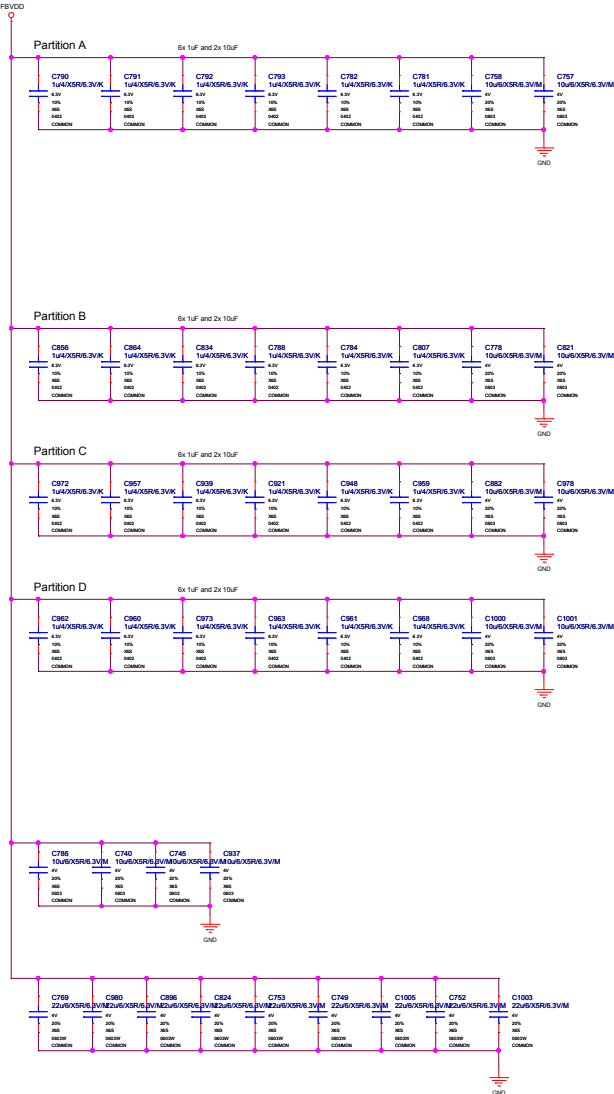


PCB REV	1G413-A00	PAGE	12 OF 38
---------	-----------	------	----------

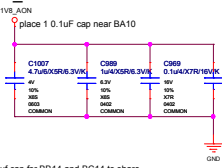




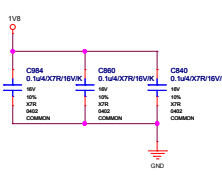
FBVDD



1V8_AON



VDD18



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File: <Title>
Size: Document Number: GV-N1080AORUS X-8GD Rev: 1.0
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ASSEMBLY	<ASSEMBLY DESCRIPTION>
PAGE: DETAIL	GPU Decoupling

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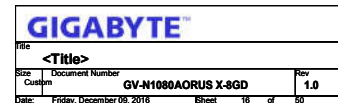
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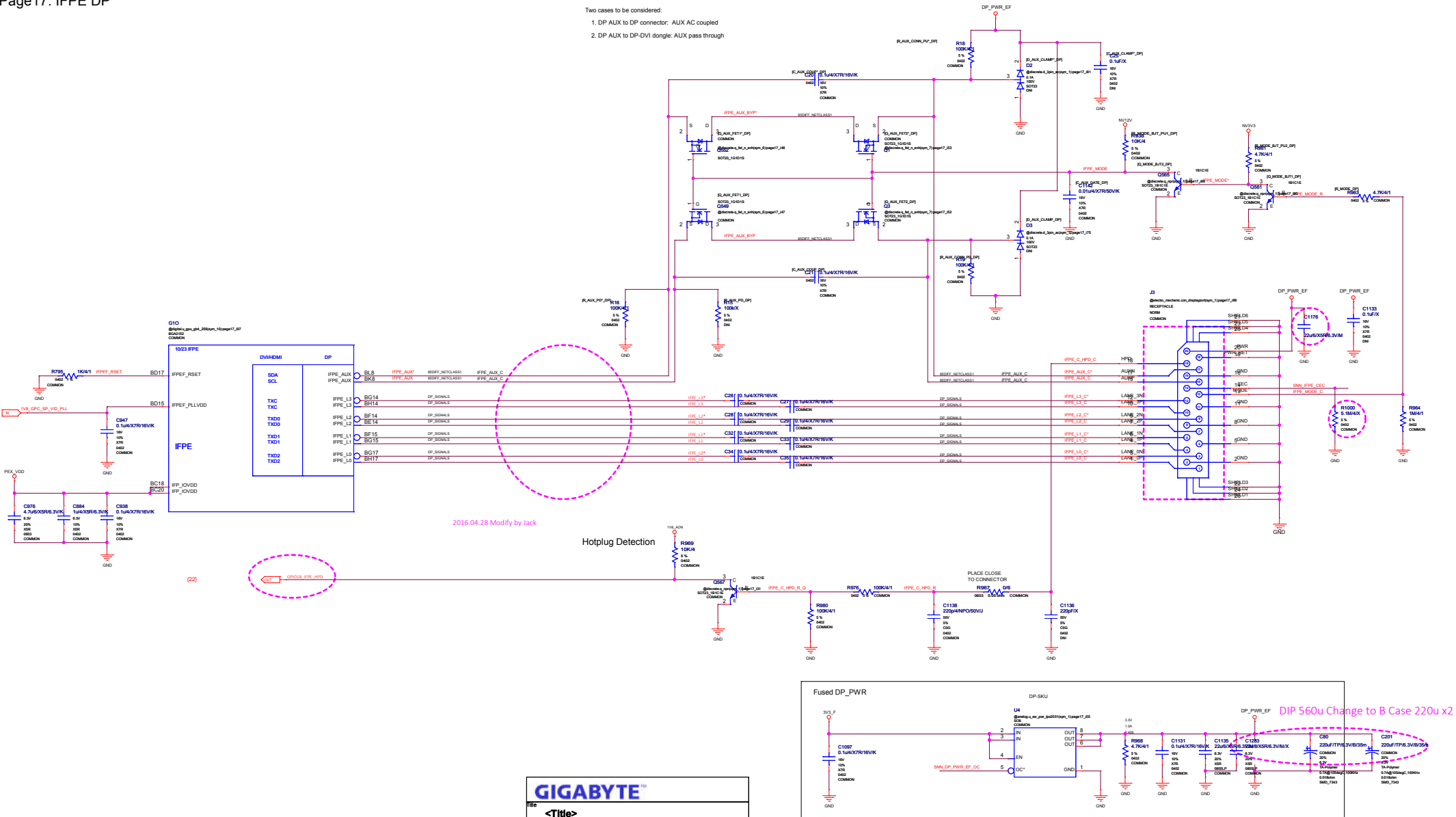
DATE: 25-MAR-2016

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	IFPAS DVI-D-DL

- Two cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP-DVI dongle: AUX pass through

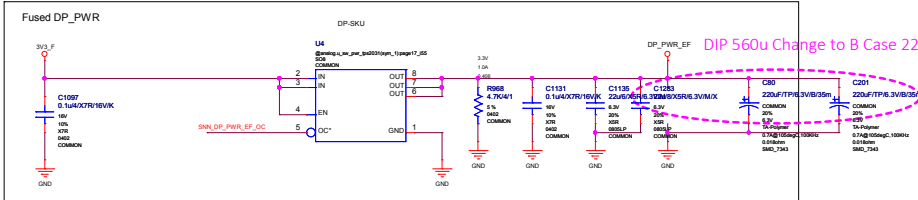


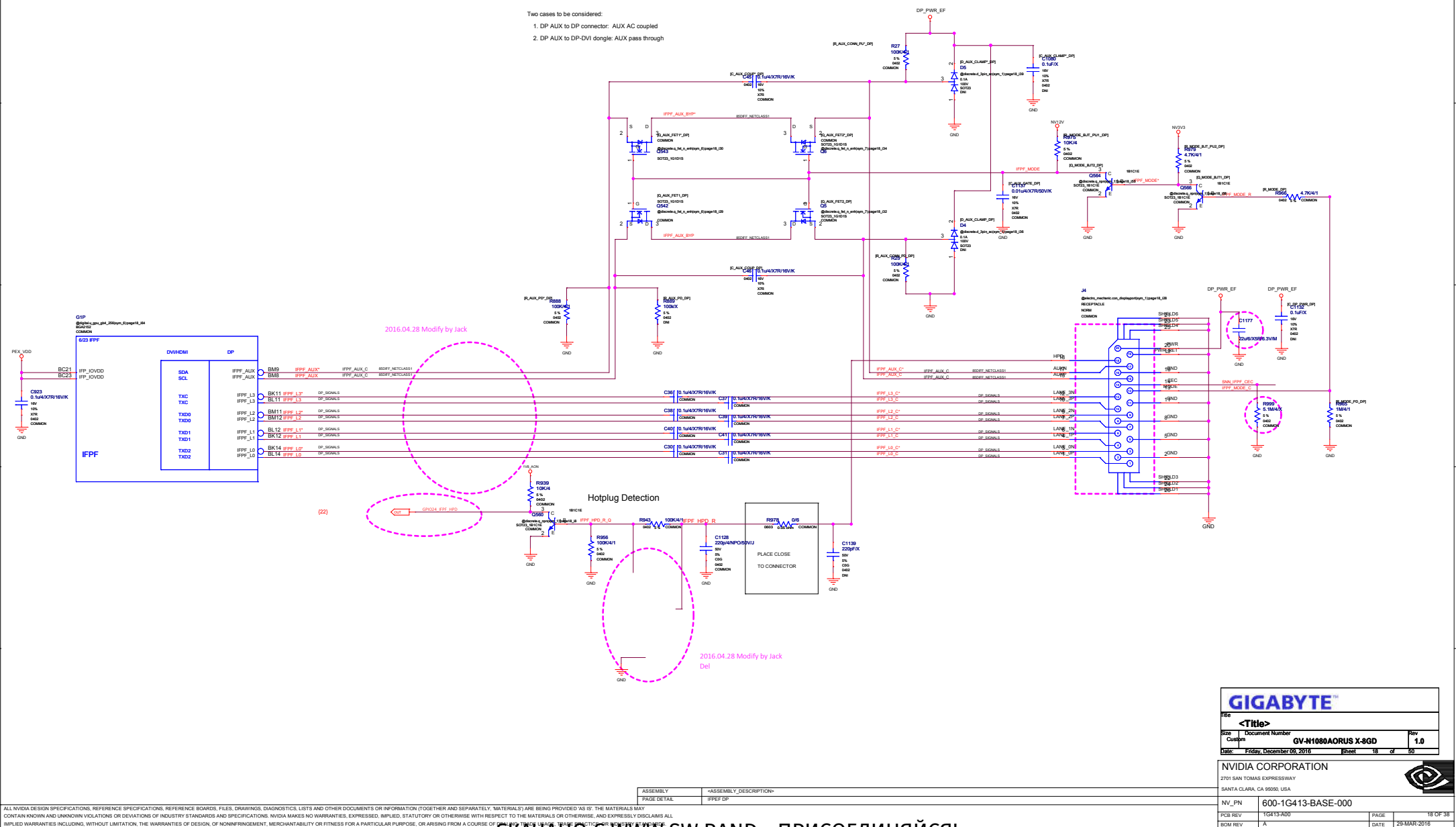
GIGABYTE

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Two cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through

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<Title>

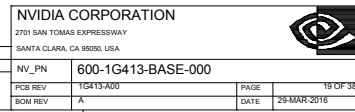
Size: 18441
Document Number: GV-N1080AORUS X-8GD
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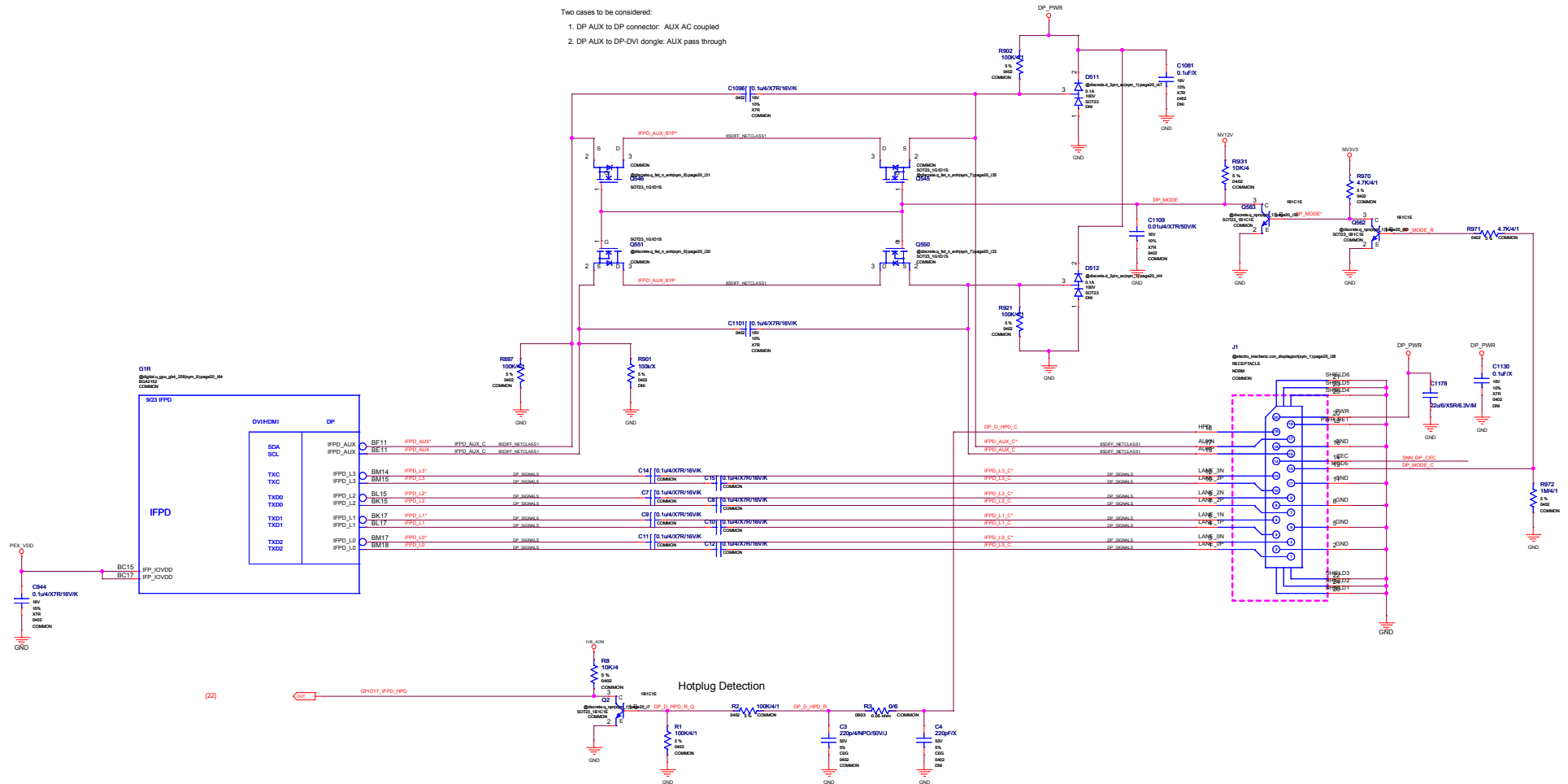
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1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through



			
File <Title>			
Size	Document Number		Rev
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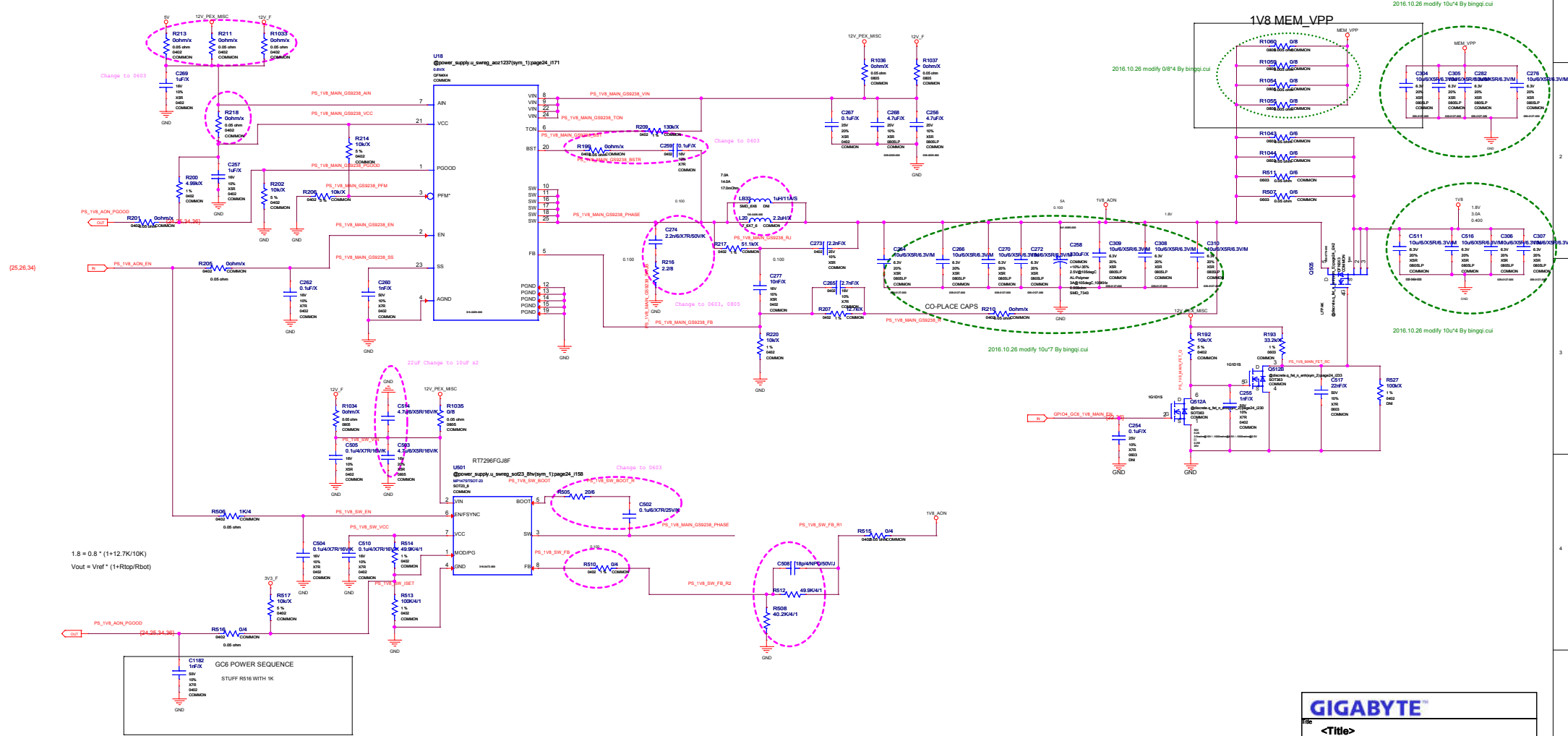
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GIGABYTE			
<Title>			
Size	Document Number	Rev	
Quantity	GV-N1080AORUS X-8GD	1.0	
Date		Friday, December 09, 2016	Sheet 24 of 50

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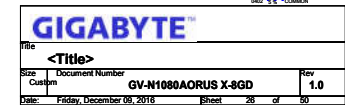
NV_PN	600-1G413-BASE-000	24 OF 38
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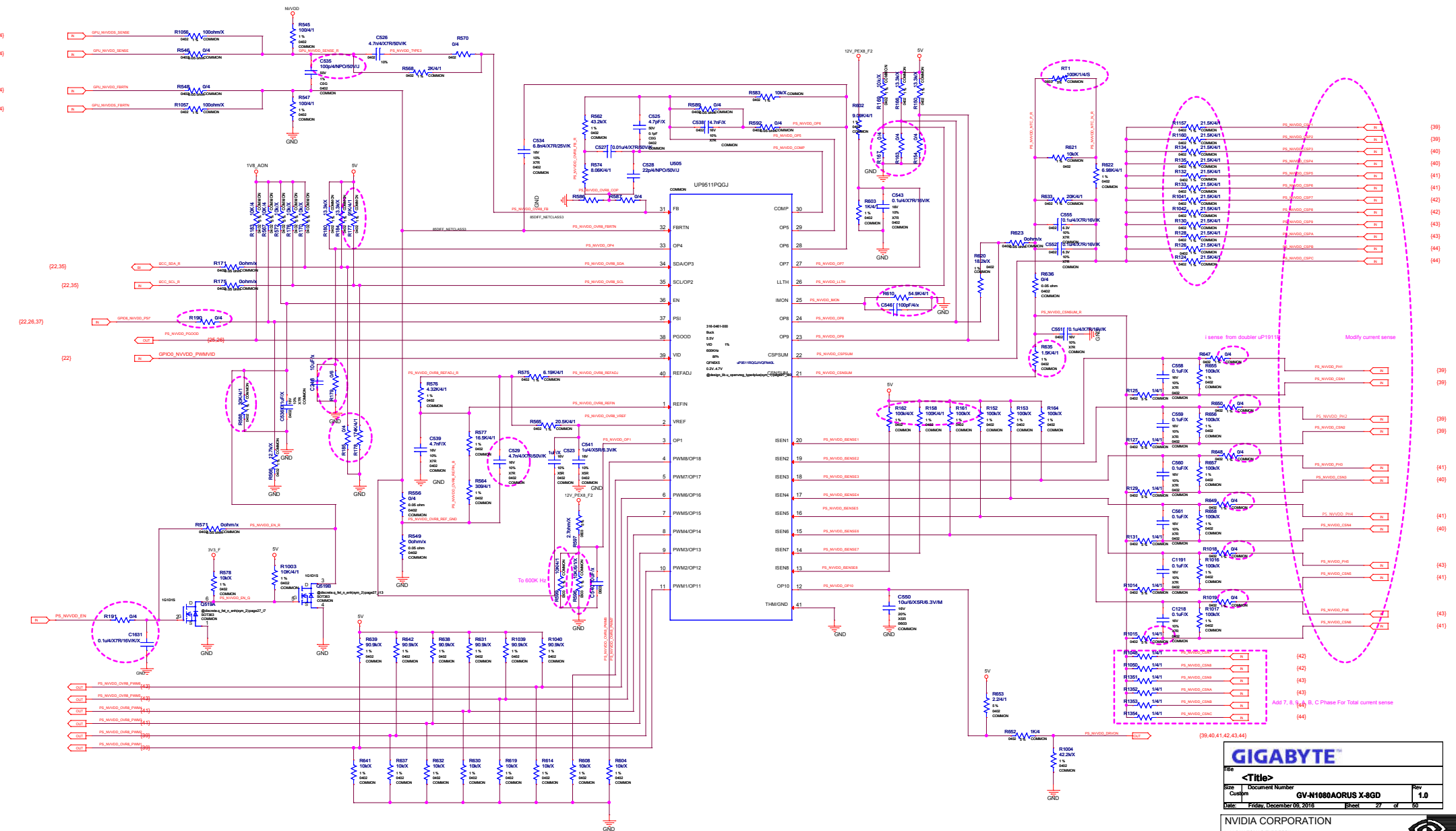
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PAGE DETAIL	PS: NVDD_OVR8

			
File <Title>			
Size	Document Number		Rev
Custom	GV-N1080AORUS X-8GD		1.0
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FEEDO

C1116 22u/6XSR/6.3V/M

6.3V
20%
XSR
6063
COMMON

C1117 22u/6XSR/6.3V/M

6.3V
20%
XSR
6063
COMMON

C1118 22u/6XSR/6.3V/M

6.3V
20%
XSR
6063
COMMON

C1119 22u/6XSR/6.3V/M

6.3V
20%
XSR
6063
COMMON

GND

GND

GND

GND

空間關係刪除

The schematic diagram illustrates a 4-channel differential signal conditioning circuit. It consists of four identical stages connected in series. Each stage is composed of a 22uF/5XSR/6.3V capacitor (labeled C1256, C1257, C1258, and C1259) connected to a 3.3V supply, followed by a 20k resistor (labeled R1256, R1257, R1258, and R1259) connected to a common ground. The output of each stage is connected to the input of the next stage. The final output is connected to a 0VDD pin.

The schematic diagram shows a power supply section with four electrolytic capacitors connected in parallel to a common ground. The capacitors are labeled C1222, C1230, C1231, and C1232. Each capacitor has a value of 22uF and a voltage rating of 35V. The capacitors are connected to a common ground and a 5V supply line. The capacitors are labeled with their values and ratings: 22uF, 35V, 20%, 100, 0603, COMMON. The capacitors are connected to a common ground and a 5V supply line.

The diagram illustrates a 4-channel differential signal conditioning circuit. It features four identical stages connected in series. Each stage includes a 22uF/6.3V electrolytic capacitor (C1201-C1204) in parallel with a 30k resistor (X301-X304) and a 6.3V Zener diode (X301-X304) connected to a common ground. The output of each stage is connected to the input of the next stage. The final output is connected to a 5VDD supply.

The schematic diagram illustrates a four-channel differential signal input circuit. It consists of four identical channels, each represented by a vertical line with a horizontal branch at the top. Each channel has two input terminals labeled 'XSR' and 'XSD'. A 1.3V input signal source is connected to the 'XSR' terminal of each channel. The 'XSD' terminal of each channel is connected to a common ground. The input impedances for each channel are specified as 22u/6/X5R/6.3V/1M. The output of each channel is connected to a common output line, which is labeled 'OUT' at the top right.

The diagram shows a 4-stage common-emitter amplifier. The input signal is applied to the base of the first stage (C1205). The output of each stage is taken from the collector and coupled to the base of the next stage. The output of the fourth stage (C1208) is taken from the collector and connected to a speaker. Each stage has a common-emitter configuration with a collector resistor and a common emitter connection to ground.

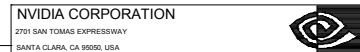
The schematic diagram shows a four-stage common-emitter amplifier. The input signal is applied to the base of the first stage (C1237). The output of the first stage is connected to the base of the second stage (C1238). The output of the second stage is connected to the base of the third stage (C1239). The output of the third stage is connected to the base of the fourth stage (C1240). The output of the fourth stage is taken from the collector of C1240. All emitter resistors (XSR) are connected to ground (GND).

The diagram shows a 4-channel differential signal distribution network. A single input signal from a 'NUT' source is split into four parallel channels. Each channel consists of a series resistor (C1268, C1269, C1270, C1271) followed by a parallel combination of a resistor (22u/0X5R6, 3V/M) and a capacitor (6.3V, 20%, 102, 0603). All capacitors are connected to a common ground.

The diagram shows a 4-stage ladder network for a 100 dB attenuator. The input is labeled IN (VCC) and the output is labeled OUT (VCC). The network consists of four stages, each with a series resistor and a shunt resistor to ground. The series resistors are labeled C1244, C1245, C1246, and C1247. The shunt resistors are labeled 22u/6XSR/6.3V/M, 6.3V, 20%, 10%, 0.003, and COMMON. The ground connections are labeled GND.

The diagram shows a 4-stage cascaded common-emitter amplifier. The input signal is applied to the base of the first stage (C1248). The output of each stage is coupled to the next stage's base through a 10k resistor. The output of the final stage (C1251) is taken from the collector and connected to a 1k load resistor. All emitter resistors are 1k and connected to ground. All collector resistors are 10k and connected to a common 12V supply. The supply is also connected to the bases of the second, third, and fourth stages through 10k resistors.

The diagram illustrates a 4-channel differential signal source. It consists of four identical channels, each represented by a 125Ω resistor in series with a 22pF capacitor. The capacitors are connected to a common ground. The resistors are connected to a common output line, which is also connected to a 5V supply. The output line is labeled 'VDD'.



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Title <Title>			
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File <Title>			
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Custpm	GV-N1080AORUS X-8GD		1.0
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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: Blank Page

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ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: Dynamic Power Balance Phases

1

Delete

			
File			
<Title>			
Size	Document Number		Rev
Custom	GV-N1080AORUS X-8GD		1.0
Date	Friday, December 08, 2018	Sheet 33 of 50	

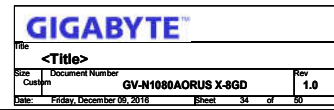
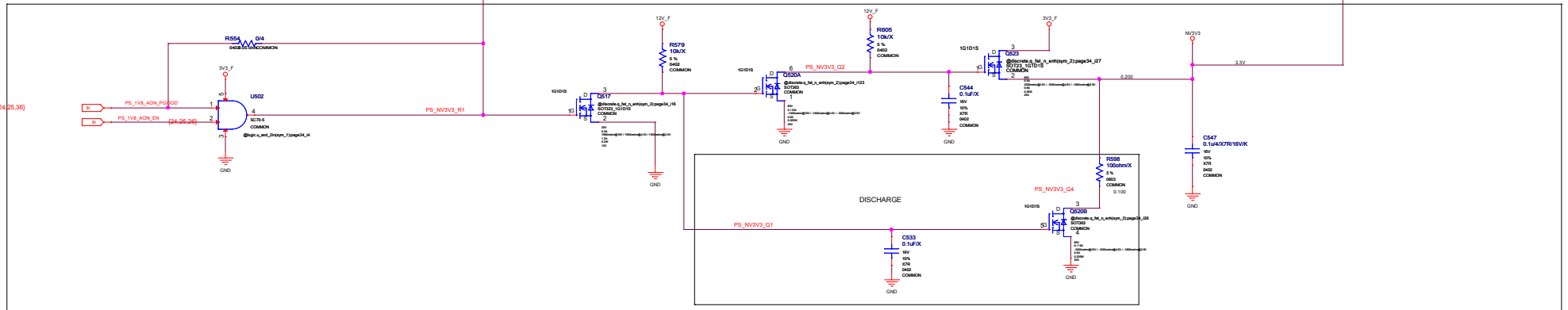
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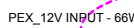
ASSEMBLY		ASSEMBLY DESCRIPTION		SANTA CLARA, CA 95050, USA	
PAGE DETAIL		PS: Dynamic Power Balance Logic			
5 MAY 2016		REV. PN		600-1G413-BASE-000	
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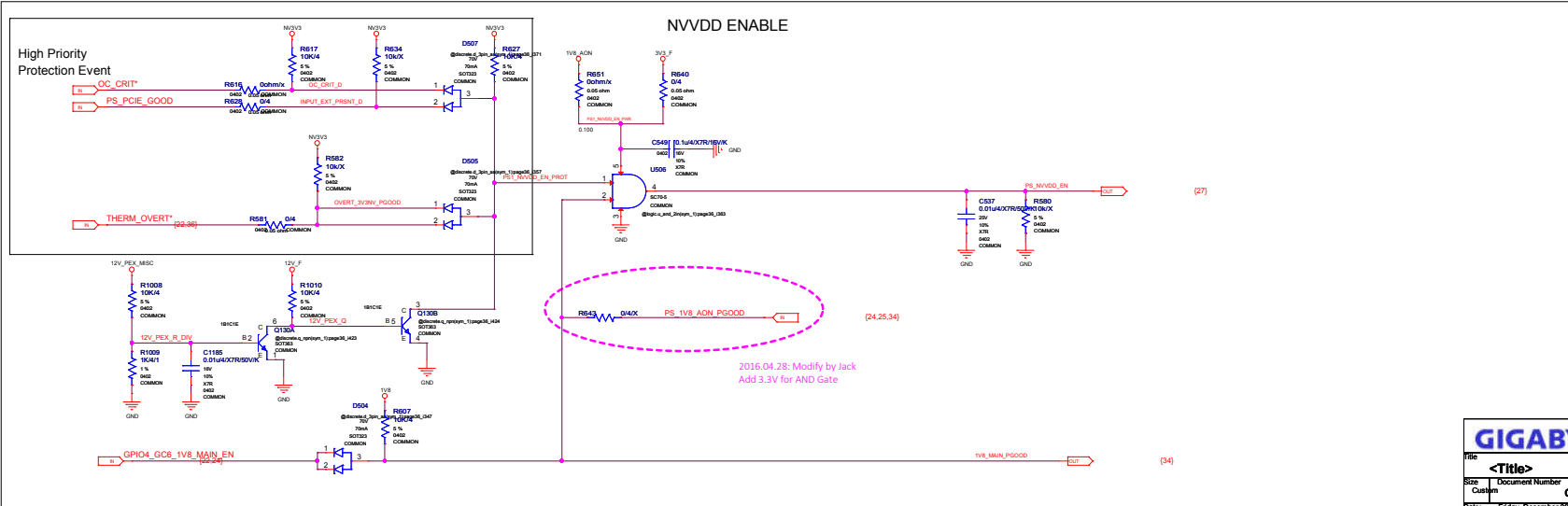
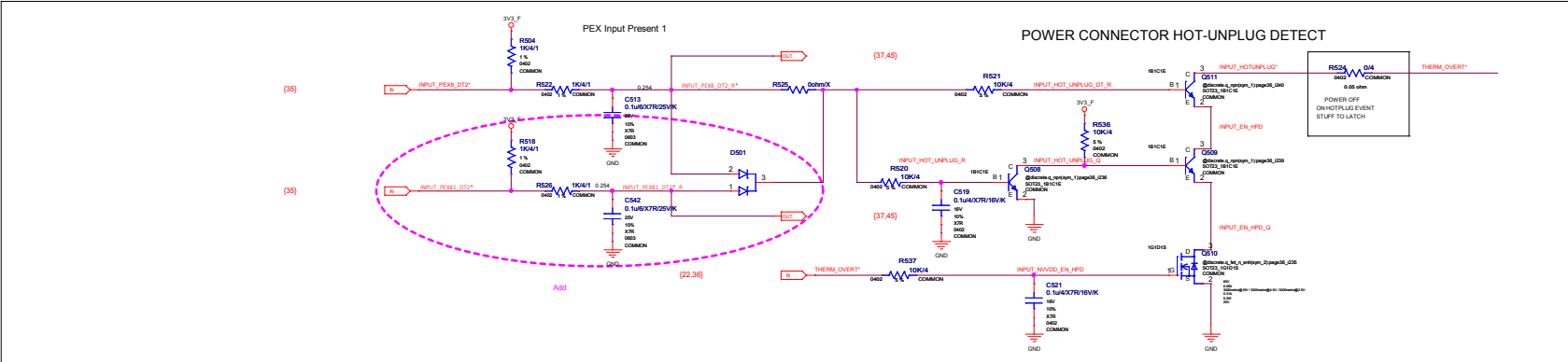
С КАЧАНО С WWW.SW.BAND - ПРИСОЕДИНЯЙСЯ!



ASSEMBLY	<ASSEMBLY_DESCRIPTION>
PAGE DETAIL	PS: NV3V3, NV12V



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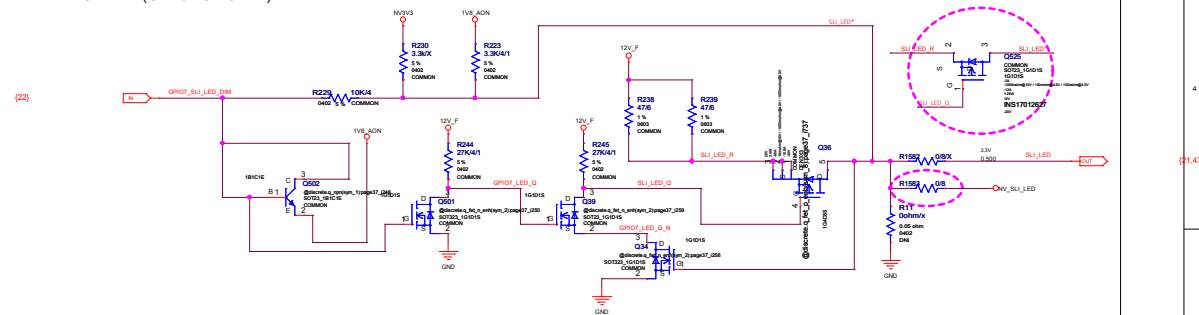
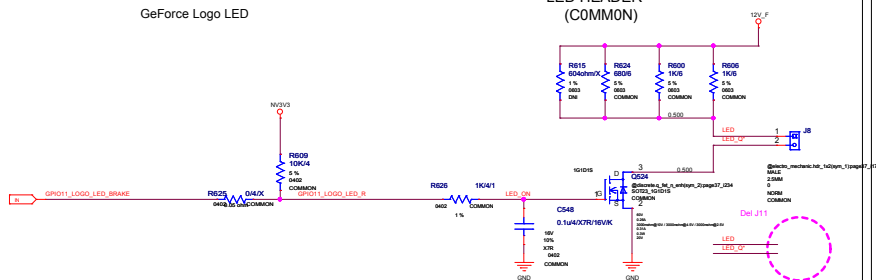
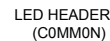
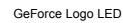
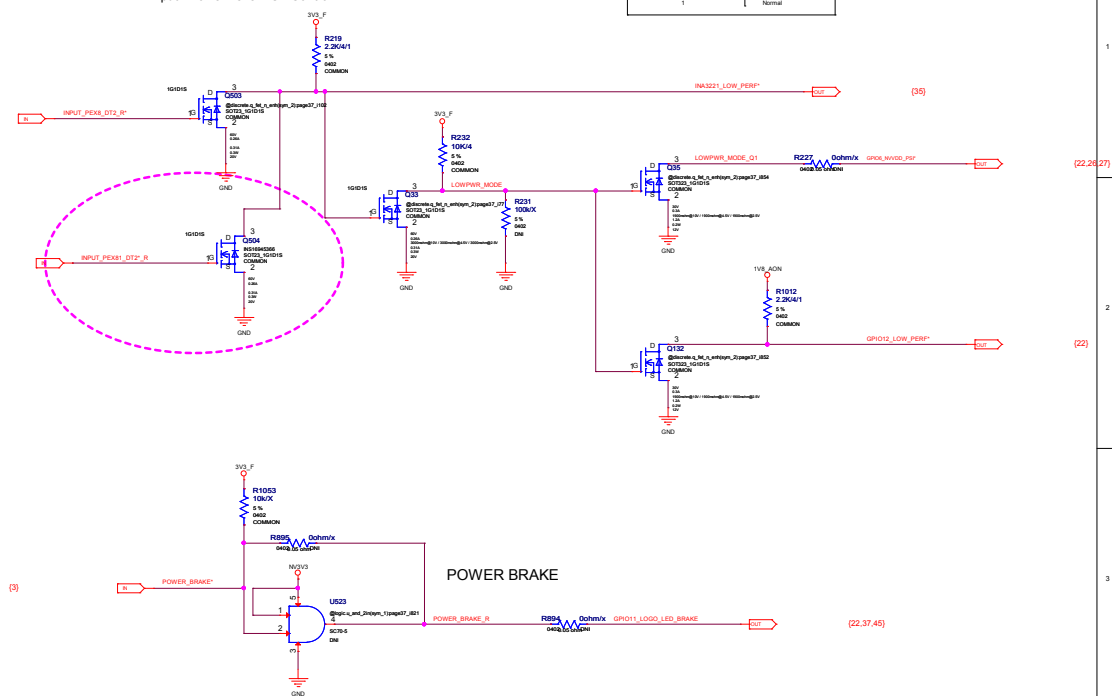
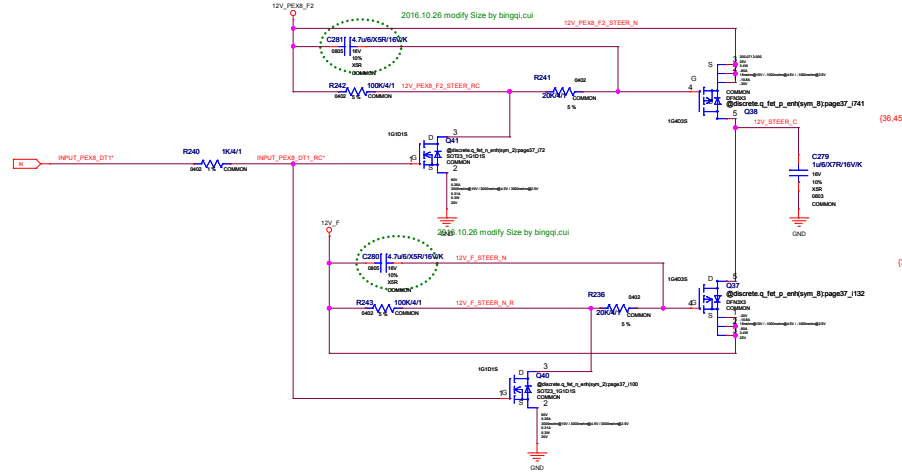
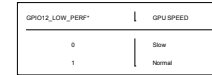
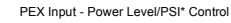
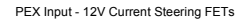


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Doc: <Title>
Size: Document Number
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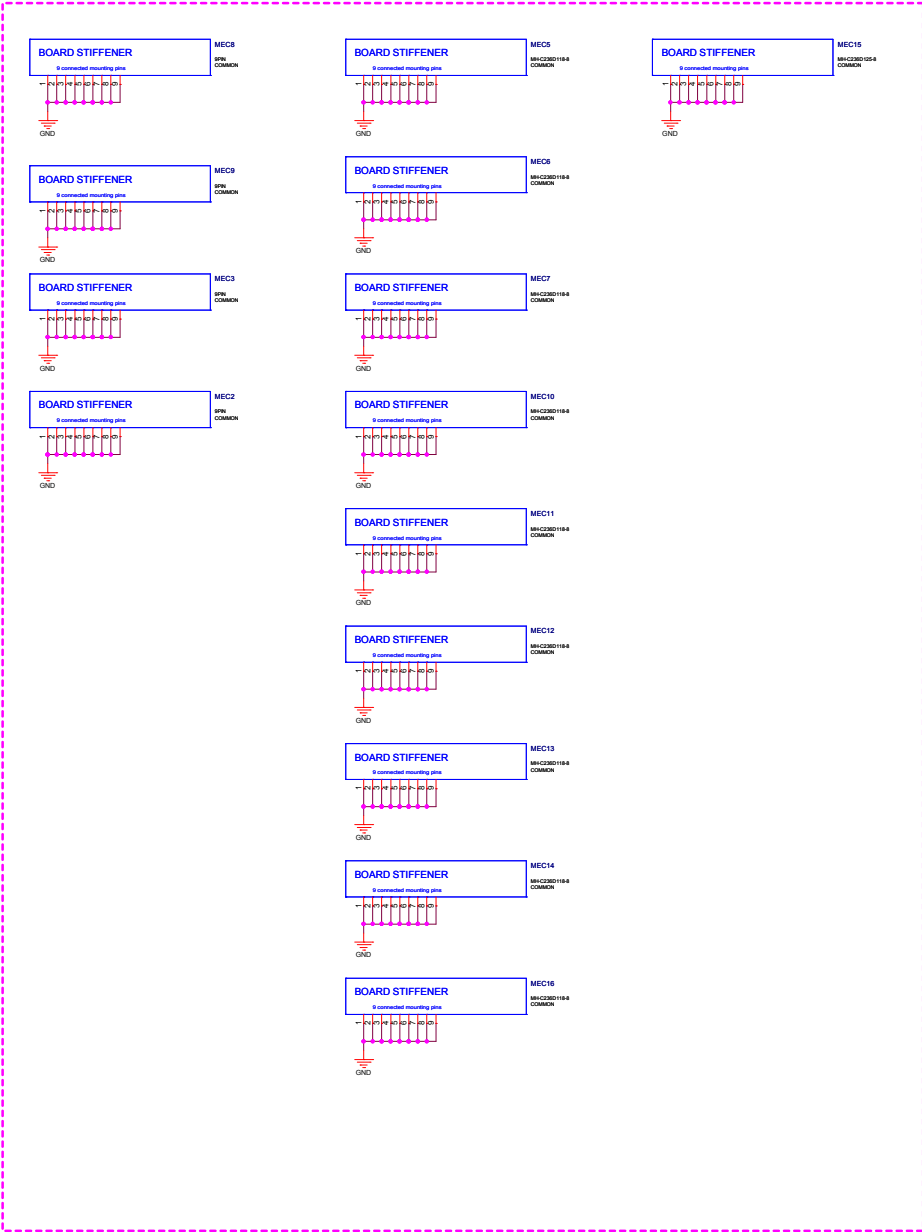
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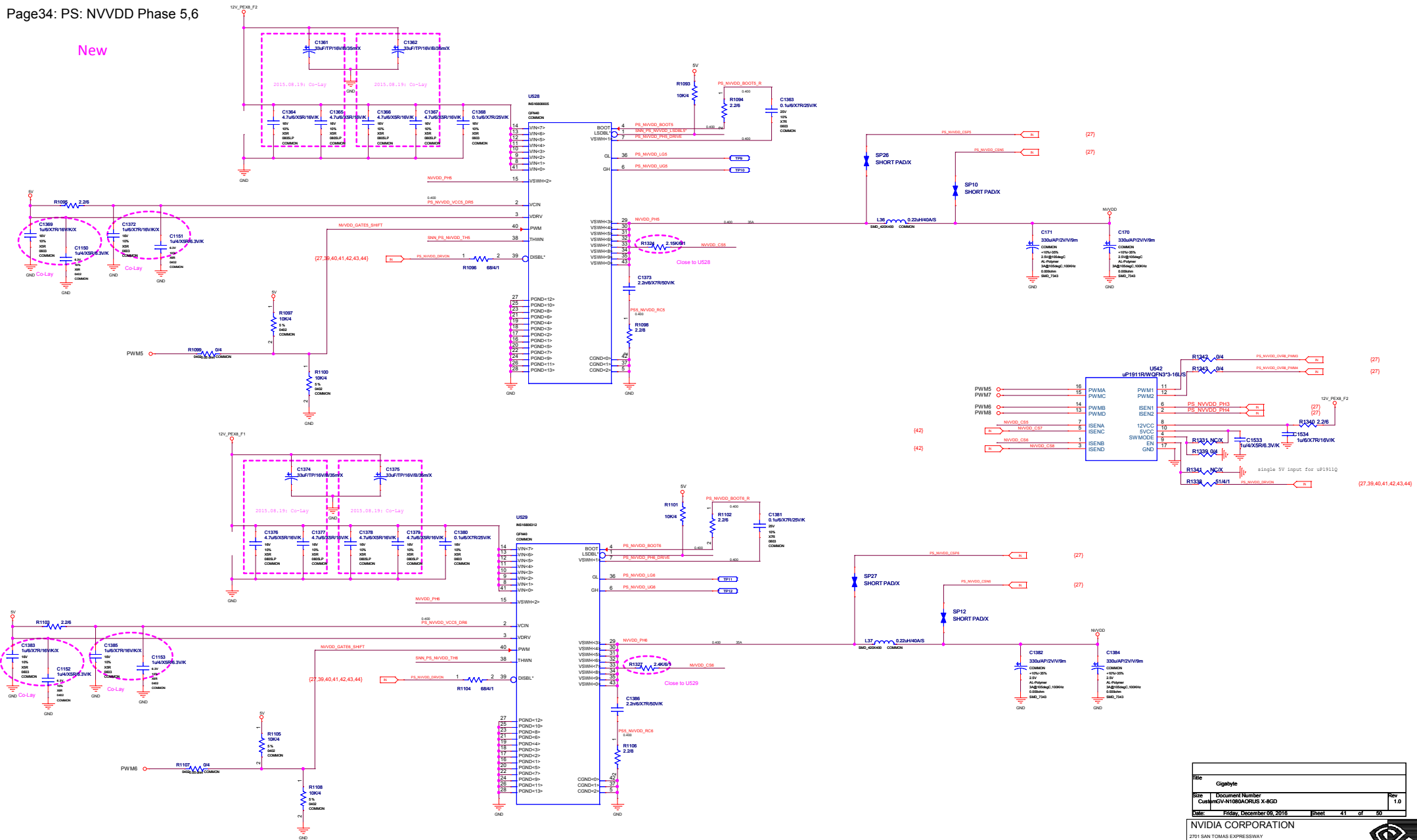


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NV_PN	600-1G600-BASE-QS1		
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BOM REV	A	DATE	21-APR-2015

New

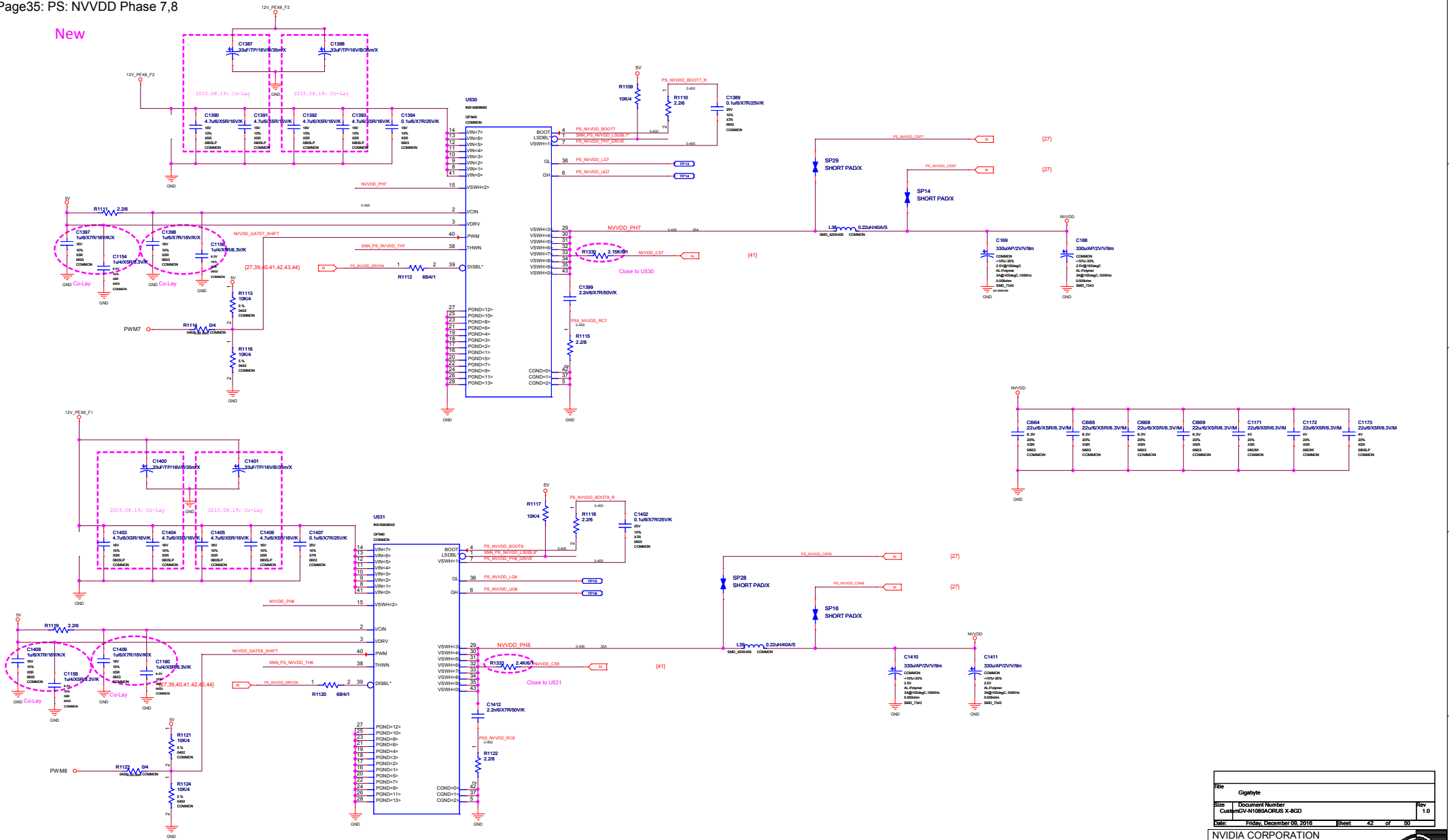


Site	Gigabyte
Size	Document Number
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NV_PN	600-1G600-BASE-QS1
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New



CO-LAYOUT: CHOOSE UA OR UB

ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	PS: NVDD Dynamic Switch Phases

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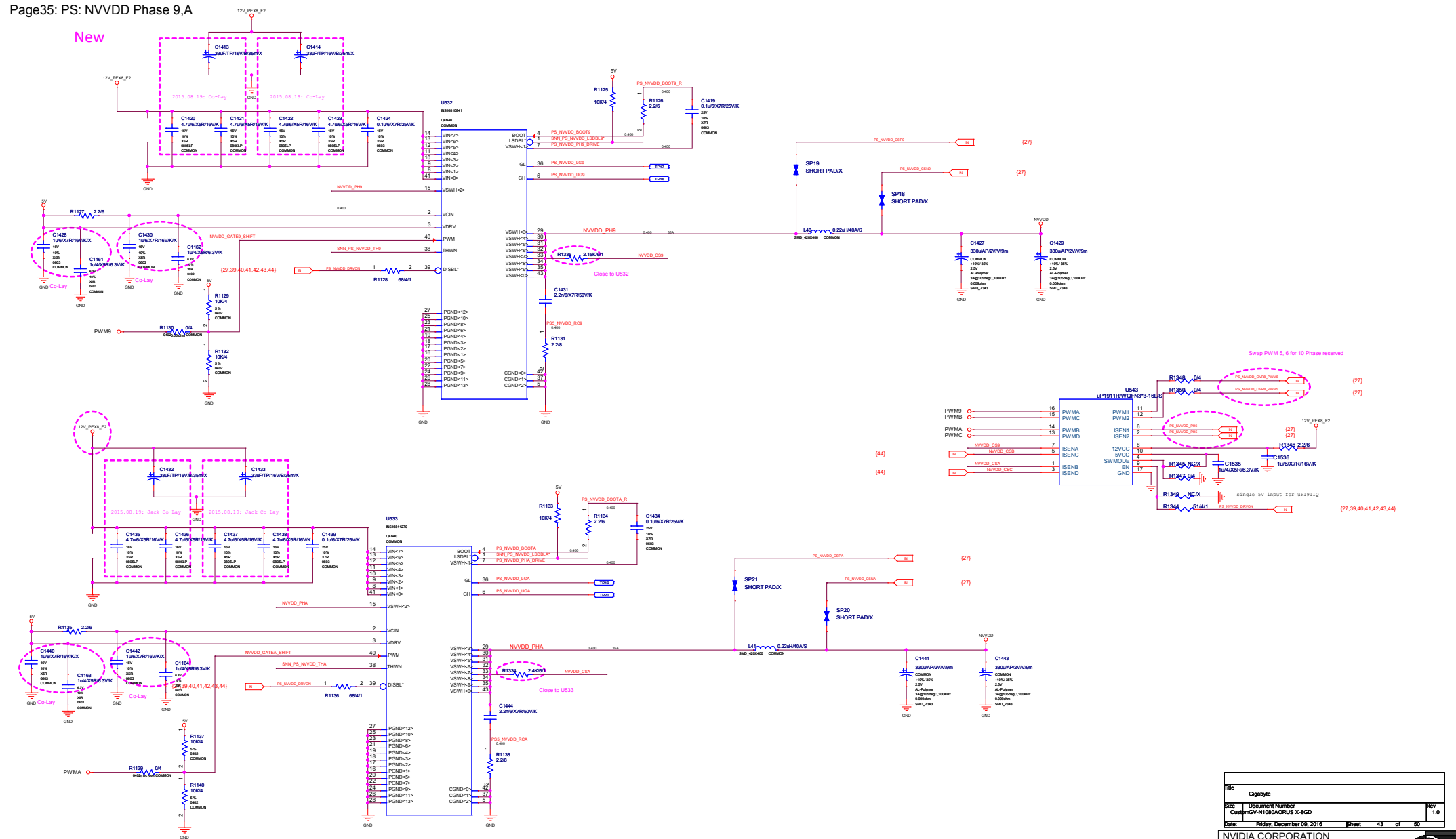
NV_PN 6000-G1600-BASE-QS1

PCB REV PGRD01B0Z

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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
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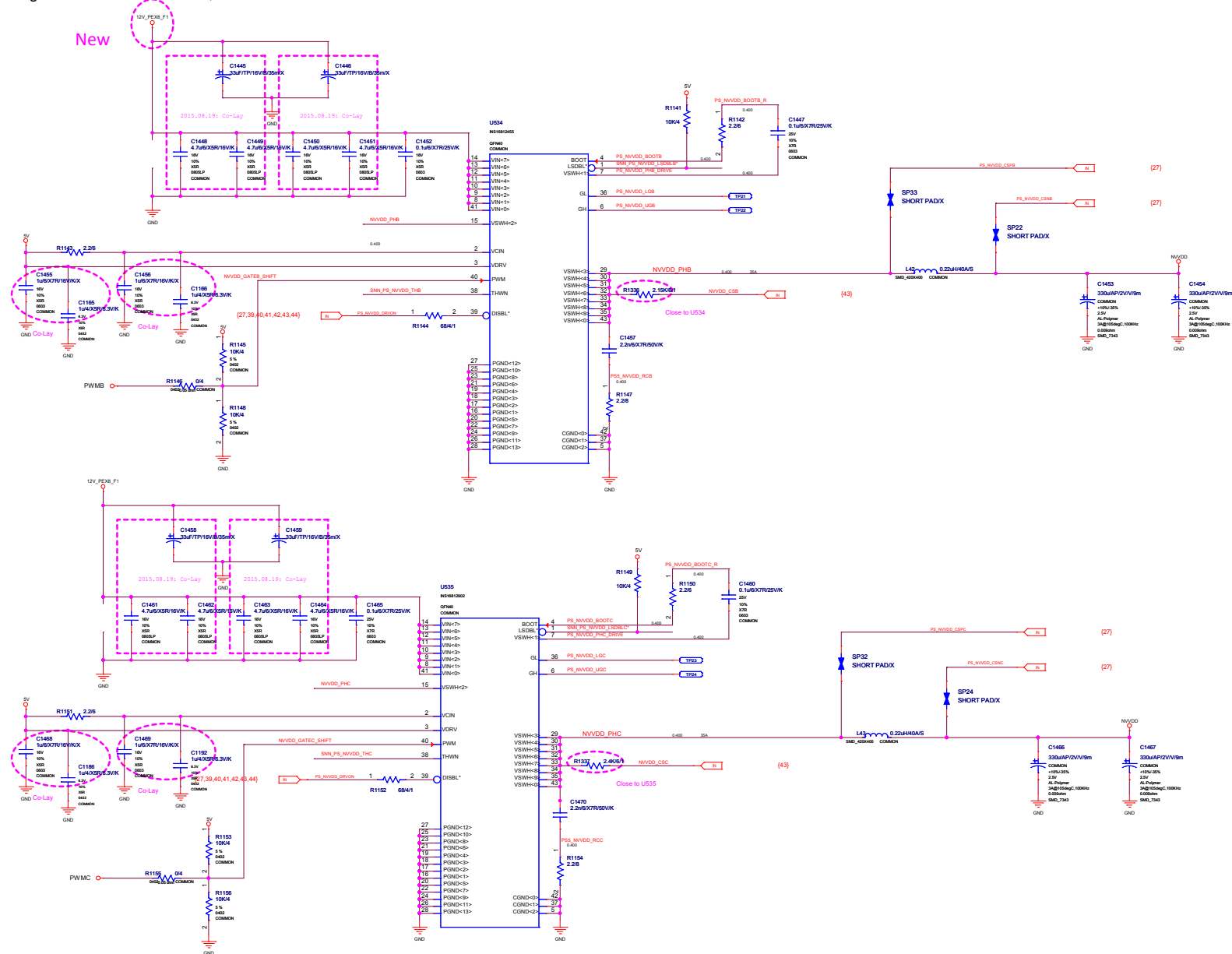
File		Gigabyte			
Size	Document Number				Rev
Curtain-CV-N1800AKRUS X-RG0					1.0
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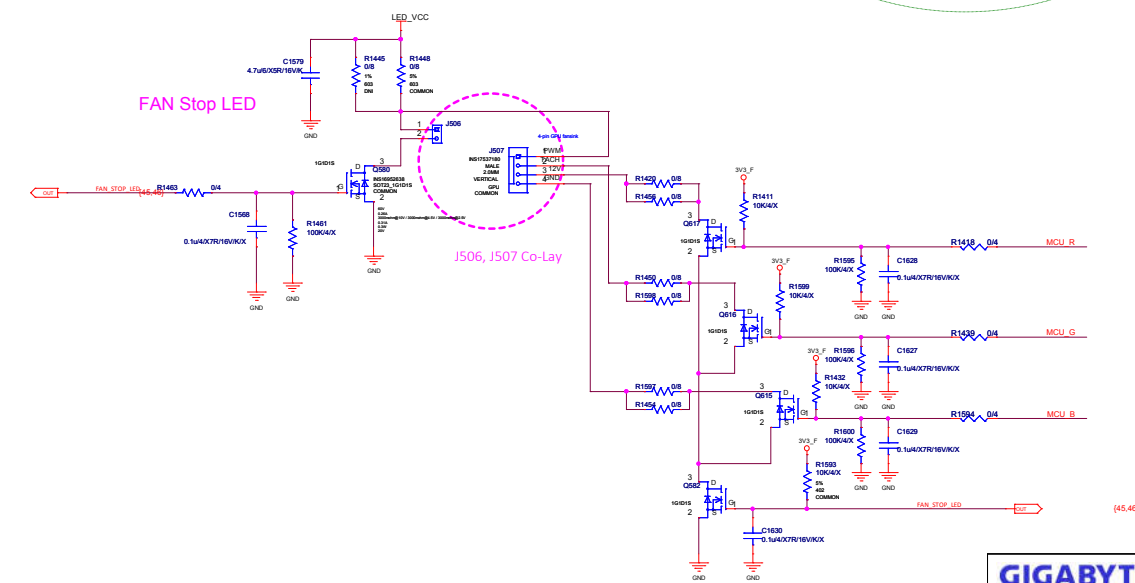
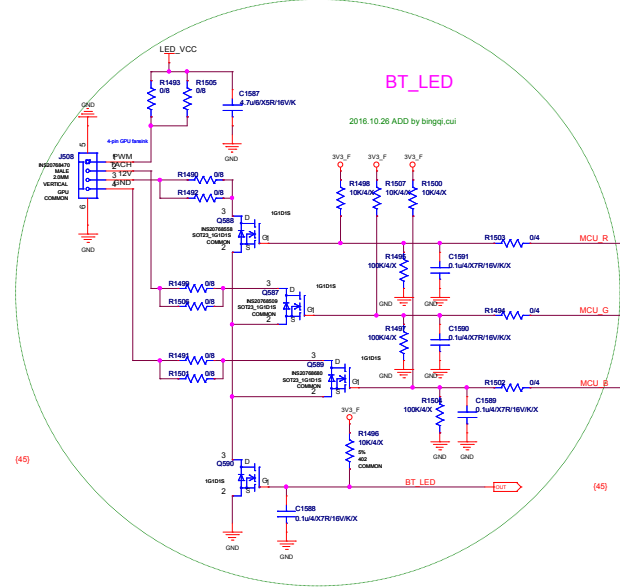
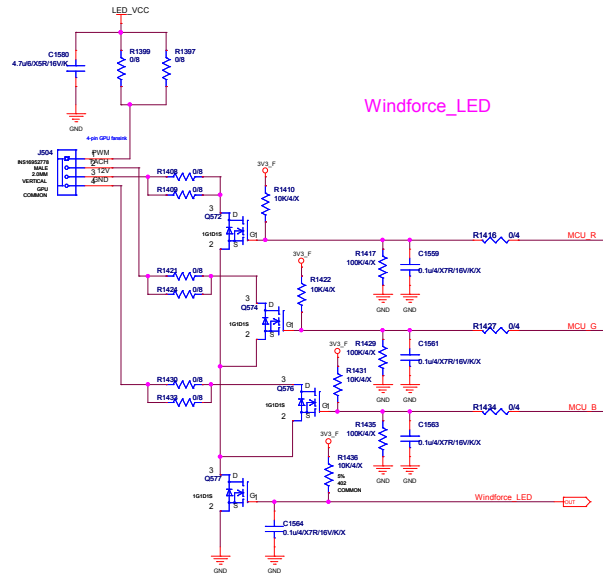
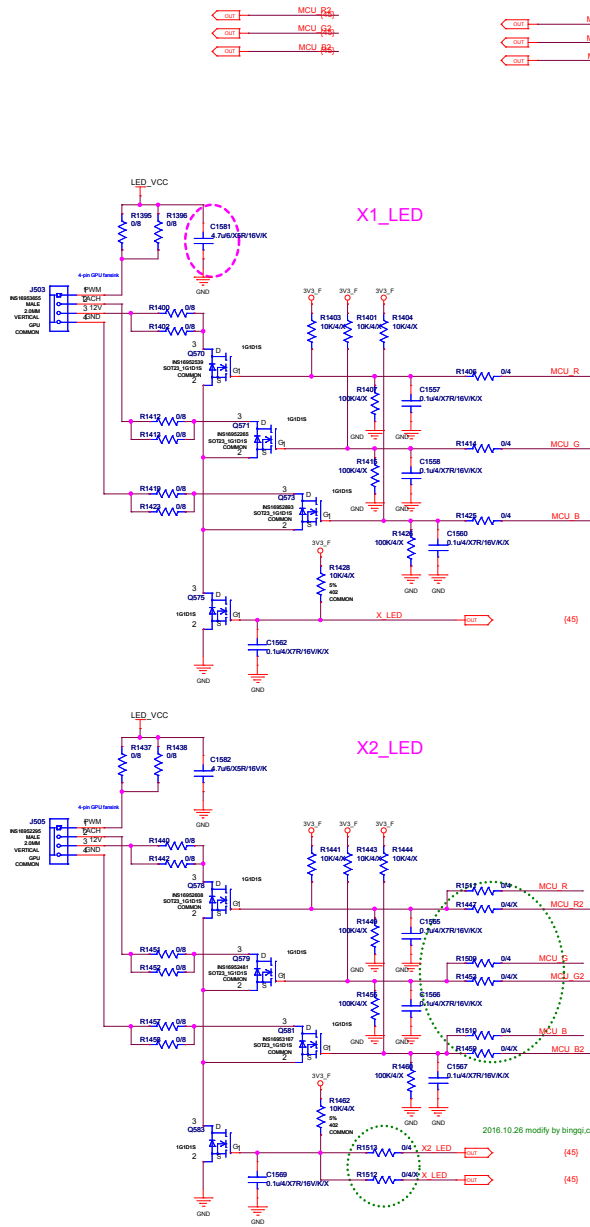
File		Gigabyte	
Size	Document Number	New	
Customer	GV-H100AORUS X-860	1.0	
Date	Friday, December 08, 2016	Sheet	44 of 50

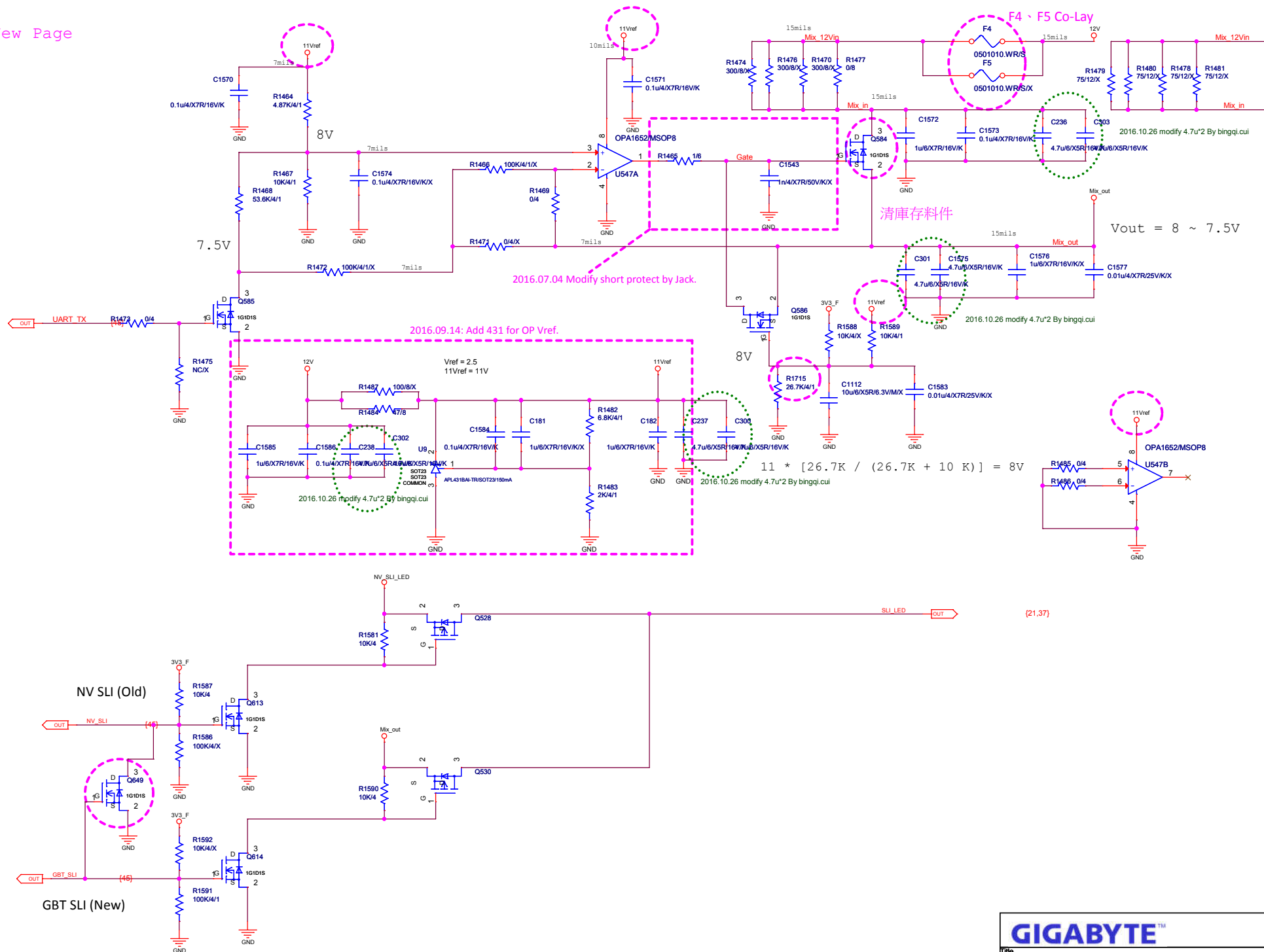
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History

Date	Modify
2016.04.04	First version
2016.05.04	0.21 modify DVI vs VRHDMI switch
2016.06.16	2.0 Only modify PCB text REV 0.21 to 2.0
2016.07.04	0.6 Modify P.47 Q586 Mix_out protect.
2016.08.26	2.01 Only modify PCB text REV 0.21 to 2.0
2016.09.24	0.7 modify From 2.01, Modify P.47 OP Vref.