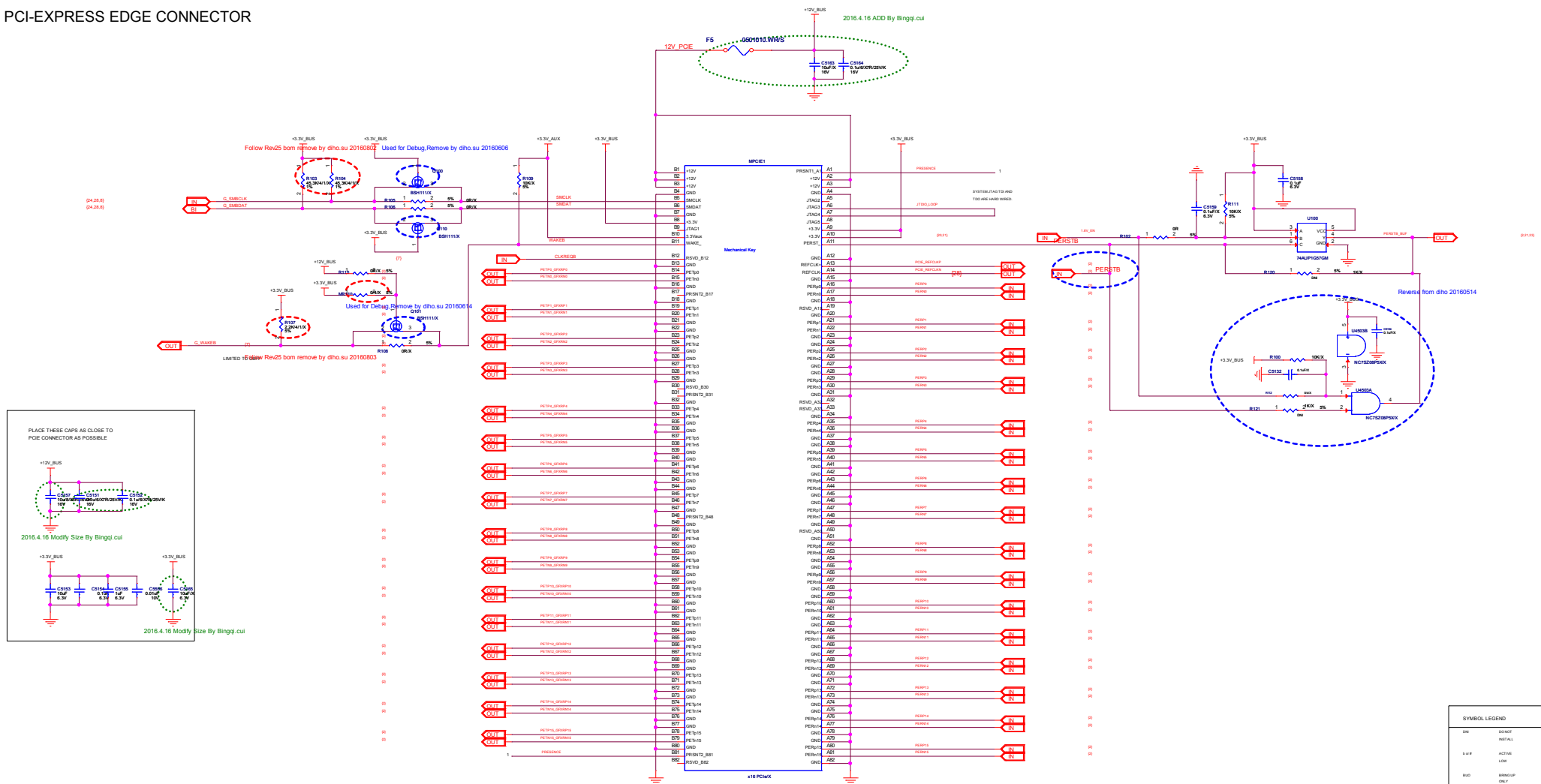
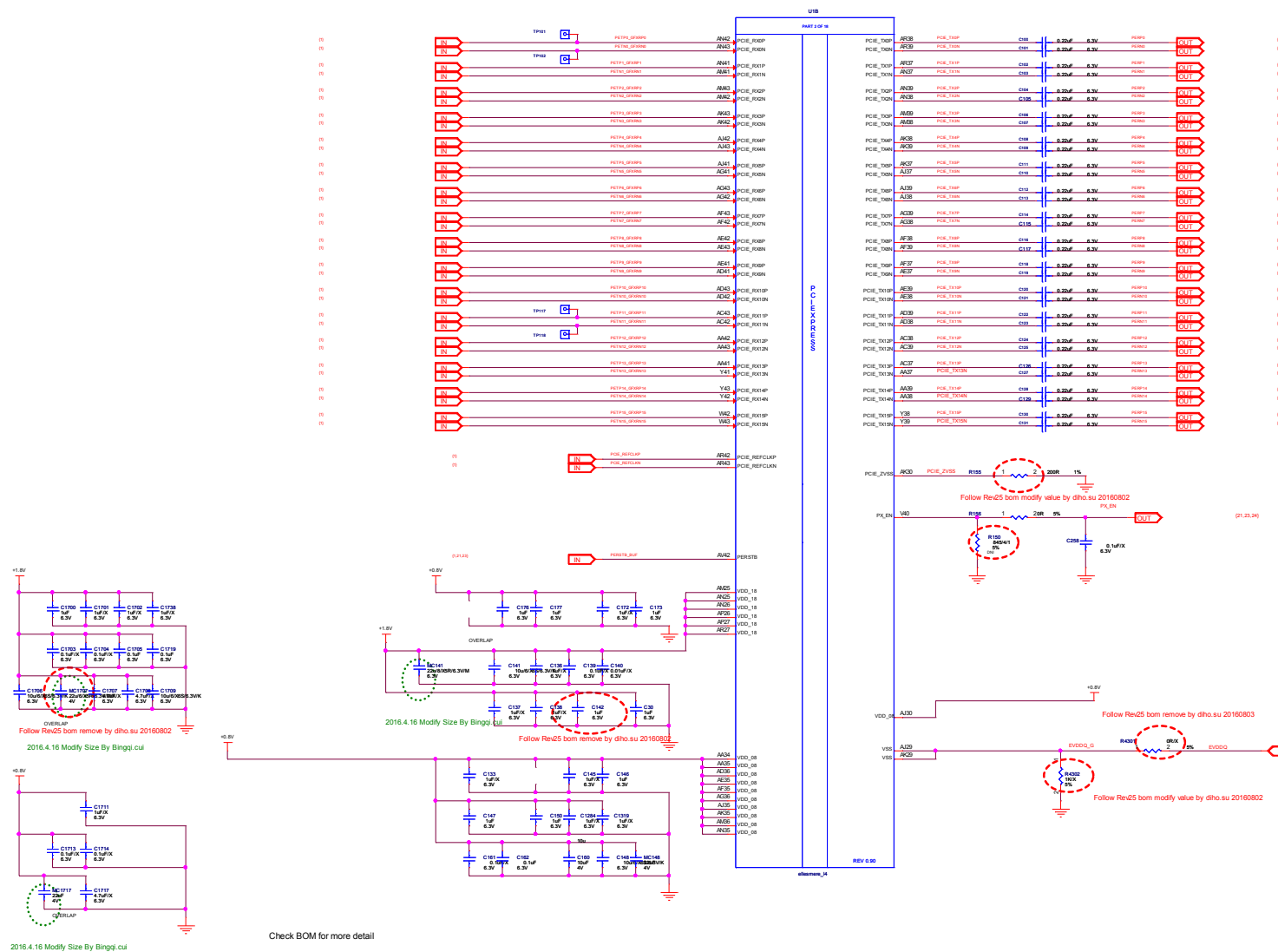


(1) PCI-EXPRESS EDGE CONNECTOR

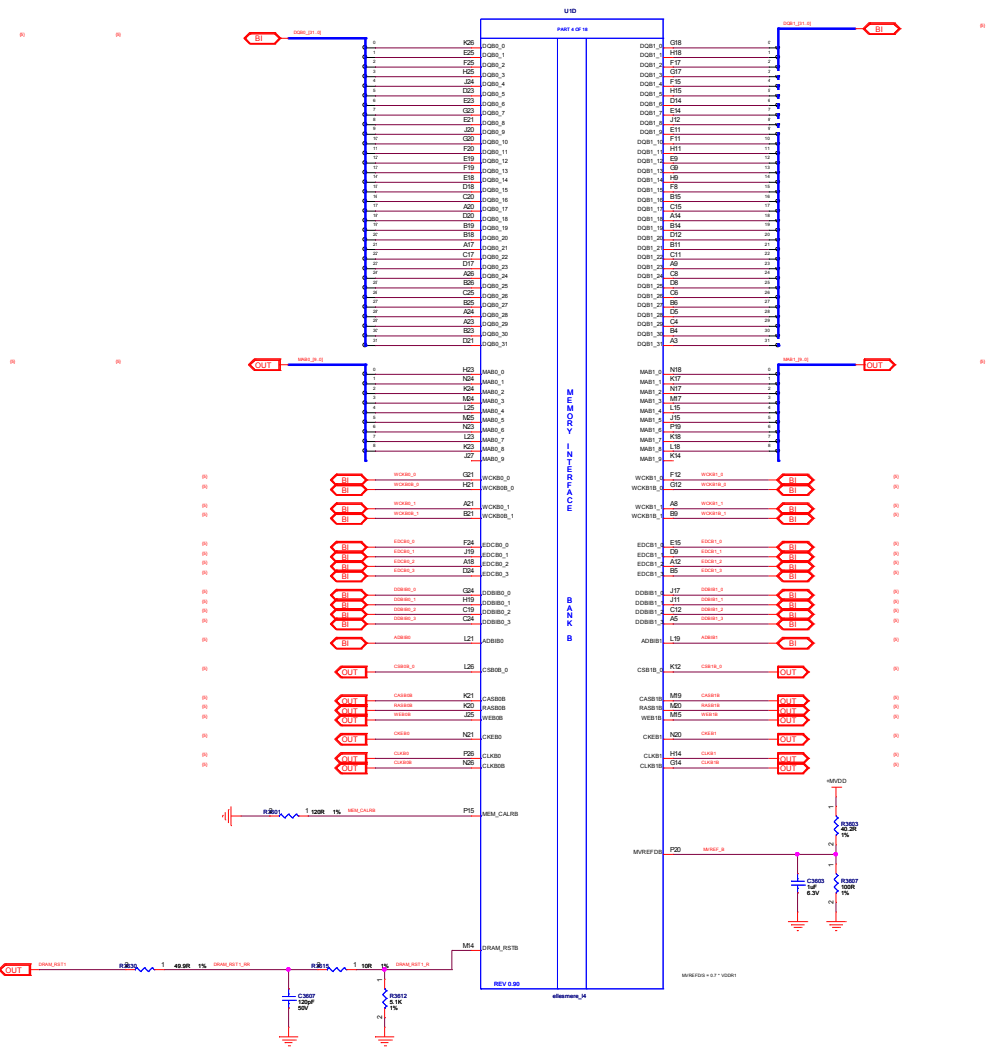
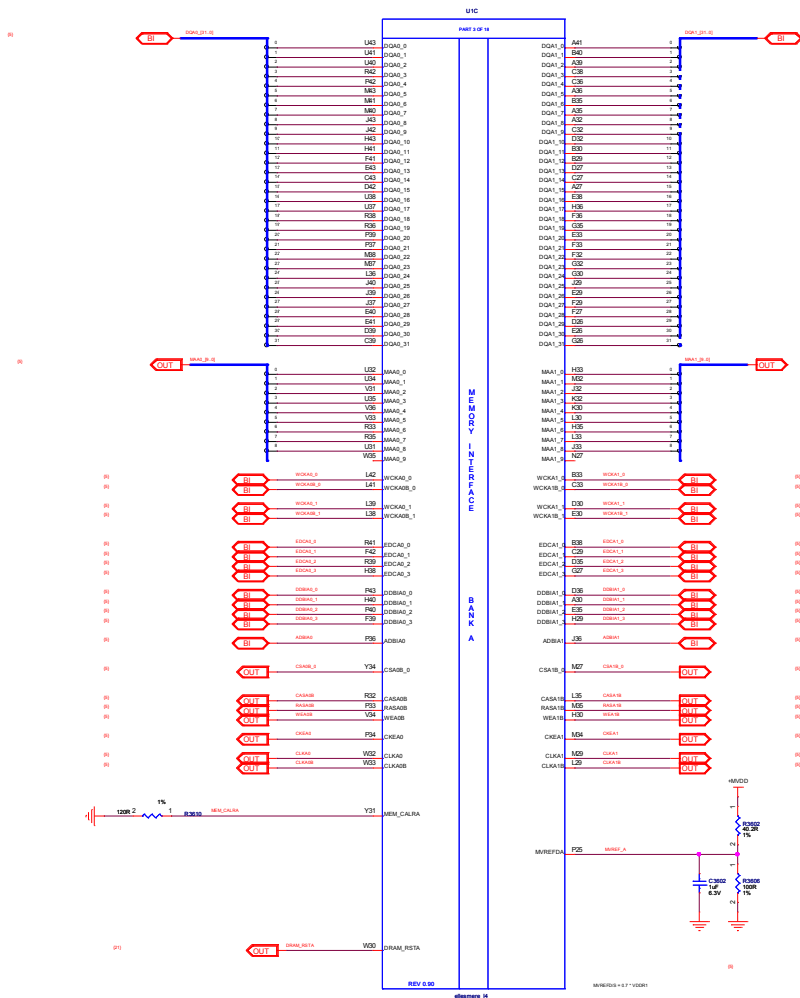


SYMBOL LEGEND	
DNI	DO NOT INSTALL
A or F	ACTIVE LOW
BUO	BROWN-UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

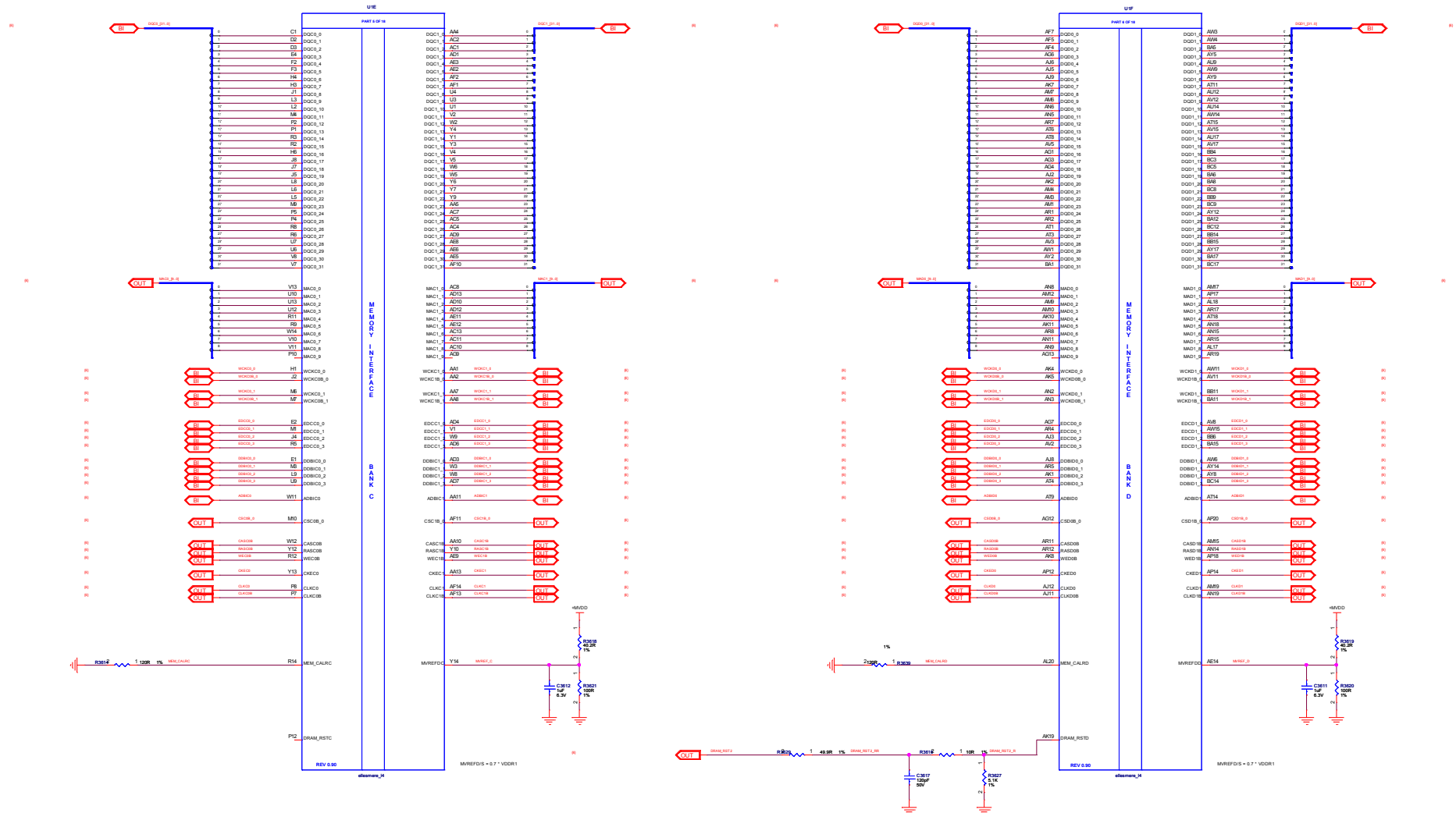
(2) ELLESMERE PCIE INTERFACE



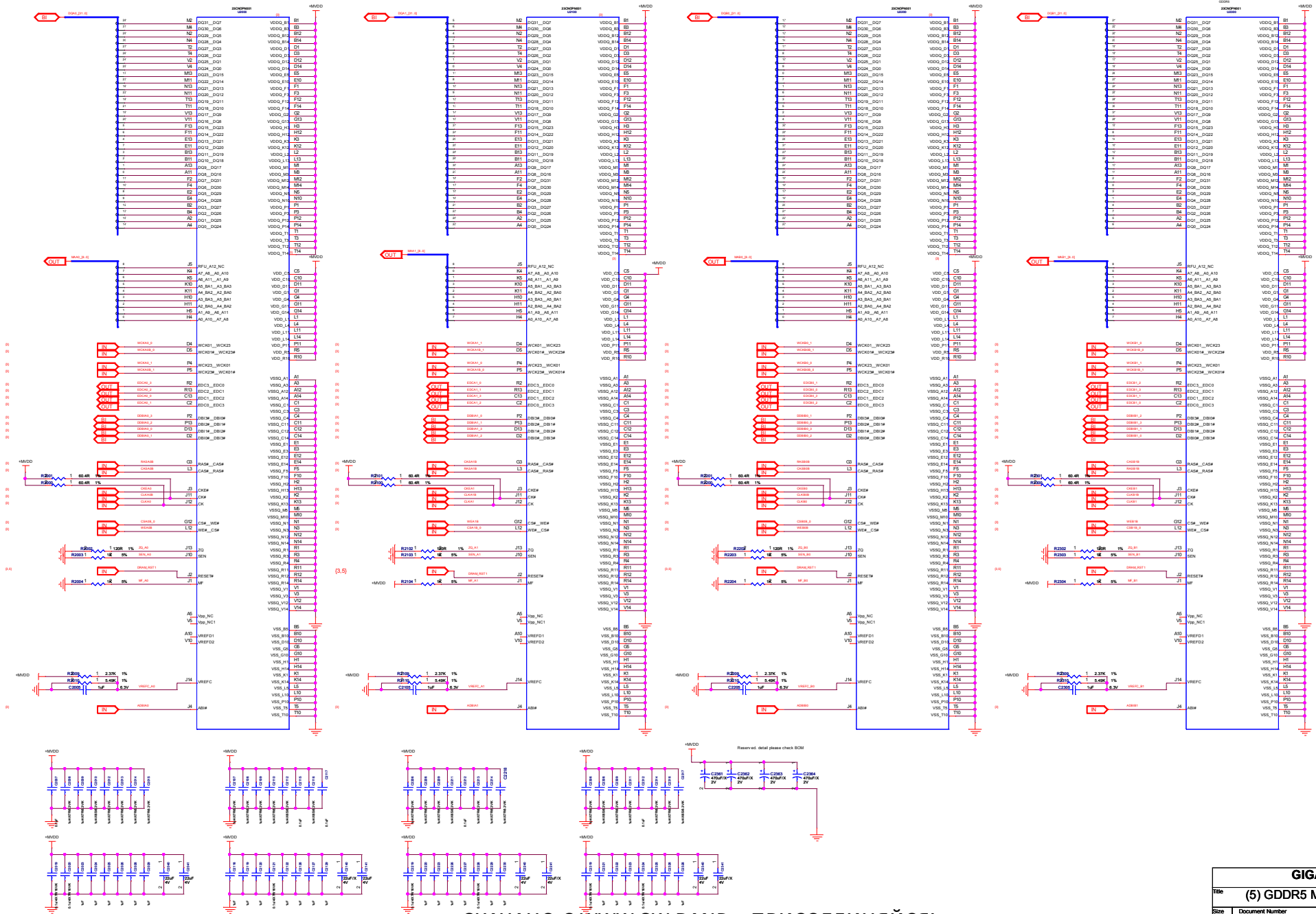
(3) ELLESMERE MEM INTERFACE CH A/B



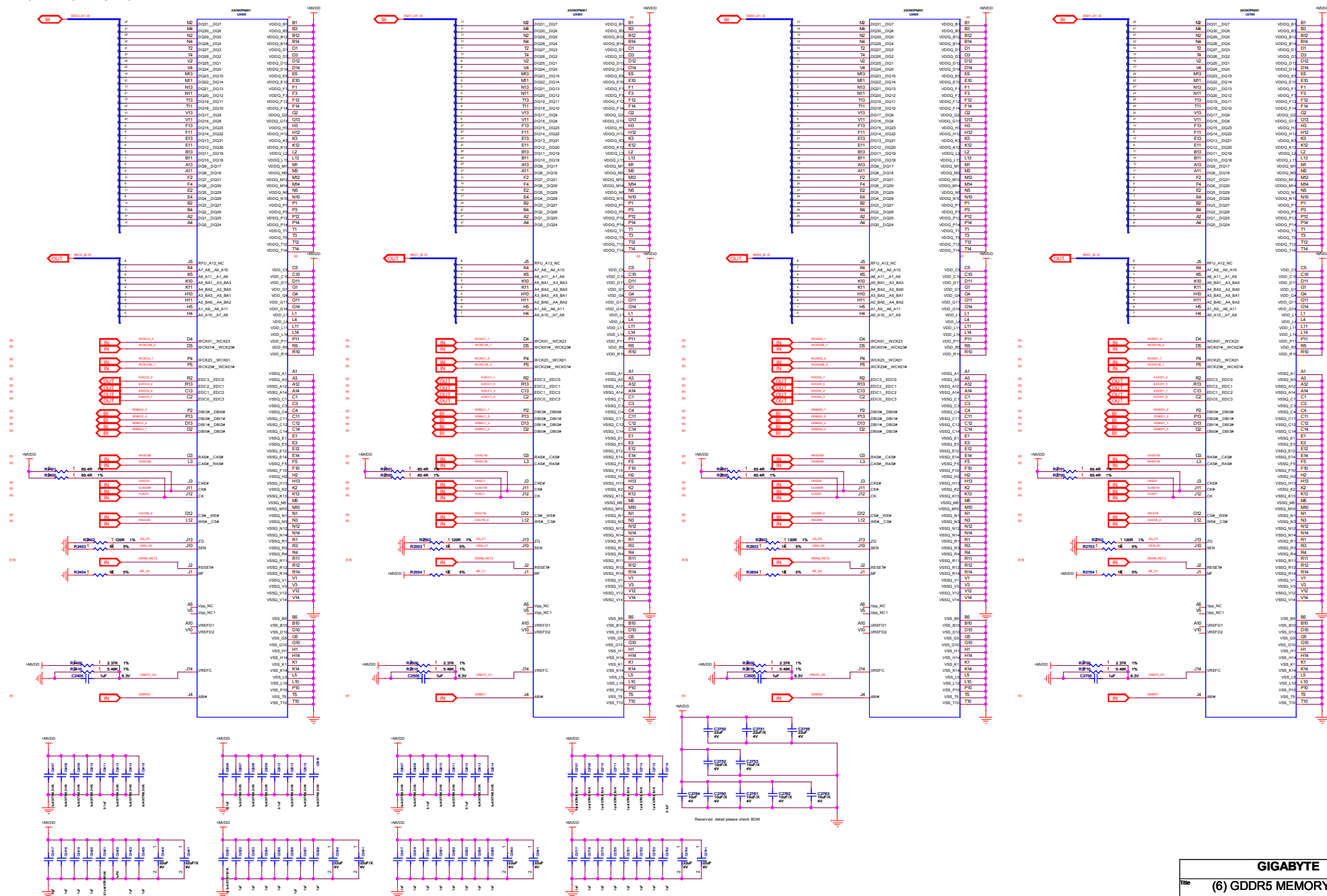
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B

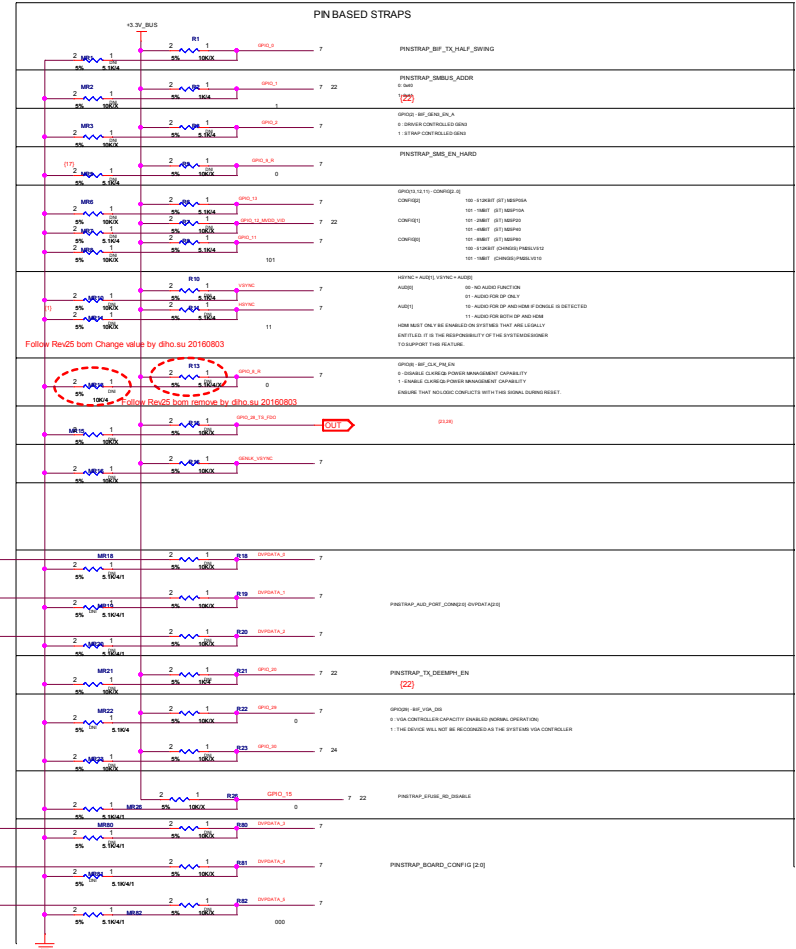
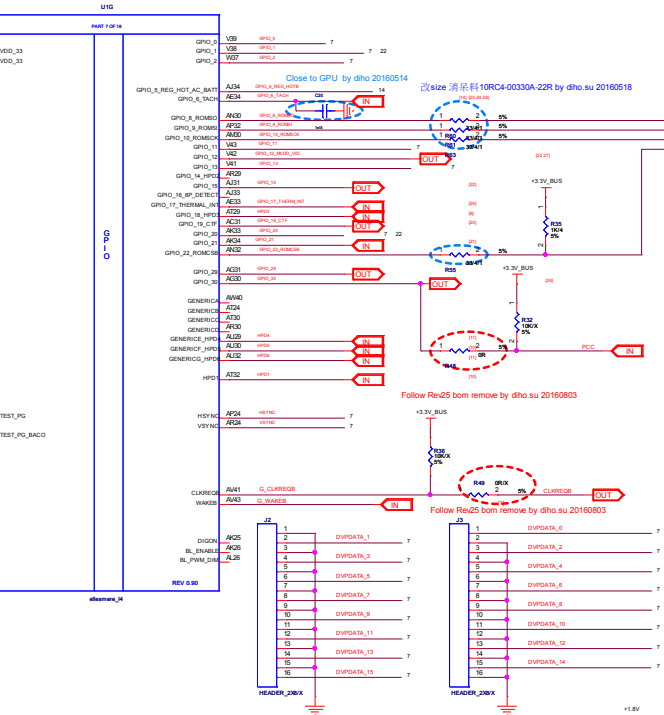
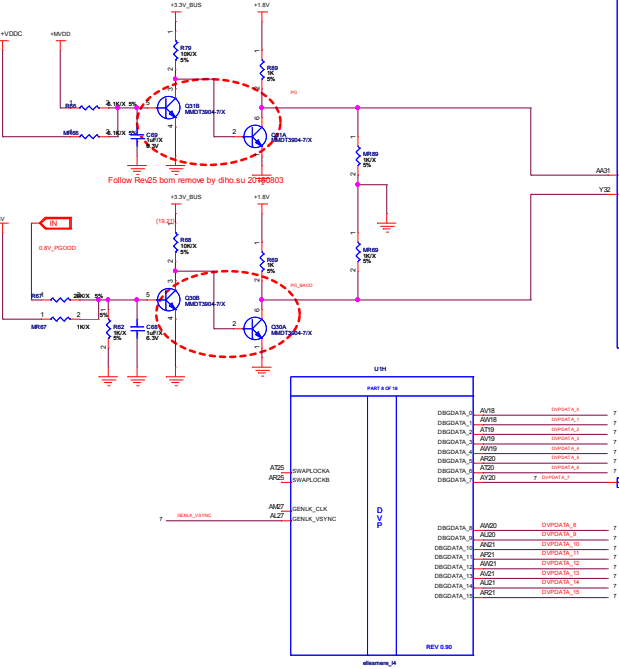


(6) GDDR5 MEMORY CH C/D

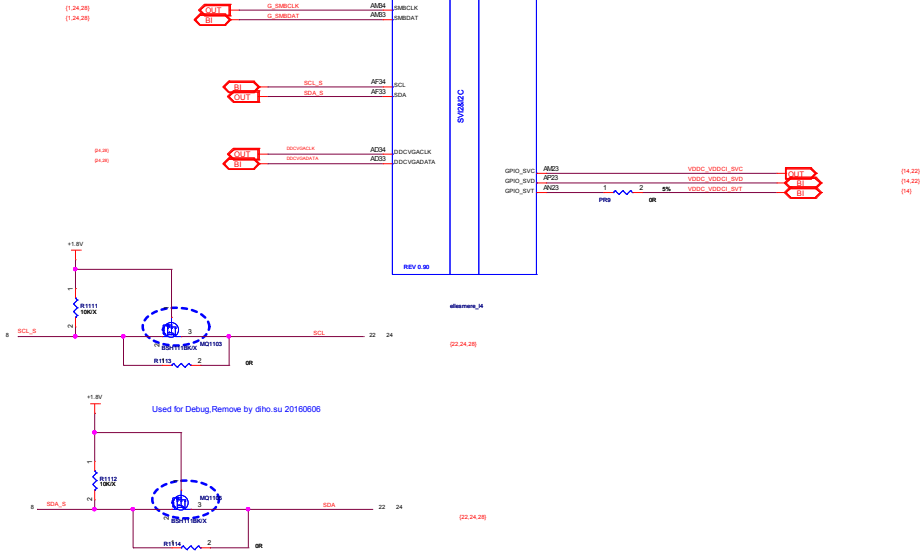


(7) ELLESMERE GPIO STRAP CF XTAL

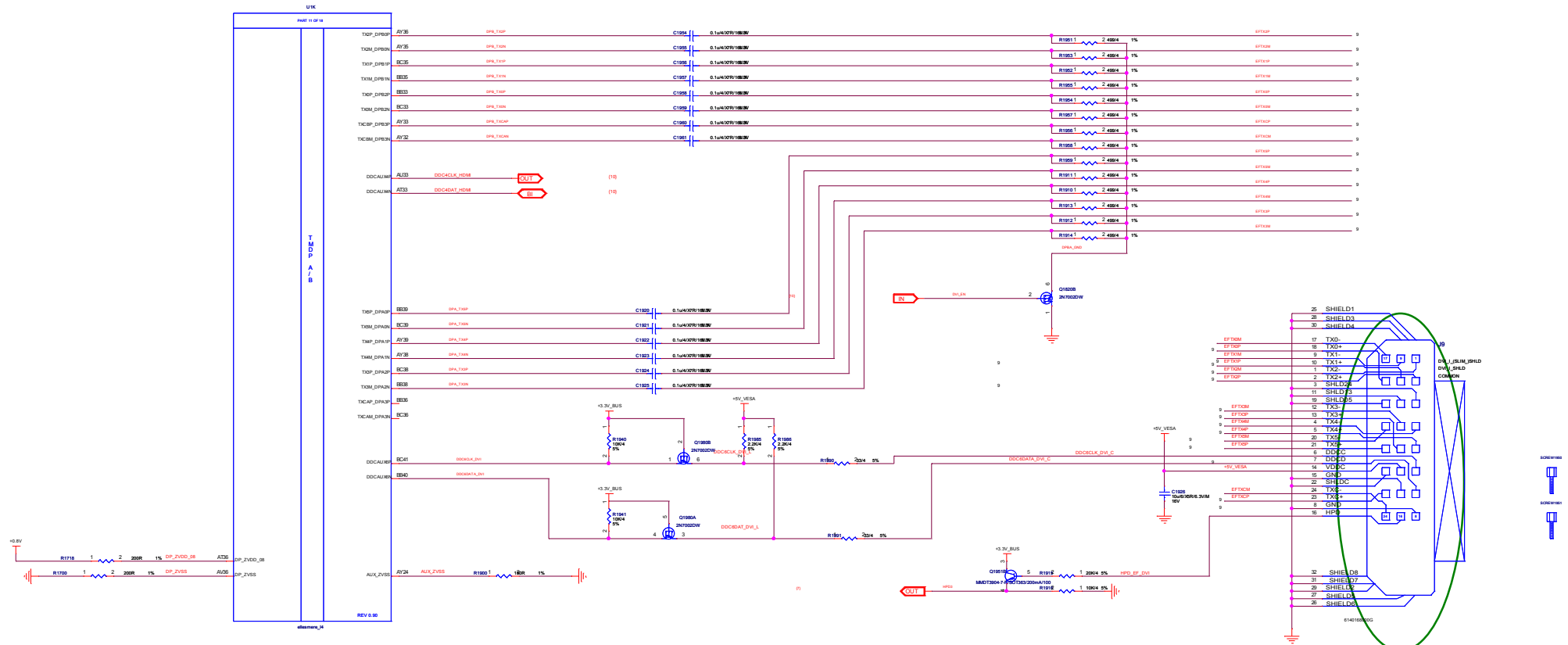
I2C/SDA BUS		
IC ADDRESS	FUNCTION	DEVICE
DDCVGA BUS		
IC ADDRESS	FUNCTION	DEVICE
0x48	EXT TEMP SENSOR	LM80B3



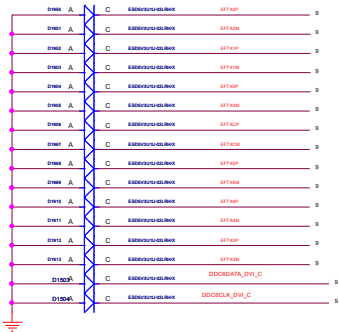
(8) ELLESMERE DAC1 LOCK



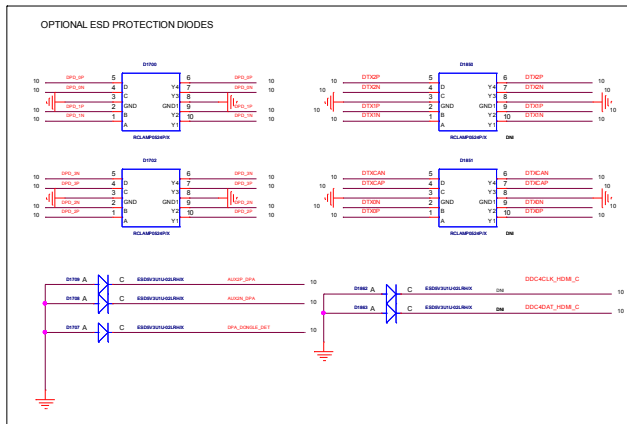
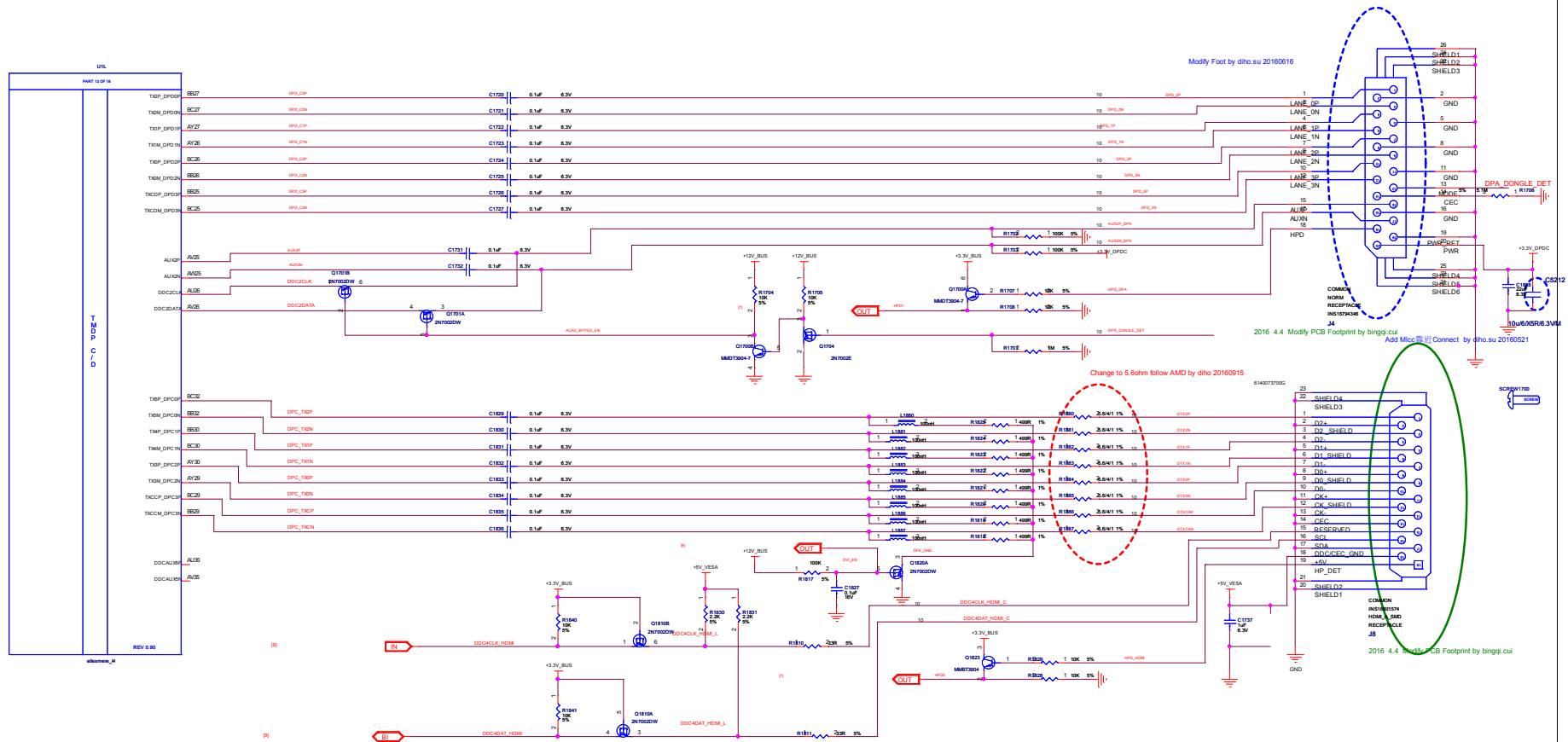
GIGABYTE			
Title ELLESMERE DAC1 LOCK			
Size	Document Number		Rev
Custom	GV-RX580GAMING-8GD		1.0
Date:	Monday, February 27, 2017	Sheet	8 of 29

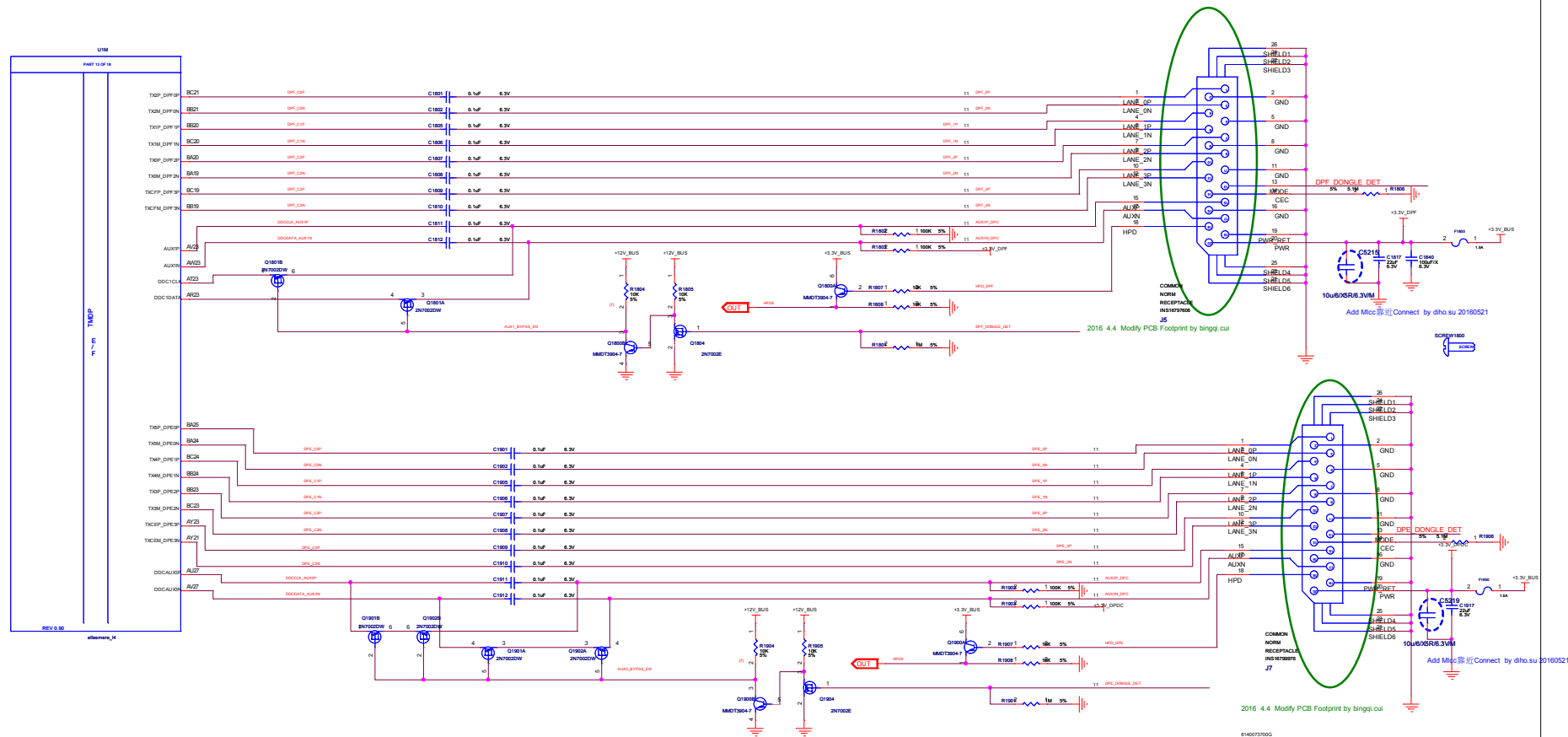


OPTIONAL ESD PROTECTION DIODES

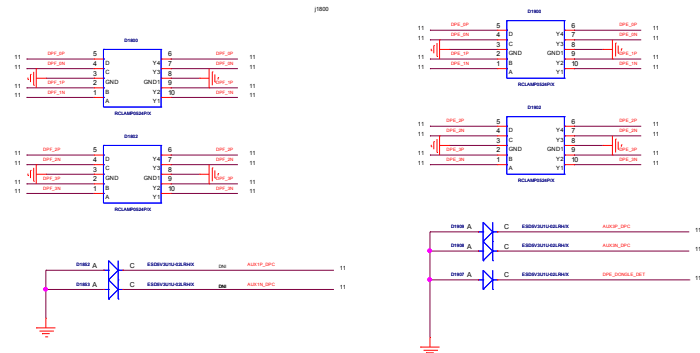


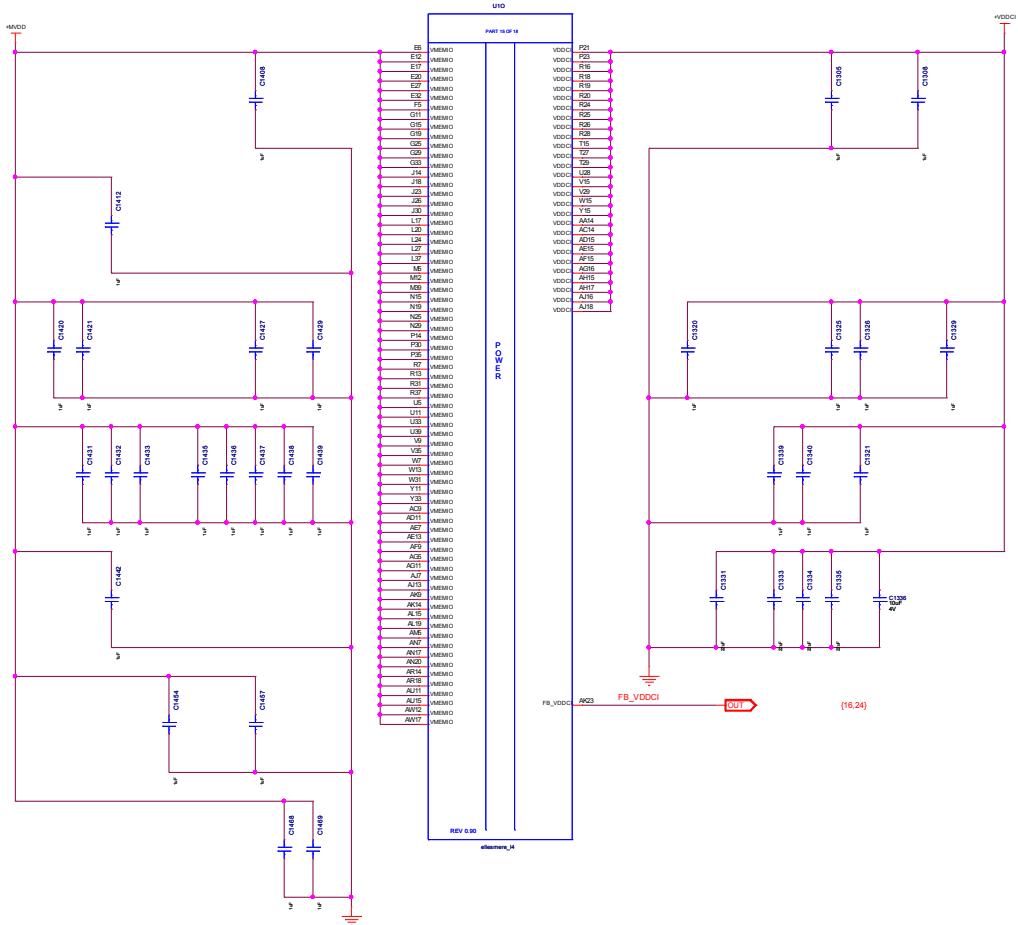
2016 5.20 Modify PCB Footprint by DIHO.SU



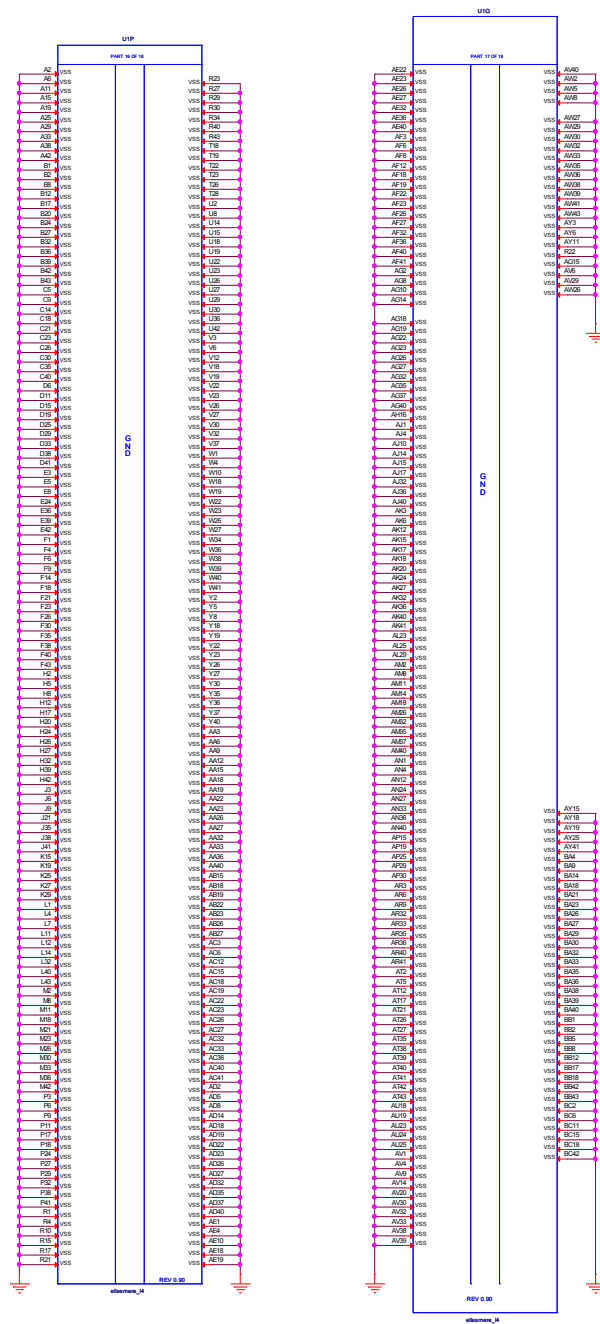


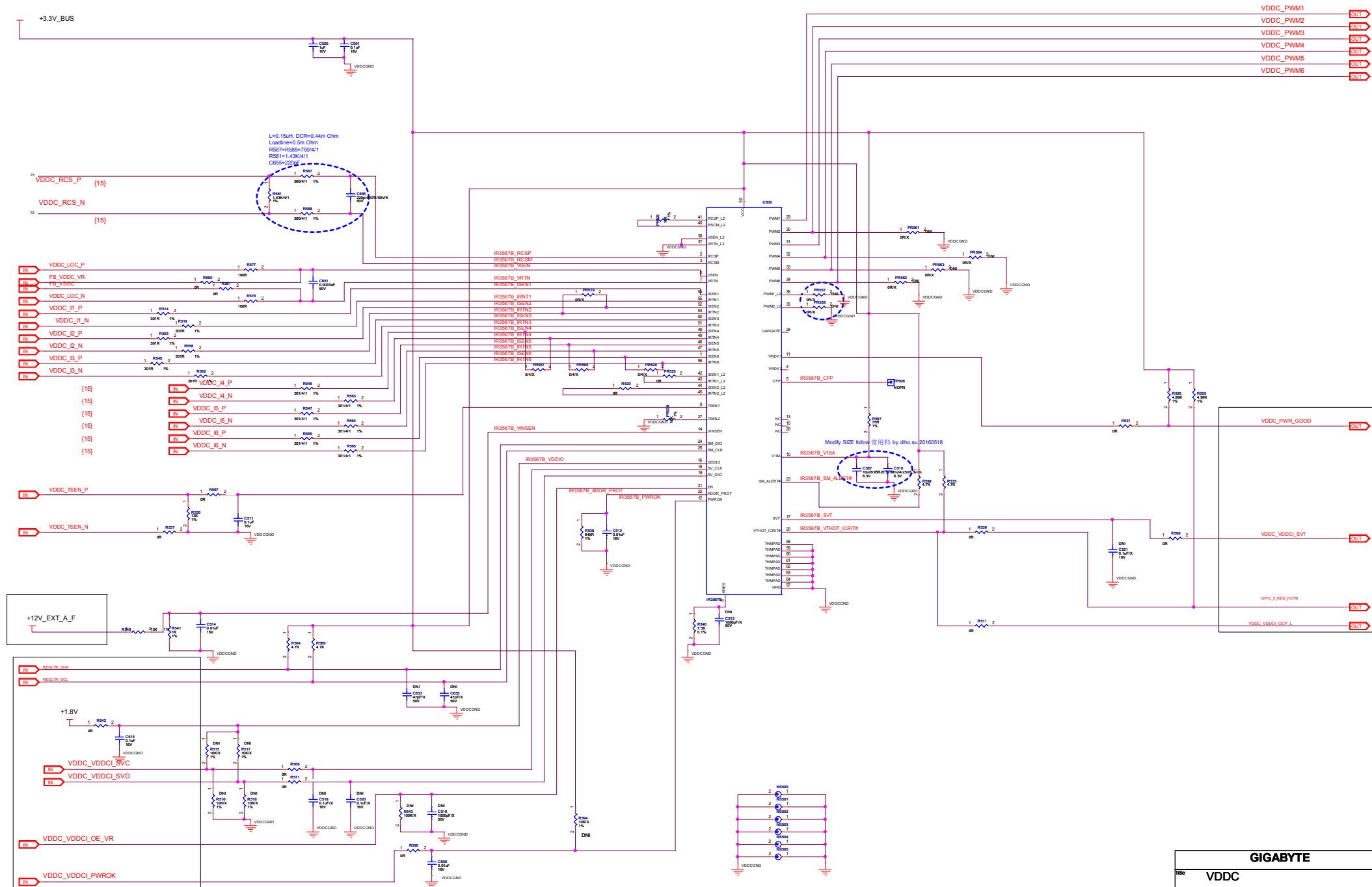
OPTIONAL ESD PROTECTION DIODES

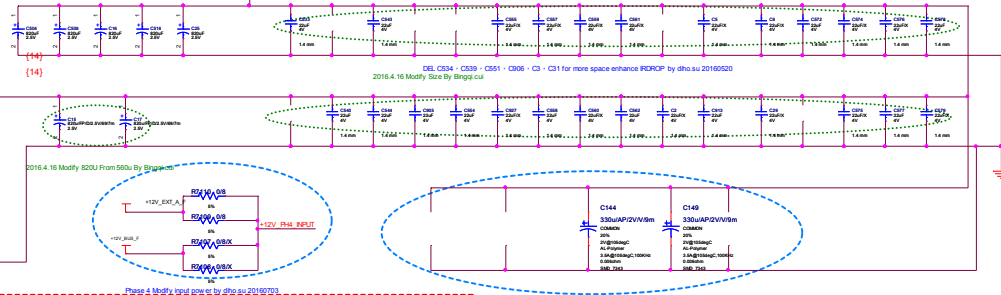




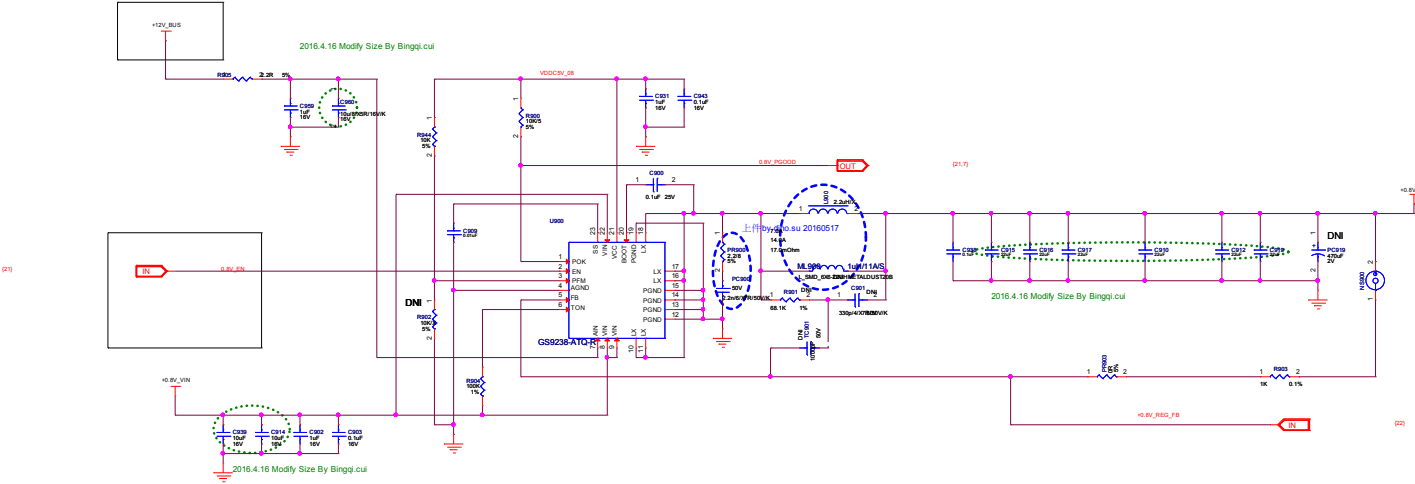
(13) ELLESMERE GROUND









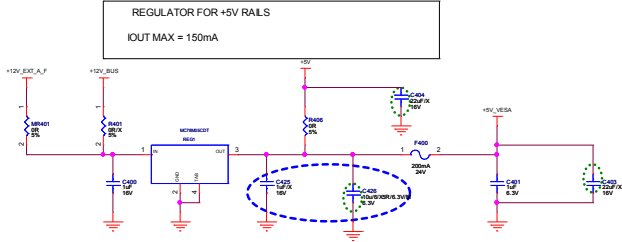
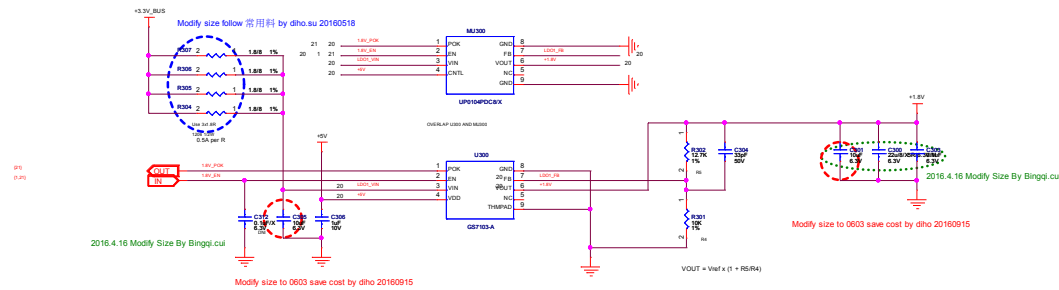


(18) SMALL RAIL REGULATORS

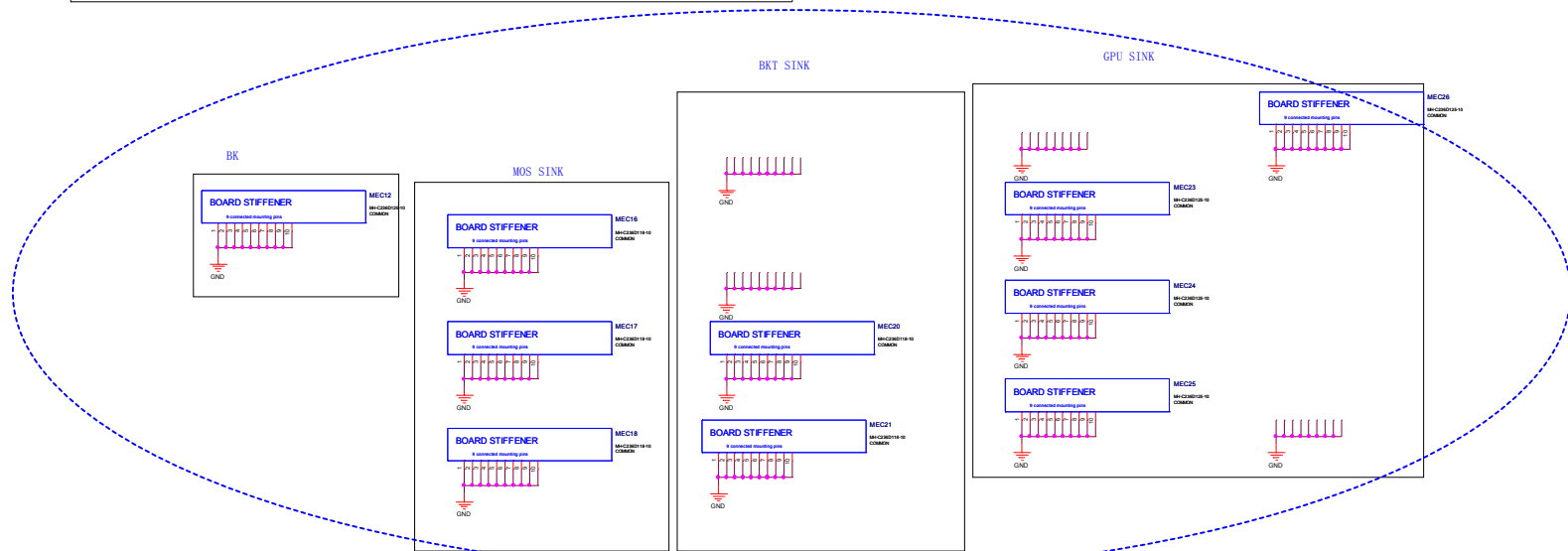
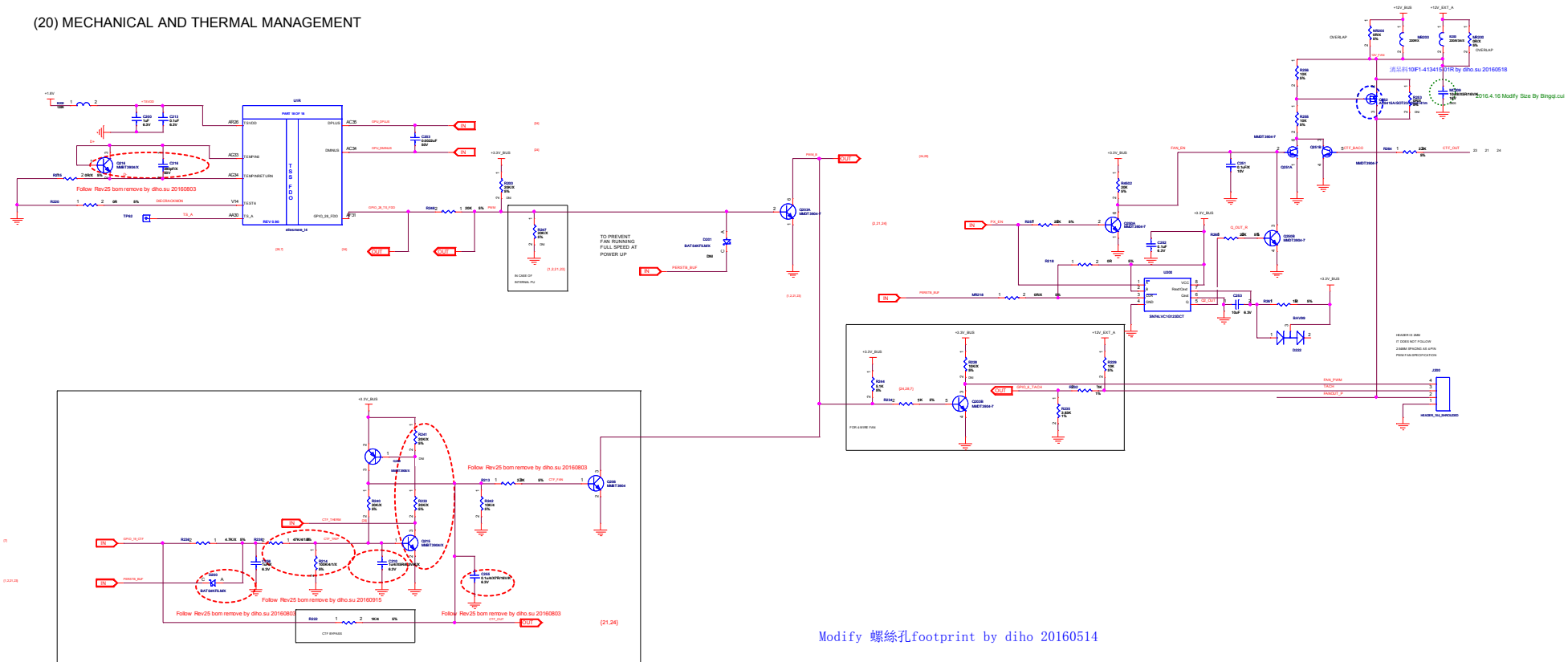
LDO #1:
VIN = 3.0V TO 3.6V MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

VOUT = +1.8V +/- 2%

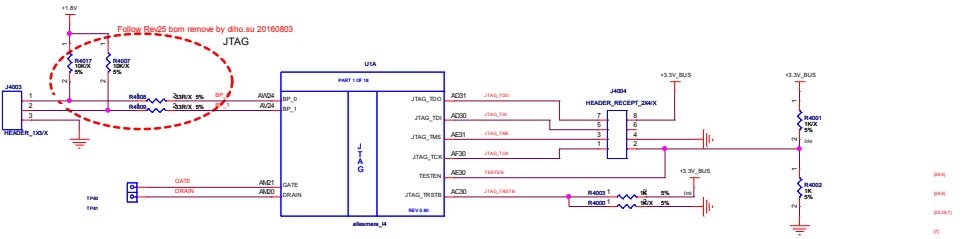
IOUT = 1.3A RMS MAX



(20) MECHANICAL AND THERMAL MANAGEMENT

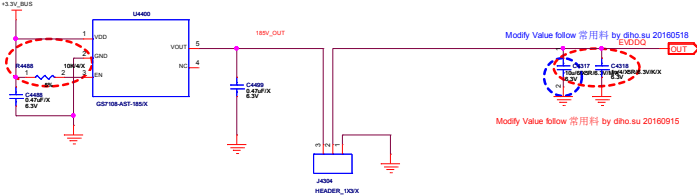


(21) DEBUG CIRCUITS



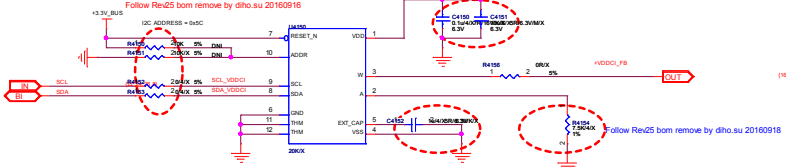
Follow Rev25 bom remove by dho.su 20160915

E-FUSE CAPABILITY
DEFAULT = GPIO-CONTROLLED
(MANUAL OPTION AS BACK-UP)



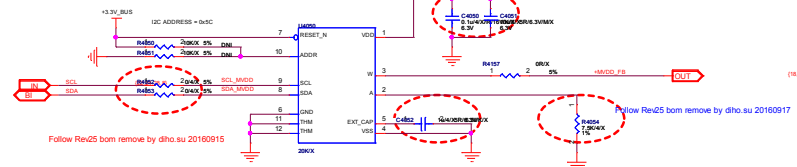
Follow Rev25 bom remove by dho.su 20160915

DIGITAL POTS

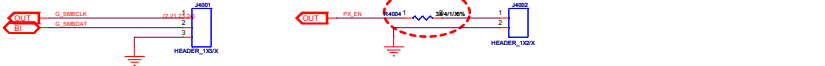


Follow Rev25 bom remove by dho.su 20160915

DIGITAL POTS



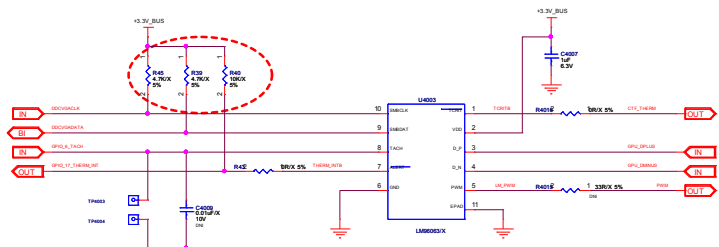
Follow Rev25 bom remove by dho.su 20160915



SWITCHES

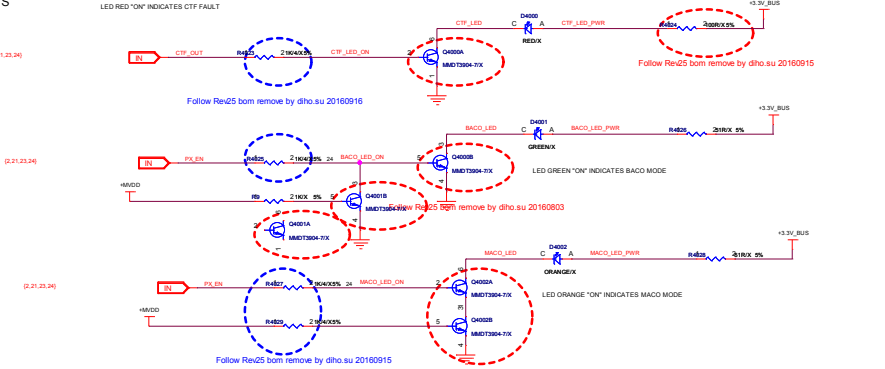


LM96163 FOR BACKUP THERMAL CONTROL

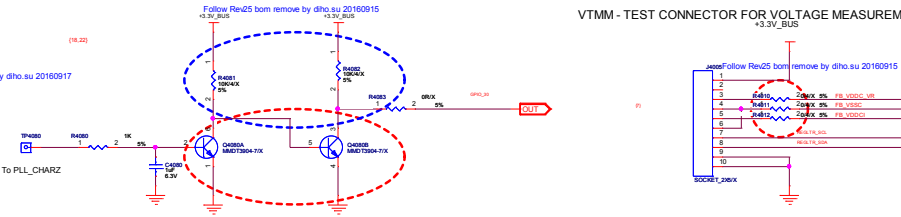


(2)

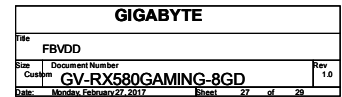
LED LIGHTS

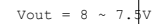


VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS

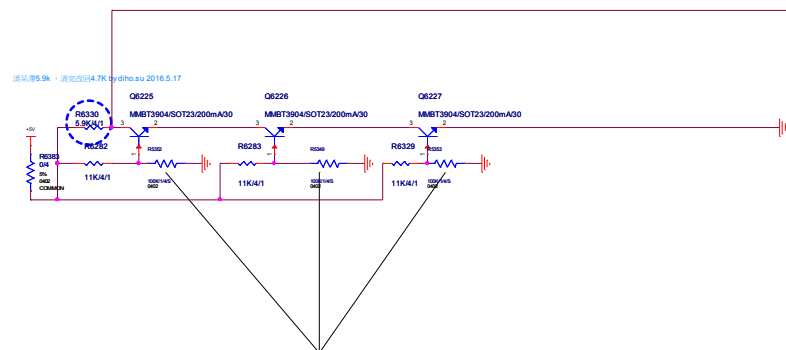


0	00A	0000000	
1	00B	0000000	1. Add GPIO1 for graphics VBIOS 2. update VBIOS driver sequence
2	00C	0000000	HDMI: - add register indicator R1380 - R1387 - add pull-down indicator L1380-L1387 - remove C405, C405, VR405, C410, C414, R405
3	00D	0000000	Modify USB Design





Add MOS VRHOT CIRCUIT



Near VDDC each high side MOS

